

NEC

Customer Notification

QB-V850ESX3H

In-Circuit-Emulator

Operating Precautions

Target Device

V850E/Sx3-H

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(A) Table of Operating Precautions

QB-V850ESX3H

Outline		Control Code			
		Rev.	A	B	C
No. 1	Access to addresses 0x100000 or higher in internal ROM or internal expansion RAM (Technical Limitation)		x	✓	✓
No. 2	Bus control function when using memory map 2 (Technical Limitation)		x	x	✓

✓: Not applicable

x: Applicable

(B) Description of Operating Precautions

No. 1	Access to addresses 0x100000 or higher in internal ROM or internal expansion RAM (Technical Limitation)
<p><u>Details:</u> If the below mentioned addresses shown in (1.) and (2.) shall be accessed via the external bus interface in multiplexed bus mode when the CPU operating frequency is 32 MHz or higher, an additional data wait cycle is required.</p> <p>Concerned address ranges: (1.) Addresses 0x100000 to 0x17FFFF of the memory space shared with internal ROM (2.) Internal expansion RAM space</p> <p><u>Workaround:</u> There is no workaround.</p> <p>To access the spaces shown above in (1.) and (2.), add one data wait cycle as shown below. (1.) Addresses 0x100000 to 0x17FFFF of the memory space shared with internal ROM: Set DWC0 to 0xnnn1 (n depends on the user program and specifies the wait cycles for CS1 to CS3 - not relevant here). (2.) Internal expansion RAM space Set DWC1 to 0x1777.</p> <p>This behaviour has been corrected in products with Control Code “B” of the emulator.</p>	

No. 2	Bus control function when using memory map 2 (Technical Limitation)
<p><u>Details:</u> The access to CS3 does not operate as expected when using memory map 2 by setting the Chip area select control register 0 (CSC0).</p> <p><u>Workaround:</u> There is no workaround.</p> <p>This behaviour has been corrected in products with Control Code “C” of the emulator.</p>	

(C) Valid Specification

Item	Date published	Document No.	Comment
1	October 2008	ZUD-CD-08-0190-1_E	QB-V850ESX3H (Preliminary User's Manual)

(D) Revision History

Item	Date published	Document No.	Comment
1	March 2009	U19726EE1V0IF00	Initial release
2	October 2009	U19726EE2V0IF00	Added Control Code "C", Item 2.

