RENESAS TECHNICAL UPDATE

TOYOSU FORESIA, 3-2-24, Toyosu, Koto-ku, Tokyo 135-0061, Japan
Renesas Electronics Corporation

Product Category	MPU/MCU		Document No.	TN-16C-A246A/E	Rev.	1.00
Title	Precaution of the transition to WAIT Mode from Low Power Dissipation Mode		Information Category	Technical Notification		
Applicable Product	M16C/26A(M16C/26A, 26B, 26T) Group M16C/28 Group M16C/29 Group Flash Memory products of above.	Lot No. –	Reference Document	User's Manual of target products		

A problem of the applied products is explained in the below.

1-1. Applied Products

M16C/26A (M16C/26A, 26B, 26T) Group, M16C/28 Group, M16C/29 Group

Flash Memory products of above.

1-2. Precaution

It is possible that MCU illegal operation occurs when WAIT instruction and interrupt requests for return occur

simultaneously, in case the MCU makes the transition to WAIT Mode from Low Power Dissipation Mode[1].

Note 1. In this mode, the main clock is turned off after being placed in low speed mode. The sub clock provides the CPU

clock.

1-3. Workaround for software

When using WAIT mode, please make the transition from modes [1] except Low Power Dissipation Mode to WAIT mode.

Note 1. High-speed Mode, PLL Operation Mode, Medium-speed Mode, Low-speed Mode, On-chip Oscillator mode,

On-chip Oscillator Low Power Dissipation Mode

Please contact Renesas sales division.