

RENESAS TECHNICAL UPDATE

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Product Category	MPU/MCU	Document No.	TN-V85-A036A/E	Rev.	1.00
Title	Precaution of external RESET during Data-Flash programming operation		Information Category	Technical Notification	
Applicable Product	V850E2/Px4-1M (uPD70F3508, uPD70F3509)	Lot No.	Reference Document	V850E2/Px4 User's Manual :Hardware Rev.1.03	
		All lots			

The precaution of external RESET during Data-Flash programming operation is shown below.

[Precaution]

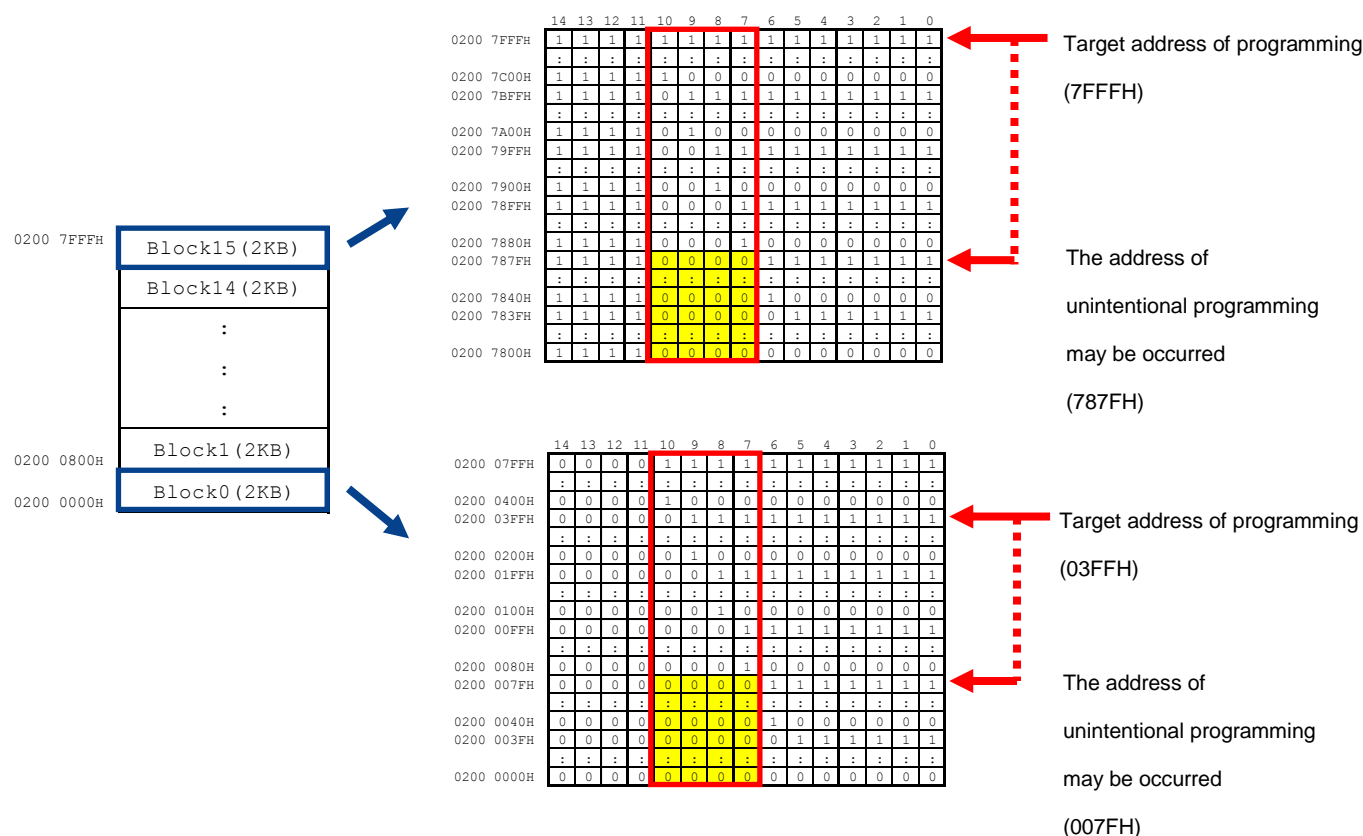
When an external RESET is activated during Data-Flash programming operation, Unintentional programming may be occurred at the address where the upper 4 bit of the target address of programming AD[10-7]=0000b.

[Example]

Unintentional programming may be occurred at

AD[14-0] = 111_1000_0111_1111b during programming operation at AD[14-0] = 111_1111_1111_1111b.

AD[14-0] = 000_0000_0111_1111b during programming operation at AD[14-0] = 000_0011_1111_1111b.



When an external RESET is activated during Data-Flash programming operation, take measures such as re-programming if necessary.