

Exclusively for design purposes! No part of this may be disclosed to a third party without the consent of NEC Electronics

Concerned Products: μPD75F4264	Customer Notification		Date: February 21, 2000
			NEC-Electronics (Europe) GmbH EAD –Technical Product Support
	Bug Report		Source Doc: SBG-T-1890
			Author: K. Liebig
	Feb. 21, 2000	Doc. No.:	TPS-LE-B-75F4264-1
1 st revision	:	Doc. No.:	

Exclusively for design purposes! No part of this may be disclosed to a third party without the consent of NEC Electronics

(A) BUG LIST

Bug No.	Outline	μPD75F4264
1	Handling of internal pull up resistor at port 8	● [✖]

- ✓: No problem
- ✋: Bug (will be corrected by next version upgrade)
- [✖]: Bug (restriction, not corrected by version upgrade)

Exclusively for design purposes! No part of this may be disclosed to a third party without the consent of NEC Electronics

(B) BUG DESCRIPTION

Bug No.	Outline	Description
1	Handling of internal pull up resistor at port 8	<p>Detail: When an internal pull up resistor is enabled, the pull up resistor is switched off automatically when the port is set to output mode.</p> <p>Not so at port 8 in 75F4264, the pull up resistor is NOT disabled when port 8 is set to output.</p> <p>Remedy: Set bit 0 (PO8) of POGB (pull-up resistor specification register group B) to "0" (pull up disabled) when the pull up at port 8 is not used.</p>