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<b>Concerned Products:</b>  <i>IE-784046-NS-EM1</i>  <i>I/O-Emulation board for the <math>\mu</math>PD78404x and <math>\mu</math>PD784054 devices</i>	<b>Customer Notification</b>		Date: 8 February, 2002
	<b>Customer Notification</b> <b>IE-784046-NS-EM1</b>		NEC-Electronics (Europe) GmbH LSI & Discrete Business Unit Technical Product Support
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1 <sup>st</sup> revision	:		

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**(A) LIST**

No.	Outline	IE-784046-NS-EM1									
		Rev./ Control code <small>Note1</small>	A								
1	Initial value of the standby control register (STBC)		☛								

- ✓ : No problem
- ☞ : Will be corrected by version upgrade.
- ☛ : Permanent restriction, not corrected by version upgrade.

Note 1 : The (control) code of the version information is the second character of the serial number.

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**(B) DESCRIPTION**

No.	Outline	Description
1	Initial value of the standby control register (STBC)	<p><u>Details</u></p> <p>The initial value of the standby control register (STBC) is 0x30. When using the emulator, however, fix bits 2 to 7 of the standby control register (STBC) to 0; otherwise the emulator will not work correctly. There is no problem regarding the operation even if a value such as 000000xxB is written to the actual CPU. (The CPU reads the value as 001100xxB).</p> <p>*Note: x is equal to 0 or 1, B: binary code</p> <p><u>Workaround</u></p> <p>There is no workaround.</p>

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**(C) Revision history**

<b>Rev.- No.</b>	<b>Outline</b>	<b>Description</b>
1.	Initial issue of this document	1. Initial issue of this document.