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Concerned Products:	Customer Notification	Date: May 25, 1999
IE-789046-NS-EM1	Bug Report	NEC-Electronics (Europe) GmbH EAD -Technical Product Support
		Source Doc: SBG-T-1592
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May 25th, 99		Doc. No.: TPS-LE-B-ST04

(A) BUG LIST

Bug No.	Outline	IE-789046-NS-EM1
		V1.A DS1.0 Control Code: A
1	8-bit timer / interval timer restriction	

- ✓: No problem
- : Bug (will be corrected by next version upgrade)
- : Bug (restriction, not corrected by version upgrade)

(B) BUG DESCRIPTION

Bug No.	Outline	Description
1	8-bit timer restriction	Detail: When using these timers, rewrite the value of the compare register (CRxx) in a state where the timer operation is inhibited. Rewriting the value of the compare register (CRxx) in a state where the timer operation is permitted may generate coincidence signals immediately. (In the case that interrupts are permitted, interrupt requests will occur.)