

To our customers,

Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: <http://www.renesas.com>

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

Send any inquiries to <http://www.renesas.com/inquiry>.

RENESAS TECHNICAL UPDATE

Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan
RenesasTechnology Corp.

Product Category	MPU&MCU		Document No.	TN-SH7-A562A/E	Rev.	1.00
Title	Notice for using PCMCIA I/O card interface		Information Category	Technical Notification		
Applicable Product	R5S72060W200FPV	Lot No.	Reference Document	SH7206 Group Hardware Manual (Mar.18.05 REJ09B0191-0100 Rev.1.00)		
	R5E72060W200FPV	ALL				

There is the notice for using PCMCIA I/O card interface.

1.Content

There is a case that read/write access is invalid even though using 16bit data bus width for PCMCIA I/O card interface.

2.Conditions

The above invalid access occurs when the following all 2 conditions are satisfied. There is no relation with input level from IOIS16 pin.

- (1) Using 16bit data bus width for PCMCIA I/O card interface
(CSnBCR.TYPE[2:0]=B'101, CSnBCR.BSZ[1:0]=B'10 and CSnWCR.SA[1:0] is not B'00)
- (2) Number of delay cycle from address output to RD/WE assertion is not 0.5 cycle (CSnWCR.TED[3:0] is not B'0000)

3.Notice

Notice for the above invalid access is shown below.

- Using except PCMCIA I/O card interface
- In using PCMCIA I/O card interface, set 8bit data bus width
- In using PCMCIA I/O card interface with 16bit data bus width, number of delay cycle from address output to RD/WE assertion is set 0.5 cycle (CSnWCR.TED[3:0] is not B'0000)