To our customers,

Old Company Name in Catalogs and Other Documents

   On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: http://www.renesas.com

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

Send any inquiries to http://www.renesas.com/inquiry.

RENESAS

RENESAS TECHNICAL UPDATE

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Product Category	MPU&MCU		Document No.	TN-SH7-A560A/E	Rev.	1.00
Title	Notice for using Burst ROM interface (Error correction and additional information for TN-SH7-A553A/E)		Information Category	Technical Notification		
Applicable Product	R5S72060W200FPV R5E72060W200FPV	Lot No.	Reference Document	· SH7206 Group Hardware Manual (Mar.18.05 REJ09B0191-0100 Rev.1.00) · TN-SH7-A553A/E		
		ALL				

Regarding the above SH2A products, technical update TN-SH7-A553A/E was previously released with an erroneous information.

This technical update presents error correction and additional information for TN-SH7-A553A/E.

1. Error correction for TN-SH7-A553A/E

Error:

  3. Notice

  2) When there is DMA write access for the Burst ROM interface (clocked asynchronous),

   Please use Byte/word/Long-Word unit access or set LSB 4 bits of the transfer start address to H0 or H08

   in 16-byte unit access.

Correct:

  3. Notice

  2) When there is DMA write access for the Burst ROM interface (clocked asynchronous),

   Please use Byte/word/Long-Word unit access. Do not use 16-byte unit access.

2. Additional Information for TN-SH7-A553A/E

 When CS0 or CS4 is selected for Burst ROM interface (clocked asynchronous) with 8 bit data bus width, for access as 16 burst

 X1 operation, BST[1:0] of CS0WCR or CS4WCR must be set as 2'b00. Setting BST[1:0] to 2'b10 is not allowed.