

To our customers,

Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: <http://www.renesas.com>

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

Send any inquiries to <http://www.renesas.com/inquiry>.

RENESAS TECHNICAL UPDATE

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Product Category	MPU&MCU	Document No.	TN-SH7-A563A/E	Rev.	1.00
Title	Notice for Timer Status Register (TSR) in Multi-Function Timer Pulse Unit 2 (MTU2) and Multi-Function Timer Pulse Unit 2S (MTU2S)		Information Category	Technical Notification	
Applicable Product	R5S72060W200FPV	Lot No.	Reference Document	SH7206 Group Hardware Manual (Mar.18.05 REJ09B0191-0100 Rev.1.00)	
	R5E72060W200FPV	ALL			

There is a notice for Timer Status Register(TSR0~4) in Multi-Function Timer Pulse Unit 2 (MTU2) and Timer Status Register (TSR3~4) in Multi-Function Timer Pulse Unit 2S (MTU2S).

1.Content

When flag bit of Timer Status Register (TSR) is set at the next cycle of read access for it, the flag bit is illegally cleared by 0 write access and interrupt routine is not executed.

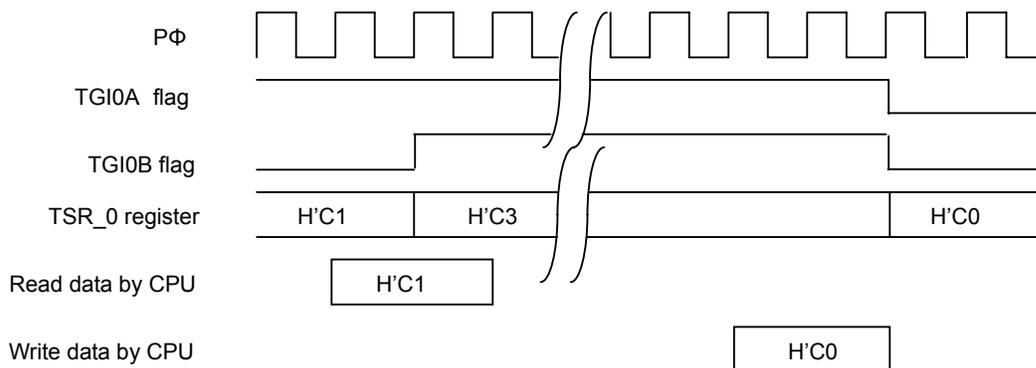


Figure 1 example of illegal flag clear activation about TGI0B

2.Notice

Notice for the above illegal operation is shown below.

- In writing Timer Status Register (TSR), please do 0 write access for the flag bit to be cleared and 1 write access for other flag bits. In this case, 1 write access is not executed and previous value is held.