To our customers,

Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: http://www.renesas.com

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)
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# RENESAS TECHNICAL UPDATE

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<th>Classification of Production</th>
<th>MPU&amp;MCU</th>
<th>No</th>
<th>TN-SH7-489A/E</th>
<th>Rev</th>
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<td>THEME</td>
<td>A notice on the 16-bit Timer Pulse Unit (TPU) – an errata on the hardware manual</td>
<td>Classification of Information</td>
<td>Spec change</td>
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<td>Limitation of Use</td>
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<td>PRODUCT NAME</td>
<td>HD6417705</td>
<td>Lot No.</td>
<td>Reference Documents</td>
<td>SH7705 Group Hardware Manual Rev. 2.00</td>
<td>REJ09B0082-02000</td>
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When falling edge of count clock is selected by the clock edge bits, CKEG1,0=01 in the timer control registers (TCR), the input clock $P\Phi/1$ cannot be selected. If it is selected, the timer does not work properly.

The section 14.3.1 Timer Control Registers (TCR), bit4,3 Clock Edge,

Errata:

00: Count at rising edge  
01: Count at falling edge  
1x: Count at both edges *  

[Legend] X: Don’t care  

Note: *Internal clock-edge selection is valid when the input clock $P\Phi/4$ or slower.  
If the input clock is $P\Phi/1$, this operation is not performed.

Correction:

00: Count at rising edge  
01: Count at falling edge *  
1x: Count at both edges *  

[Legend] X: Don’t care  

Note: *Internal clock-edge selection is valid when the input clock $P\Phi/4$ or slower.  
If the input clock is $P\Phi/1$, this operation is not performed.