RENESAS TECHNICAL UPDATE

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Product Category	MPU/MCU		Document No.	TN-R8C-A018B/E	Rev.	2.00
Title	Notes on R8C Family (3)		Information Category	Technical Notification		
Applicable Product	R8C/3x Series R8C/Lx Series R8C/Mx Series	Lot No. –	Reference Document			

This technical update is a revised version of TN-R8C-A018A/E (issued on June 28, 2011). The R8C/Lx Series and R8C/Mx Series have been included in applicable products in 4 below.

The following notes have been added to the hardware user's manual. The functions described below do not exist in some MCUs. Refer to the hardware user's manual for applicable products.

1. Note on UART2

    Applicable products: R8C/3x Series, R8C/Lx Series

    Use the MOV instruction to write to the UART2 transmit buffer register (U2TB). When the MP bit in the U2SMR5 register is 0 (multiprocessor communication disabled) and the transfer data length is 9 bits, write in 16-bit units or write to the higher byte first and then the lower byte in 8-bit units.

2. Note on DTC chain transfer

    Applicable products: R8C/3x Series, R8C/Lx Series

    When performing chain transfers using several control data, the number of transfers set to the first control data is enabled and the number of transfers proceeded after the first control data is disabled.

    Examples: When DTCCT0 = 5 and DTCCT1 = 10, chain transfers are performed as DTCCT0 = DTCCT1 = 5.

    When DTCCT0 = 10 and DTCCT1 = 5, chain transfers are performed as DTCCT0 = DTCCT1 = 10.

    When DTCCT0 = 10, DTCCT1 = 5, and DTCCT2 = 2, chain transfers are performed as DTCCT0 = DTCCT1 = DTCCT2 = 10.

3. Note on wait mode

    Applicable products: R8C/3x Series, R8C/Lx Series

    When setting bits CM37 and CM36 to values other than 00b to enter wait mode from low-speed clock mode, set the XCIN clock frequency to 28 kHz or more.

    When setting bits CM37 and CM36 to values other than 00b to enter wait mode from high-speed clock mode, set the XIN clock frequency to 28 kHz or more.

4.　Note on wait mode and stop mode

　　Applicable products: R8C/35A Group, R8C/35C Group, R8C/35M Group, R8C/36A Group, R8C/36C Group, R8C/36M Group,

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　　　　　　　　　　　　Series

　　When entering wait mode or stop mode from low-speed clock mode using the clock generated externally as the XCIN clock,

　　do not stop the clock generated externally while in wait mode or stop mode.