RENESAS TECHNICAL UPDATE

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Product Category	MPU/MCU		Document No.	TN-SH7-A907A/E	Rev.	1.00
Title	Notes about reading from FLDTFIFO of NAND flash memory controller		Information Category	Technical Notification		
Applicable Product	See following	Lot No.	Reference Document	See following		
		All				

In the NAND flash memory controller of the following applicable products, there are notes about reading from data FIFO register (FLDTFIFO).

[1] Notes

If FIFO data are read when FLDTFIFO becomes full, read data might be invalid.

[2] Workaround

When reading data from NAND flash memory, read with following settings.

- In the case of command access mode

  Specify the Data Count Specification bits (DTCNT[11:0]) of Data Counter Register (FLDTCNTR) to 128 bytes or less.

  *Notes: Don't use the sector access mode to read data.

Applicable products and relevant documents

Applicable products		Relevant documents	Rev.	Document number
Series	Group			
SH7260	SH7262 SH7264	SH7262 Group, SH7264 Group User's Manual: Hardware	4.00	R01UH0134EJ0400
	SH7266 SH7267	SH7266 Group, SH7267 Group User's Manual: Hardware	2.00	R01UH0412EJ0200
	SH7268 SH7269	SH7268 Group, SH7269 Group User's Manual: Hardware	2.00	R01UH0048EJ0200

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