

RENESAS TECHNICAL UPDATE

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Product Category	MPU/MCU		Document No.	TN-RZ*-A042A/E	Rev.	1.00
Title	Notes about CLUT table of Video Display Controller 4 and 5		Information Category	Technical Notification		
Applicable Product	See following	Lot No.	Reference Document	See following		
		All				

In the following applicable products, a bug about CLUT table of Video Display Controller 4 and 5 is found.

The detail of bug is shown in bellow. According to this update, relevant manuals will be revised.

Applicable products and relevant documents

Applicable products		Relevant documents	Rev.	Document number
series	Group			
SH7260	SH7268, SH7269	SH7268 Group, SH7269 Group User's Manual: Hardware	Rev.3.00	R01UH0048EJ0300
RZ/A	RZ/A1H, RZ/A1M	RZ/A1H Group, RZ/A1M Group User's Manual: Hardware	Rev.3.00	R01UH0403EJ0300
	RZ/A1L, RZ/A1LU, RZ/A1LC	RZ/A1L Group, RZ/A1LU Group, RZ/A1LC Group User's Manual: Hardware	Rev.3.00	R01UH0437EJ0300

1. Error occurrence condition

When CLUT table of Video Display Controller 4 and 5 is switched by GR_CLT_SEL, the last access to the CLUT table of CPU side is write access.

2. Error phenomenon

When CLUT table of Video Display Controller 4 and 5 is switched by GR_CLT_SEL, the CLUT value of CPU side before switching might be changed.

3. Workarounds

After the last write access to the CLUT table, please execute a dummy read of the CLUT table address. And switch the CLUT table by GR_CLT_SEL after dummy-read.

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