To our customers,

Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: http://www.renesas.com

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Renesas Electronics Corporation

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RENESAS TECHNICAL UPDATE

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Product Category	MPU&MCU		Document No.	TN-H8*-A377A/E	Rev.	1.00
Title	Note regarding usage of port pins P66 and P67		Information Category	Technical Notification		
Applicable Product	H8SX/1648, H8SX/1644, H8SX/1642	Lot No.	Reference Document	H8SX/1648 Group Hardware Manual (REJ09B0365-0100)		
		All lots				

We would like to inform you of a point regarding usage of port pins P66 and P67 of H8SX/1648 Group products. Please take this information into consideration when using this product.

Port pins P66 and P67 can be used as I/O port pins; pin P67 can also be used as an IRQ input. The pin functions of these ports are switched by the settings of the P66DDR and P67DDR bits. Note that, however, the settings of the SCK6 (P65) and TxD6 (P63) outputs of SCI_6 affect switching of the pin function. The settings of TxD6 and SCK6 affect port pins P66 and P67, respectively.
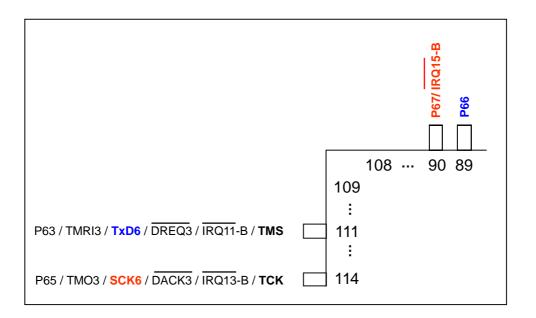

Figure 1. Pin Assignments

When port pin P65 is set as the SCK6 output (SCK6_OE = 1), port pin P67 functions as an output pin of the I/O port.

When port pin P63 is set as the TxD6 output (TxD6_OE = 1), port pin P66 functions as an output pin of the I/O port.

Tables 2.1 and 2.2 show the description in the H8SX/1648 Group Hardware Manual, Rev. 1.0 (page 406).

Tables 3.1 and 3.2 show the specifications that reflect this note regarding usage of port pins P66 and P67.

Table 2.1   Specification for port pin P67

[Before]

Module Name	Pin Function	Setting
		I/O port
		P67DDR
I/O port	P67 output	1
	P67 input (Initial setting) IRQ15-B input	0

Table 3.1   Specification for port pin P67

[After]

Module Name	Pin Function	Setting	
		I/O port	SCI_6
		P67DDR	SCK6_OE
I/O port	P67 output	1	-
		0	1
	P67 input (Initial setting) IRQ15-B input	0	0

Table 2.2   Specification for port pin P66

[Before]

Module Name	Pin Function	Setting
		I/O port
		P66DDR
I/O port	P66 output	1
	P66 input (Initial setting)	0

Table 3.2 Specification for port pin P66

[After]

Module Name	Pin Function	Setting	
		I/O port	SCI_6
		P66DDR	TXD6_OE
I/O port	P66 output	1	-
		0	1
	P66 input (Initial setting)	0	0

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