RENESAS TECHNICAL UPDATE

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Product Category	MPU/MCU			Document No.	TN-V85-A011A/E	Rev.	1.00
Title	The matter about the level of output pin in System Reset.			Information Category	Technical Notification		
Applicable Product	Refer to a body.	Lot No.		Reference Document	User Manual of each product		
		All lots					

The following contents are added to the User Manual.

1. Additional contents

   In the User Manual, it is mentioned that pin status is Hi-Z during and after reset. However the output pin has a possibility to become unstable at the transition moment (less than 10ns) of external and internal reset. After that, pin status will be Hi-Z.

   Note. The phenomenon of unstable output period and influence of the pin level after the unstable situation, it depend on customer board and measurement conditions.

2. Target Product

Target Product Series		
V850E2/Fx4-H　Series	V850E2/Fx4　Series	V850E2/FF4-M
V850E2/Px4　Series	V850E2/Dx4-H　Series	V850E2/Dx4　Series
V850E2/Fx4-L　Series	V850E2/Fx4-G　Series	V850E2/Px4-L
V850E2/PG4-S	V850ES/Fx3　Series	V850ES/Fx3-K　Series
V850ES/Fx3-N　Series	V850ES/Fx3-L　Series	V850E/Dx3　Series
V850E/Rx3　Series	V850ES/Fx2　Series	V850ES/SL2　Series
V850ES/Dx2　Series	V850ES/GB1　Series	V850ES/PM1
V850E/FK3-H	V850E/Rx2　Series	V850E/Rx1　Series
V850E/PHO3　Series	V850E/PHJ1	V850E/PH4
V850/PHC3	V850/Sx1　Series	V850/SBx　Series
V850/CAx　Series	V850/SCx　Series	V850/CGx　Series
CARGATE	CARGATE-A	CARGATE+
VIVIAN	V853A	―

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