

# RENESAS TECHNICAL UPDATE

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|                    |   |          |                      |                             |      |      |
|--------------------|---|----------|----------------------|-----------------------------|------|------|
| Product Category   | MPU/MCU   |          | Document No.         | TN-V85-A019A/E              | Rev. | 1.00 |
| Title              | The matter about the level of output pin in System Reset. |          | Information Category | Technical Notification      |      |      |
| Applicable Product | Refer to a body.  | Lot No.  | Reference Document   | User Manual of each product |      |      |
|                    |   | All lots |                      |                             |      |      |

The following contents are added to the User Manual.

## 1. Additional contents

In the User Manual, it is mentioned that pin status is Hi-Z during and after reset. However the output pin has a possibility to become unstable at the transition moment (less than 10ns) of external and internal reset. After that, pin status will be Hi-Z.

Note. The phenomenon of unstable output period and influence of the pin level after the unstable situation, it depend on customer board and measurement conditions.

## 2. Target Product

| Target Product Series |                   |                    |
|-----------------------|-------------------|--------------------|
| V850ES/SG1 Series     | V850ES/SG2 Series | V850ES/SJ2 Series  |
| V850ES/SG3 Series     | V850ES/SJ3 Series | V850E/Sx3-H Series |
| V850E2/Sx4-H Series   | —                 | —                  |