

To our customers,

Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

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HITACHI SEMICONDUCTOR TECHNICAL UPDATE

Classification of Production	MPU		No	TN-SH7-462A/E	Rev	1
THEME	Manual correction about the control register of SCIF of SH7750		Classification of Information	1. Spec change ② Supplement of Documents 3. Limitation of Use 4. Change of Mask 5. Change of Production Line		
PRODUCT NAME	SH7750	Lot No.	Reference Documents	SH7750Series Hardware Manual (ADE-602-124E) Rev.6.0	Effective Date	
		All			Eternal	

It corrects about the Transmit FIFO Data Number Trigger (TTRG1 and 0) of the Serial Communication Interface with FIFO (SCIF) of SH7750. However, regarding SH7750S and SH7750R, it is as a manual written. Moreover, please check the number of non-transmitted data stored in the Transmit FIFO Data Register (SCFTDR2) by the FIFO Data Count Register (SCFDR2).

In the case of SH7750 (the contents after manual correction)

Bit 5 and 4: Transmit FIFO Data Number Trigger (TTRG1 and 0)

These bits are used to set the number of remaining transmit data bytes the sets the transmit FIFO data register empty (TDFE) flag in the serial status register (SCFSR2). The TDFE flag is set when the number of transmit data bytes in SCFTDR2 is equal to or less than the trigger set number shown in the following table.

Bit 5: TTRG1	Bit 4: TTRG0	Transmit Trigger Number
0	0	<u>7 (9)</u> (Initial value)
	1	<u>3 (13)</u>
1	0	<u>1 (15)</u>
	1	<u>0 (16)</u>

Note: Figures in parentheses are the number of empty bytes in SCFTDR2 when the flag is set.

In the case of SH7750S and SH7750R (the same contents as a manual)

Bit 5 and 4: Transmit FIFO Data Number Trigger (TTRG1 and 0)

These bits are used to set the number of remaining transmit data bytes the sets the transmit FIFO data register empty (TDFE) flag in the serial status register (SCFSR2). The TDFE flag is set when the number of transmit data bytes in SCFTDR2 is equal to or less than the trigger set number shown in the following table.

Bit 5: TTRG1	Bit 4: TTRG0	Transmit Trigger Number	
0	0	<u>8 (8)</u>	(Initial value)
	1	<u>4 (12)</u>	
1	0	<u>2 (14)</u>	
	1	<u>1 (15)</u>	

Note: Figures in parentheses are the number of empty bytes in SCFTDR2 when the flag is set.