To our customers,

Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: http://www.renesas.com

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

Send any inquiries to http://www.renesas.com/inquiry.

Date: Jul.05.2005

RENESAS TECHNICAL UPDATE

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RenesasTechnology Corp.

Product Category	MPU&MCU		Document No.	TN-16C-A143A/E	Rev.	1.00
Title	M16C/Tiny Series Document Revision for CRC Snoop Function in the CRC Calculation Circuit		Information Category	Technical Notification		
Applicable Product	M16C/26A Group (M16C/26A, M16C/26T), M16C/29 Group	Lot No. All	Reference Document			

The description of CRCSAR register (CRC snoop address register) has been revised as follows.

When using the subject documents, please pay attention to the changes.

1. Subject Documents

M16C/26A Group (M16C/26A, M16C/26T) Hardware Manual Rev.1.00

M16C/29 Group Hardware Manual Rev.1.00

2. Correction

M16C26A Group(M16C26A, M16C/26T) Hardware Manual Rev. 1.00

P209 (Figure 15.2)

M16C/29 Group Hardware Manual Rev.1.00

P307 (Figure 18.2)

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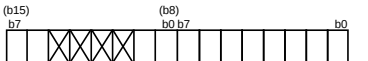
[Before revision]

CRC snoop address register



Symbol	Address	After reset
CRCSAR	03B5$_{16}$ to 03B4$_{16}$	00XXXX?? ????????$_{16}$

Correction

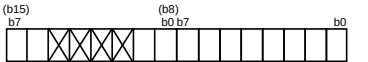
Bit symbol	Bit name	Function	RW
CRCSAR9-0	CRC mode polynomial selection bit	SFR address to snoop	RW
(b13-b10)	Nothing is assigned. Write "0" when writing to this bit. The value is indeterminate if read.		—
CRCSR	CRC Snoop on read enable bit	0: Disabled 1: Enabled	RW
CRCSW	CRC Snoop on write enable bit	0: Disabled 1: Enabled	RW

Correction

Note 1 added

[After revision]

CRC snoop address register



Symbol	Address	After reset
CRCSAR	03B5$_{16}$ to 03B4$_{16}$	00XXXXX XXXXXXXX$_2$

Bit symbol	Bit name	Function	RW
CRCSAR9-0	CRC mode polynomial selection bit	SFR address to snoop	RW
(b13-b10)	Nothing is assigned. Write "0" when writing to this bit. The value is indeterminate if read.		—
CRCSR	CRC Snoop on read enable bit	0: Disabled 1: Enabled(1)	RW
CRCSW	CRC Snoop on write enable bit	0: Disabled 1: Enabled(1)	RW

NOTES:
1. Set the CRCSR bit and CRCSW bit to "0" if the PLC07 bit in the PLC0 register is set to "1" (PLL on) and the PM20 bit in the PM2 register is set to "0" (SFR access 2 wait).