Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

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RENESAS TECHNICAL NEWS

No.M16C-100-0309

M16C/62P Group

Precautions when using UART0 or UART1 as a slave in I²C mode

Classification

Corrections and supplementary explanation of document

√ Notes Knowhow

Others

Concerned Products

M16C/62P Group

1. Precautionary Note

If UARTi is used as a slave in the I²C mode after the SMD2 to SMD0 bits in the UiMR (i=0,1) register are set to "010b" (I²C mode) and the CKDIR bit is set to "1" (external clock), P61 or P65 are placed in a high-impedance state. P61 or P65 cannot be used as an output port even if the PD6_1 or PD6_5 bits in the PD6 register are set to "1" (output mode).

Using UART0 as a slave in I²C mode

Despite setting the PD6_1 bit to "1" (output mode), P61 is placed in a high-impedance state and cannot be utilized as an output port. P61 can be used as an input port when the PD6_1 bit is set to "0" (input mode).

If the CKDIR bit is set to "0" (internal clock) and UARTi is used as a master in I²C mode, P61 can be used as input and output port depending on a PD6_1 bit setting

Using UART1 as a slave in I²C mode

Despite setting the PD6_5 bit to "1" (output mode), P65 is placed in a high-impedance state and cannot be utilized as an output port. P65 can be used as an input port when the PD6_5 bit is set to "0" (input mode).

If the CKDIR bit is set to "0" (internal clock) and UARTI is used as a master in I²C mode, P65 can be used as input and output port depending on a PD6_5 bit setting.

Table 1 lists port direction bit settings and pin state in I²C mode.

Table 1. Port Direction Bit Settings and Pin State in I²C mode

	Slave in I ² C Mode		Master in I ² C Mode	
Ports	Port Direction Bit = "1" (Output Mode)	Port Direction Bit = "0" (Input Mode)	Port Direction Bit = "1" (Output Mode)	Port Direction Bit = "0" (Input Mode)
P61	High-Impedance*	Input Port	Output Port	Input Port
P65	High-Impedance*	Input Port	Output Port	Input Port
P72	Output Port	Input Port	Output Port	Input Port

^{*}Cannot be used as an output port.

2. Countermeasure

If using UART0 or UART1 as a slave in I²C mode, set the PD6_1 or PD6_5 bits to "0" (Input mode).

Set the PD6_1 or PD6_5 bits to "0" (input mode) and connect each port to VSS via a resistor when P61 or P65 are not in use.