

RENESAS TECHNICAL UPDATE

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Product Category	MPU/MCU		Document No.	TN-RX*-A0217A/E	Rev.	1.00
Title	Improvement of Oscillation Frequency Accuracy for Hi-speed On-chip Oscillator (HOCO) on RX130 Group, RX230 Group, and RX231 Group		Information Category	Technical Notification		
Applicable Product	RX130 Group RX230 Group, RX231 Group,	Lot No. All	Reference Document	RX130 Group User's Manual: Hardware Rev.3.00 (R01UH0560EJ0300) RX230 Group, RX231 Group User's Manual: Hardware Rev.1.20 (R01UH0496EJ0120)		

This document describes an improvement of the accuracy for the HOCO oscillation frequency in the “Electrical Characteristics” section of the User’s Manual: Hardware for the applicable products stated above.

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Descriptions of the test conditions and presentation of the HOCO specifications in Table 40.25, Clock Timing are modified as follows.

Before modification

Table 40.25 Clock Timing

Conditions: $1.8\text{ V} \leq VCC = AVCC0 < 2.0\text{ V}$, $2.0\text{ V} \leq VCC \leq 5.5\text{ V}$, $2.0\text{ V} \leq AVCC0 \leq 5.5\text{ V}$, $VSS = AVSS0 = 0\text{ V}$, $T_a = -40\text{ to }+105^\circ\text{C}$

Item	Symbol	Min	Typ	Max	Unit	Test Condition
(Omitted)						
HOCO clock oscillation frequency	f_{HOCO} (32MHz)	31.52	32	32.48	MHz	$T_a = -40\text{ to }+85^\circ\text{C}$
		31.68	32	32.32		$T_a = 0\text{ to }+55^\circ\text{C}$
		31.36	32	32.64		$T_a = -40\text{ to }+105^\circ\text{C}$
(Omitted)						

After modification

Table 40.25 Clock Timing

Conditions: $1.8\text{ V} \leq VCC \leq 5.5\text{ V}$, $AVCC0 = VCC$ ($VCC < 2.0\text{V}$), $1.8\text{ V} \leq AVCC0 \leq 5.5\text{ V}$ ($VCC \geq 2.0\text{V}$), $VSS = AVSS0 = 0\text{ V}$, $T_a = -40\text{ to }+105^\circ\text{C}$

Item	Symbol	Min	Typ	Max	Unit	Test Condition
(Omitted)						
HOCO oscillation frequency	f_{HOCO}	—	32	—	MHz	
HOCO oscillation frequency error	Δf_{HOCO}	—	—	± 1.5	%	$T_a = -40\text{ to }-20^\circ\text{C}$
		—	—	± 1.0		$T_a = -20\text{ to }+85^\circ\text{C}$
		—	—	± 2.0		$T_a = +85\text{ to }+105^\circ\text{C}$
(Omitted)						

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Descriptions of the test conditions and presentation of the HOCO specifications in Table 50.26, Clock Timing are modified as follows.

Before modification

Table 50.26 Clock Timing

Conditions: $1.8\text{ V} \leq VCC = VCC_USB = AVCC0 \leq 5.5\text{ V}$, $VSS = AVSS0 = VREFL0 = VSS_USB = 0\text{ V}$, $T_a = -40\text{ to }+105^\circ\text{C}$

Item	Symbol	Min	Typ	Max	Unit	Test Condition
(Omitted)						
HOCO clock oscillation frequency	f _{HOCO} (32MHz)	31.52	32	32.48	MHz	T _a = -40 to +85°C
		31.68	32	32.32		T _a = 0 to +55°C
		31.36	32	32.64		T _a = -40 to +105°C
	f _{HOCO} (54MHz)	53.19	54	54.81	MHz	T _a = -40 to +85°C
		53.46	54	54.54		T _a = 0 to +55°C
		52.92	54	55.08		T _a = -40 to +105°C
(Omitted)						

After modification

Table 50.26 Clock Timing

Conditions: $1.8\text{ V} \leq VCC = VCC_USB = AVCC0 \leq 5.5\text{ V}$, $VSS = AVSS0 = VREFL0 = VSS_USB = 0\text{ V}$, $T_a = -40\text{ to }+105^\circ\text{C}$

Item	Symbol	Min	Typ	Max	Unit	Test Condition
(Omitted)						
HOCO oscillation frequency	32-MHz setting	f _{HOCO}	—	32	MHz	
	54-MHz setting		—	54		
HOCO oscillation frequency error		Δf _{HOCO}	—	—	±1.5	T _a = -40 to -20°C
			—	—	±1.0	T _a = -20 to +85°C
			—	—	±2.0	T _a = +85 to +105°C
(Omitted)						