SUD-T-4666-6-E (1/4)

CUSTOMER NOTIFICATION

SUD-T-4666-6-E

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CP(K)

IE-78K4-NS (Control Code: K) Operating Precautions

Be sure to read this document before using the product.

1. Product History

2. Restrictions

Notes on Using IE-78K4-NS

1. Product History

	Dauct History	Control Code Note											
No.	Bugs and Changes/Additions to Specification			С	D	E	F	G	Н	J	Κ		
1	Event Bit trace bug The message "NO EVENT BIT" is displayed on the trace.	\checkmark	-	-	-		-	-	-	-	-		
2	Path count bug Breaks and event detection are not performed correctly at the specified path count.	\checkmark	_	_	_	_	_	_	_	_	_		
3	Incorrect frame trace bug Sometimes the wrong bus cycle is traced.	\checkmark	\checkmark	-	-	_	-	-	-	-	_		
4	Bug related to P64, 65 output Between turning the power on, and clicking OK on the Configuration dialog in the debugger, a high level continues to be output from P64 and P65.		\checkmark	\checkmark	\checkmark	\checkmark	_	_	_	_	_		
5	Products with memory (buffer RAM, etc.) on the EM1 board are not supported. The affected product is the IE-784976-NS-EM1.	\checkmark	\checkmark	\checkmark	\checkmark		\checkmark	-	-	-	-		
6	Interrupt may be enabled if one of the following instructions is executed after setting a DI instruction: MOV1 PSWH.n,CY; SET1 PSWH.n; CLR1 PSWH.n; NOT1 PSWH.n; BTCLR PSWH.n,\$addr; BFSET PSWH.n,\$addr; RETI; RETB; RETCS !addr16; RETCSB !addr16; POP PSW; POPU PSW Workaround: None.	V	V	V	V	V	V	V	_	_	_		
7	An illegal break may occur if an SFR is accessed in user program RUN. This probability exists for all EM boards and all SFRs. Workaround: None.	\checkmark	\checkmark	\checkmark	\checkmark	\checkmark	\checkmark	\checkmark	_	_	-		
8	The number of frames may rise during qualify trace. (The same trace frame is displayed twice.) Workaround: None.	\checkmark	\checkmark	\checkmark	\checkmark		\checkmark	\checkmark	-	-	-		
9	The flash self-mode of the debug function (software emulation) is not supported.	\checkmark	\checkmark	\checkmark	\checkmark	\checkmark	\checkmark	\checkmark	-	-	_		
10	Software break cannot be used. To use this function upgrade ID-78K4-NS to V2.30 or later (scheduled to be released in Aug. 2000).	\checkmark	\checkmark	\checkmark	\checkmark		\checkmark	\checkmark	\checkmark	_	-		
11	The trace search function, added when upgrading ID-78K4-NS to V 2.30 or later, retrieves byte data (8-bit) even if word data (16-bit) is specified.	\checkmark	\checkmark	\checkmark	\checkmark		\checkmark	\checkmark	\checkmark	_	-		
12	The trace result of step execution is not displayed if "NON BREAK" is executed, the trace is stopped using event, a forcible break is executed, and then step execution is used.	\checkmark		\checkmark	\checkmark		\checkmark	\checkmark	\checkmark	_	_		

Ne	Bugg and Charge		no to Sr	onification		Control Code Note										
INO.							С	D	E	F	G	Η	J	Κ		
No.	 (1) The SFR values displayed correct [Condition 1] and [Condition 1] and [Condition 1] and 8-bit SFR (We have a strangle: In the Address Address n (0FF10H) Address n+1 (0FF10H) (b) 16-bit SFR (R) [Phenomenon] The value at add n+1. The same a displayed correct [Condition 2]. (2) The SFR values displayed correct [Condition 2]. (a) When the 16-bit bit SFR (R or RA [Phenomenon] The value at add as "0". 	 a) When the 16-bit SFR (R or R/W) is at address n, and 8-bit SFR (W) is at address n+1 Example: In the case of D784928 Address Symbol R/W Bit length Address n CR00 R/W 16 (0FF10H) Address n+1 ECC0 R 8 (0FF11H) b) 16-bit SFR (R) [Phenomenon] The value at address n is displayed at address n+1. The same applies to subsequent SFR. c) The SFR values in the SFR window are not displayed correctly when the SFR is in [Condition 2]. [Condition 2] a) When the 16-bit SFR (W) is at address n, and 8-bit SFR (R or R/W) is at address n+1 [Phenomenon] The value at address n+1 is always displayed as "0". 					C √	1				H		<u>к</u>		
	 Reading/writing in the program Remedy for bug (1)] This bug will be corrected after upgrading IE-78K4- NS to control code J and ID78K4-NS to E1.11j or later. [Remedy for bug (2)] This bug will be corrected after upgrading ID78K4- 															
14	NS to E1.11j or later For details of ID78K representative respo It may not be possib instruction starting for program on the emu Workaround: None.	r. 4-NS (E1.1 onsible for c ole to correc rom an odd	1j), cont debugge ctly fetch address	act the NEC ers.	~	\checkmark	~	√	√					_		
							$\sqrt{1}$: Applicable, – : Not applicable									

 $\sqrt{1}$: Applicable, - : Not applicable

Note The "control code" is the second digit from the left in the 10-digit serial number in the warranty supplied with the in-circuit emulator you purchased (if it has not been upgraded). If the in-circuit emulator has been upgraded, a label indicating the new version is attached to the in-circuit emulator and the x in V-UP LEVEL x on this label indicates the control code.

The functions of products with control codes C and D are the same as the functions of products with control code E. The control code I does not exist.

2. Restrictions

- Flash self-mode related restrictions
 - The versions listed below support flash self-mode with the following restrictions.
 - IE-78K4-NS : Control code H

ID78K4-NS : E1.11h

- (1) Of the four execution events and four access events in flash self-mode, only one of each may be consumed. Consequently, users should release the above events on the debugger side and then set a maximum of three events each when switching to flash self-mode.
- (2) The system may enter restart processing once a break occurs in flash self-mode that is not the result of break settings. This will cause some of the time measurement results and trace data to become invalid.
- Request flags other than the first interrupt request flag will not be enabled if nesting interrupts are generated during non-realtime execution (1 instruction execution, Step, Next (excluding CALL statement), Slowmotion) with the debugger. As a result, there is a possibility of incorrect operation with programs that perform some type of processing by checking request flags.

Workaround: Execute two or more instructions in realtime (Go, Return, Go&Go, Come, CPU Reset&Go).