

CUSTOMER NOTIFICATION

SUD-T-4666-6-E
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CP(K)

# **IE-78K4-NS**

(Control Code: K)

## **Operating Precautions**

Be sure to read this document before using the product.

1. Product History
2. Restrictions



No.	Bugs and Changes/Additions to Specification	Control Code <sup>Note</sup>																					
		A	B	C	D	E	F	G	H	J	K												
13	<p>(1) The SFR values in the SFR window are not displayed correctly at the SFR that matches the [Condition 1] and onward.</p> <p>[Condition 1]  (a) When the 16-bit SFR (R or R/W) is at address n, and 8-bit SFR (W) is at address n+1  Example: In the case of D784928</p> <table border="1"> <thead> <tr> <th>Address</th> <th>Symbol</th> <th>R/W</th> <th>Bit length</th> </tr> </thead> <tbody> <tr> <td>Address n (0FF10H)</td> <td>CR00</td> <td>R/W</td> <td>16</td> </tr> <tr> <td>Address n+1 (0FF11H)</td> <td>ECC0</td> <td>R</td> <td>8</td> </tr> </tbody> </table> <p>(b) 16-bit SFR (R)  [Phenomenon]  The value at address n is displayed at address n+1. The same applies to subsequent SFR.</p> <p>(2) The SFR values in the SFR window are not displayed correctly when the SFR is in [Condition 2].</p> <p>[Condition 2]  (a) When the 16-bit SFR (W) is at address n, and 8-bit SFR (R or R/W) is at address n+1  [Phenomenon]  The value at address n+1 is always displayed as "0".</p> <p>The other SFR-related operations are performed normally:</p> <ul style="list-style-type: none"> <li>- Writing in the SFR window</li> <li>- Reading/writing in the program</li> <li>- SFR illegal access break</li> </ul> <p>[Remedy for bug (1)]  This bug will be corrected after upgrading IE-78K4-NS to control code J and ID78K4-NS to E1.11j or later.</p> <p>[Remedy for bug (2)]  This bug will be corrected after upgrading ID78K4-NS to E1.11j or later.  For details of ID78K4-NS (E1.11j), contact the NEC representative responsible for debuggers.</p>	Address	Symbol	R/W	Bit length	Address n (0FF10H)	CR00	R/W	16	Address n+1 (0FF11H)	ECC0	R	8	√	√	√	√	√	√	√	√	-	-
Address	Symbol	R/W	Bit length																				
Address n (0FF10H)	CR00	R/W	16																				
Address n+1 (0FF11H)	ECC0	R	8																				
14	<p>It may not be possible to correctly fetch a word instruction starting from an odd address via the program on the emulation memory.  Workaround: None.</p>	√	√	√	√	√	√	√	√	√	-												

√ : Applicable, - : Not applicable

**Note** The “control code” is the second digit from the left in the 10-digit serial number in the warranty supplied with the in-circuit emulator you purchased (if it has not been upgraded). If the in-circuit emulator has been upgraded, a label indicating the new version is attached to the in-circuit emulator and the x in V-UP LEVEL x on this label indicates the control code.

The functions of products with control codes C and D are the same as the functions of products with control code E. The control code I does not exist.

## 2. Restrictions

### - Flash self-mode related restrictions

The versions listed below support flash self-mode with the following restrictions.

IE-78K4-NS : Control code H

ID78K4-NS : E1.11h

(1) Of the four execution events and four access events in flash self-mode, only one of each may be consumed. Consequently, users should release the above events on the debugger side and then set a maximum of three events each when switching to flash self-mode.

(2) The system may enter restart processing once a break occurs in flash self-mode that is not the result of break settings. This will cause some of the time measurement results and trace data to become invalid.

- Request flags other than the first interrupt request flag will not be enabled if nesting interrupts are generated during non-realtime execution (1 instruction execution, Step, Next (excluding CALL statement), Slowmotion) with the debugger. As a result, there is a possibility of incorrect operation with programs that perform some type of processing by checking request flags.

Workaround: Execute two or more instructions in realtime (Go, Return, Go&Go, Come, CPU Reset&Go).