

CUSTOMER NOTIFICATION

SUD-DT-03-0212-1-E (1/2)

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IE-784046-NS-EM1 (Control Code: A)

Operating Precautions

Be sure to read this document before using the product.

1. Restrictions	2
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Notes on Using IE-784046-NS-EM1

1. Restrictions

No.	Description
1	<p>The initial value of the standby control register (STBC) is 30h. When using the emulator, however, fix bits 2 to 7 of the standby control register (STBC) to 0; otherwise the emulator will not operate correctly.</p> <p>* There is no problem regarding the operation even if a value such as 000000xxB is written to the actual CPU. (The CPU reads the value as 001100xxB.)</p> <p>Remark x: 0 or 1, B: Binary code</p>
2	<p>The initial value of the SFR register BW differs from that of the target device as shown below.</p> <ul style="list-style-type: none"> • When BWD = 0, the initial value of the target device is 0000H, whereas that of the emulator may be F000H. • When BWD = 1, the initial value of the target device is 00FFH, whereas that of the emulator may be FFFFH or 0FFFH. <p>The value of the unused bit varies because of restrictions on the emulation device, but this does not affect the emulation operation.</p>