

CUSTOMER NOTIFICATION

SUD-TT-0117-4-E (1/4)

July 26, 2002

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IE-703217-G1-EM1 (Control Code: A, B)

Operating Precautions

Be sure to read this document before using the product.

1. Product Version	2
2. Product History	2
3. Details of Bugs and Added Specifications	2
4. Other Cautions.....	4

Notes on Using IE-703217-G1-EM1

1. Product Version

Control Code	Board Version	Peripheral Chip
A	V1.00	μ PD70F3217Y DS1.1
B	V1.20	μ PD703217 ES1.4

Employ an IE-V850ES-G1 with a control code of C or later when using this emulation board.

2. Production History

No.	Bugs and Changes/Additions to Specifications	Control Code ^{Note}	
		A	B
1	ROM correction function cannot be emulated	Permanent restriction	
2	Restriction on ROM correction	×	√
3	Restriction on use-prohibited area	Permanent restriction	
4	Restriction on D/A converter	×	√
5	Restriction on interrupt request flag	×	√

×: Applicable, √: Not applicable or already corrected

Note The “control code” is the second digit from the left in the 10-digit serial number in the warranty supplied with the product you purchased (if it has not been upgraded). If the product has been upgraded, a label indicating the new version is attached to the product and the x in V-UP LEVEL x on this label indicates the control code.

3. Details of Bugs and Added Specifications

No. 1 ROM correction function cannot be emulated

[Description]

The ROM correction function cannot be emulated.

[Workaround]

There is no workaround.

No.2 Restriction on ROM correction

[Description]

Correction address registers 0 to 3 (CORAD0 to CORAD3) and the correction control register (CORCN) cannot be read/written correctly.

[Workaround]

This restriction has been corrected in control code B.

No.3 Restriction on use-prohibited area

[Description]

A fail-safe break does not occur even if the program is executed or an access occurs in 0x3FFC000 to 0x3FFDFFF (device with 6 KB internal RAM: 0x3FFC000 to 0x3FFD7FF) in the use-prohibited area of the target device.

[Workaround]

Regard this as a permanent restriction.

A break can be generated when the program is executed or an access occurs by setting a break on the debugger under the following conditions.

◆ Device with 4 KB internal RAM

- Detects program execution at 0x3FFC000 to 0x3FFDFFF
 - Event status: Execution
 - Address: 0x3ffc000 to 0x3ffdfff
 (One execution event is used by setting the above conditions.)
- Detects access for 0x3FFC000 to 0x3FFDFFF
 - Event status: R/W
 - Access size: No Condition
 - Address: 0x3ffc000 to 0x3ffdfff
 (One access event is used by setting the above conditions.)

◆ Device with 6 KB internal RAM

- Detects program execution at 0x3FFC000 to 0x3FFD7FF
 - Event status: Execution
 - Address: 0x3ffc000 to 0x3ffd7ff
 (One execution event is used by setting the above conditions.)
- Detects access for 0x3FFC000 to 0x3FFD7FF
 - Event status: R/W
 - Access size: No Condition
 - Address: 0x3ffc000 to 0x3ffd7ff
 (One access event is used by setting the above conditions.)

No.4 Restriction on D/A converter

[Description]

When AV_{REF1} is used at less than 3.0 V, the D/A conversion value cannot be guaranteed.
(A D/A converter is not provided in the V850ES/KF1.)

[Workaround]

Use the D/A converter with $AV_{REF1} = 3.0$ V or higher. ($V_{DD} \geq AV_{REF1}$)
This restriction has been corrected in control code B.

No. 5 Restriction on interrupt request flag

[Description]

The interrupt request flag is not cleared even if an interrupt request has been acknowledged.

[Workaround]

Add processing to clear the interrupt request flag at the beginning of the handler address of the acknowledged interrupt. Clearance of the interrupt request flag can be performed by instruction execution.

This restriction has been corrected in control code B.

4. Other Cautions

No.1 Caution on ID703000 (ID850)

[Description]

Use V2.41 or later when using the IE-703217-G1-EM1 with the NEC debugger.