## Old Company Name in Catalogs and Other Documents

On April 1<sup>st</sup>, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

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# **RENESAS TECHNICAL UPDATE**

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Product Category	MPU&MCU		Document No.	TN-H8*-A336A/E	Rev.	1.00
Title	H8SX/1653, H8SX/1663 Group: Error Correction of the 8-bit timer in the Hard Manual	ware	Information Category	Technical Notification		
		Lot No.				
Applicable Product	See below.	All lots	Reference Document	See below.		
Thank you fo We would like timer (TMR) i	r your consistent patronage of Renesas semice e to inform you of the error correction in tables n the H8SX/1653 and H8SX/1663 Groups.	onductor pro	oducts. 2.3, Clock Input t	o TCNT and Count Con	dition of t	he 8-bit
[Contents]						
[Applicable P	roducts and Related Document]					
H8	SX/1653 Group Hardware Manual (REJ09B02	219-0100, R	ev. 1.00)			
H8	SX/1663 Group Hardware Manual (REJ09B02	294-0100, R	ev. 1.00)			



#### [Before Change]

#### Table 12.2 Clock Input to TCNT and Count Condition (Units 0 and 1)

		TCR		тс	CR	
Channel	Bit 2 CKS2	Bit 1 CKS1	Bit 0 CKS0	Bit 1 ICKS1	Bit 0 ICKS0	Description
TMR_0	0	0	0	_	_	Clock input prohibited
	0	0	1	0	0	Uses internal clock. Counts at rising edge of Pø/8.
				0	1	Uses internal clock. Counts at rising edge of Pø/2.
				1	0	Uses internal clock. Counts at falling edge of Pø/8.
				1	1	Uses internal clock. Counts at falling edge of Pø/2.
	0	1	0	0	0	Uses internal clock. Counts at rising edge of Pø/64.
				0	1	Uses internal clock. Counts at rising edge of $P\phi/32$ .
				1	0	Uses internal clock. Counts at falling edge of Pø/64.
				1	1	Uses internal clock. Counts at falling edge of Pø/32.
	0	1	1	0	0	Uses internal clock. Counts at rising edge of Pø/8192.
				0	1	Uses internal clock. Counts at rising edge of $P\phi/1024$ .
				1	0	Uses internal clock. Counts at falling edge of Pø/8192.
				1	1	Uses internal clock. Counts at falling edge of Pø/1024.
	1	0	0	_	_	Counts at TCNT_1 overflow signal* <sup>1</sup> .
TMR_1	0	0	0	_	_	Clock input prohibited
	0	0	1	0	0	Uses internal clock. Counts at rising edge of $P\phi/8$ .
				0	1	Uses internal clock. Counts at rising edge of Po/2.
				1	0	Uses internal clock. Counts at falling edge of Pø/8.
				1	1	Uses internal clock. Counts at falling edge of $P\phi/2$ .
	0	1	0	0	0	Uses internal clock. Counts at rising edge of P
				0	1	Uses internal clock. Counts at rising edge of $P\phi/32$ .
				1	0	Uses internal clock. Counts at falling edge of Pø/64.
				1	1	Uses internal clock. Counts at falling edge of $P\phi/32$ .
	0	1	1	0	0	Uses internal clock. Counts at rising edge of $P\phi/8192$ .
				0	1	Uses internal clock. Counts at rising edge of P
				1	0	Uses internal clock. Counts at falling edge of $P\phi/8192$ .
				1	1	Uses internal clock. Counts at falling edge of $P\phi/1024$ .
	1	0	0	—	_	Counts at TCNT_0 compare match A* <sup>1</sup> .
All	1	0	1	_	_	Uses external clock. Counts at rising edge* <sup>2</sup> .
	1	1	0	_	_	Uses external clock. Counts at falling edge* <sup>2</sup> .
	1	1	1	_	_	Uses external clock. Counts at both rising and falling edges* <sup>2</sup> .

Notes: 1. If the clock input of channel 0 is the TCNT\_1 overflow signal and that of channel 1 is the TCNT\_0 compare match signal, no incrementing clock is generated. Do not use this setting.

2. Descriptions omitted (No change).



 Table 12.3
 Clock Input to TCNT and Count Condition (Units 2 and 3)

Bit 2         Bit 1         Bit 0         Bit 1         Bit 1         Bit 1         Bit 2         CKS3         CCurs at CS1         Curs at Taing edge of Pv82.           0         1         0         0         1         Uses internal clock. Counts at fining edge of Pv32.         Curs at TCNT_1 verflow signal*.           TMR_5         0         0         0         -         -         Clock input prohibited         Curs at TCNT_1	Bit 2         Bit 0         Bit 0         Bit 0           TMR_4         0         0         0         -         -         Clock input prohibited           TMR_4         0         0         0         -         -         Clock input prohibited           0         0         0         0         0         Uses internal clock. Counts at rising edge of Pq/3.           1         0         0         Uses internal clock. Counts at rising edge of Pq/3.         0           0         1         0         0         Uses internal clock. Counts at rising edge of Pq/32.           0         1         0         Uses internal clock. Counts at rising edge of Pq/32.           0         1         1         Uses internal clock. Counts at rising edge of Pq/32.           1         0         0         0         Uses internal clock. Counts at rising edge of Pq/32.           1         0         0         -         -         Counts at rising edge of Pq/32.           1         0         0         -         -         Counts at rising edge of Pq/32.           1         0         0         -         -         Counts at rising edge of Pq/32.           1         0         0         Uses internal clock. Counts at rising edge			TCR		тс	CR	
TMR_4         0         0	ThiR_4         0         0         0	Channel	Bit 2 CKS2	Bit 1 CKS1	Bit 0 CKS0	Bit 1 ICKS1	Bit 0 ICKS0	Description
0         0         1         0         0         Uses internal clock. Counts at rising edge of P4/8.           1         0         Uses internal clock. Counts at rising edge of P4/8.           1         1         Uses internal clock. Counts at rising edge of P4/8.           0         1         Uses internal clock. Counts at rising edge of P4/8.           0         1         Uses internal clock. Counts at rising edge of P4/8.           0         1         Uses internal clock. Counts at rising edge of P4/8.           1         0         Uses internal clock. Counts at rising edge of P4/8.           1         0         Uses internal clock. Counts at rising edge of P4/8.           1         0         Uses internal clock. Counts at rising edge of P4/82.           0         1         Uses internal clock. Counts at rising edge of P4/1024.           1         0         Uses internal clock. Counts at rising edge of P4/1024.           1         0         0	0         0         1         0         0         Uses internal clock. Counts at rising edge of Pv8.           0         1         0         Uses internal clock. Counts at rising edge of Pv8.           0         1         0         0         0         0         0           0         0         0         0         Uses internal clock. Counts at rising edge of Pv8.           0         1         0         0         0         Uses internal clock. Counts at rising edge of Pv8.           0         1         Uses internal clock. Counts at rising edge of Pv8.         0         1         Uses internal clock. Counts at rising edge of Pv8.           0         1         1         Uses internal clock. Counts at rising edge of Pv8.         1         0         Uses internal clock. Counts at rising edge of Pv8.           1         0         0          Clock input prohibiled         1         0         0         1         0         0         1         0         0         1         0         0         1         0         0         1         0         0         1         Uses internal clock. Counts at rising edge of Pv8.           1         0         0         0         Uses internal clock. Counts at rising edge of Pv8.         1	TMR_4	0	0	0	_	_	Clock input prohibited
Image: bit is the state of the sta	0         1         Uses internal clock. Counts at rising edge of Po/2.           1         0         Uses internal clock. Counts at rising edge of Po/2.           0         1         0         0           0         1         Uses internal clock. Counts at rising edge of Po/2.           1         0         Uses internal clock. Counts at rising edge of Po/2.           1         0         Uses internal clock. Counts at rising edge of Po/32.           1         0         Uses internal clock. Counts at rising edge of Po/32.           0         1         1         Uses internal clock. Counts at rising edge of Po/32.           1         0         0         Uses internal clock. Counts at rising edge of Po/32.           1         0         0         Uses internal clock. Counts at rising edge of Po/32.           1         0         0         -         -           1         0         0         -         -           1         0         0         -         -           1         0         0         0         0         0           1         1         Uses internal clock. Counts at rising edge of Po/2.         -           1         0         0         Uses internal clock. Counts at rising edge of Po/2. <td></td> <td>0</td> <td>0</td> <td>1</td> <td>0</td> <td>0</td> <td>Uses internal clock. Counts at rising edge of P</td>		0	0	1	0	0	Uses internal clock. Counts at rising edge of P
1         0         Uses internal clock. Counts at falling edge of Pol/8.           0         1         0         Uses internal clock. Counts at falling edge of Pol/8.           0         1         0         0         Uses internal clock. Counts at falling edge of Pol/8.           0         1         Uses internal clock. Counts at falling edge of Pol/8.         1         0           0         1         Uses internal clock. Counts at falling edge of Pol/84.         1         1           0         1         Uses internal clock. Counts at falling edge of Pol/812.         0         1           0         1         Uses internal clock. Counts at falling edge of Pol/812.         0         1           0         1         Uses internal clock. Counts at falling edge of Pol/1024.         1         0           1         0         0         -         -         Counts at falling edge of Pol/1024.           1         0         0         -         -         Counts at falling edge of Pol/1024.           1         0         0         -         -         Counts at falling edge of Pol/1024.           1         0         0         Uses internal clock. Counts at falling edge of Pol/8.         0           0         1         Uses internal clock. Counts at falling	1       0       Uses internal clock. Counts at failing edge of Pp/8.         0       1       0       0       Uses internal clock. Counts at failing edge of Pp/84.         0       1       Uses internal clock. Counts at failing edge of Pp/84.       1       1       Uses internal clock. Counts at failing edge of Pp/84.         0       1       Uses internal clock. Counts at failing edge of Pp/84.       1       1       Uses internal clock. Counts at failing edge of Pp/81.         0       1       Uses internal clock. Counts at failing edge of Pp/81.       1       0       Uses internal clock. Counts at failing edge of Pp/81.         1       0       0       Uses internal clock. Counts at failing edge of Pp/81.       1       1       Uses internal clock. Counts at failing edge of Pp/81.         1       0       0       -       -       Clock input prohibited       1       1       Uses internal clock. Counts at failing edge of Pp/8.       1       1       Uses internal clock. Counts at failing edge of Pp/82.       1       1       Uses internal clock. Counts at failing edge of Pp/82.       1       1       Uses internal clock. Counts at failing edge of Pp/82.       1       1       Uses internal clock. Counts at failing edge of Pp/82.       1       1       Uses internal clock. Counts at failing edge of Pp/82.       1       1       Uses internal clock. Counts at failing edge of					0	1	Uses internal clock. Counts at rising edge of P
Image: state of the s	1         Uses internal clock. Counts at railing edge of Py62.           0         1         Uses internal clock. Counts at railing edge of Py64.           1         0         Uses internal clock. Counts at railing edge of Py64.           0         1         Uses internal clock. Counts at railing edge of Py64.           0         1         Uses internal clock. Counts at railing edge of Py6192.           0         1         Uses internal clock. Counts at railing edge of Py6192.           0         1         Uses internal clock. Counts at railing edge of Py6192.           0         1         Uses internal clock. Counts at railing edge of Py6192.           1         0         Uses internal clock. Counts at railing edge of Py6192.           1         0         0         -           1         Uses internal clock. Counts at railing edge of Py6192.           1         Uses internal clock. Counts at railing edge of Py61.           1         Uses internal clock. Counts at railing edge of Py64.           0         0         Uses internal clock. Counts at railing edge of Py62.           1         1         Uses internal clock. Counts at railing edge of Py64.           0         1         Uses internal clock. Counts at railing edge of Py64.           0         1         Uses internal clock. Counts at railing edge of Py					1	0	Uses internal clock. Counts at falling edge of P
0         1         0         0         Uses internal clock. Counts at rising edge of P4/64.           0         1         Uses internal clock. Counts at rising edge of P4/64.           1         0         Uses internal clock. Counts at failing edge of P4/64.           0         1         1         Uses internal clock. Counts at failing edge of P4/64.           0         1         1         Uses internal clock. Counts at rising edge of P4/61.           0         1         1         Uses internal clock. Counts at rising edge of P4/61.           1         0         0         Uses internal clock. Counts at rising edge of P4/1024.           1         0         0         -         Counts at rising edge of P4/1024.           1         0         0         -         Counts at rising edge of P4/1024.           1         0         0         -         Counts at rising edge of P4/1024.           1         0         0         Uses internal clock. Counts at rising edge of P4/1024.           1         0         0         Uses internal clock. Counts at rising edge of P4/1024.           1         0         0         Uses internal clock. Counts at rising edge of P4/1024.           1         1         Uses internal clock. Counts at rising edge of P4/1024.           1 <td>0         1         0         0         Uses internal clock. Counts at fising edge of Pw/64.           1         0         Uses internal clock. Counts at failing edge of Pw/64.           1         1         Uses internal clock. Counts at failing edge of Pw/64.           0         1         1         Uses internal clock. Counts at failing edge of Pw/64.           1         0         Uses internal clock. Counts at fising edge of Pw/1024.           1         0         Uses internal clock. Counts at fising edge of Pw/1024.           1         0         Uses internal clock. Counts at failing edge of Pw/1024.           1         0         Uses internal clock. Counts at failing edge of Pw/1024.           1         0         0         -           0         0         -         Clock input prohibited           1         0         Uses internal clock. Counts at fising edge of Pw/8.           0         1         Uses internal clock. Counts at fising edge of Pw/8.           1         1         Uses internal clock. Counts at fising edge of Pw/8.           0         1         Uses internal clock. Counts at fising edge of Pw/8.           1         1         Uses internal clock. Counts at fising edge of Pw/8.           1         1         Uses internal clock. Counts at fising edge of Pw/1024.<td></td><td></td><td></td><td></td><td>1</td><td>1</td><td>Uses internal clock. Counts at falling edge of P</td></td>	0         1         0         0         Uses internal clock. Counts at fising edge of Pw/64.           1         0         Uses internal clock. Counts at failing edge of Pw/64.           1         1         Uses internal clock. Counts at failing edge of Pw/64.           0         1         1         Uses internal clock. Counts at failing edge of Pw/64.           1         0         Uses internal clock. Counts at fising edge of Pw/1024.           1         0         Uses internal clock. Counts at fising edge of Pw/1024.           1         0         Uses internal clock. Counts at failing edge of Pw/1024.           1         0         Uses internal clock. Counts at failing edge of Pw/1024.           1         0         0         -           0         0         -         Clock input prohibited           1         0         Uses internal clock. Counts at fising edge of Pw/8.           0         1         Uses internal clock. Counts at fising edge of Pw/8.           1         1         Uses internal clock. Counts at fising edge of Pw/8.           0         1         Uses internal clock. Counts at fising edge of Pw/8.           1         1         Uses internal clock. Counts at fising edge of Pw/8.           1         1         Uses internal clock. Counts at fising edge of Pw/1024. <td></td> <td></td> <td></td> <td></td> <td>1</td> <td>1</td> <td>Uses internal clock. Counts at falling edge of P</td>					1	1	Uses internal clock. Counts at falling edge of P
Image: second	0         1         Uses internal clock. Counts at failing edge of Pe/32.           1         1         Uses internal clock. Counts at failing edge of Pe/32.           0         1         1         Uses internal clock. Counts at failing edge of Pe/32.           0         1         1         Uses internal clock. Counts at failing edge of Pe/32.           1         0         0         Uses internal clock. Counts at failing edge of Pe/32.           1         0         0         Uses internal clock. Counts at failing edge of Pe/32.           1         0         0		0	1	0	0	0	Uses internal clock. Counts at rising edge of P
Image: Normal content of the second	1       0       Uses internal clock. Counts at failing edge of P4/32.         0       1       1       Uses internal clock. Counts at failing edge of P4/32.         0       1       Uses internal clock. Counts at failing edge of P4/192.         1       0       Uses internal clock. Counts at failing edge of P4/192.         1       0       Uses internal clock. Counts at failing edge of P4/192.         1       0       0       -         1       Uses internal clock. Counts at failing edge of P4/192.         1       0       Uses internal clock. Counts at failing edge of P4/192.         1       0       0       -         1       0       Uses internal clock. Counts at failing edge of P4/182.         0       0       -       -         1       0       Uses internal clock. Counts at failing edge of P4/18.         0       1       Uses internal clock. Counts at failing edge of P4/18.         1       Uses internal clock. Counts at failing edge of P4/18.         1       Uses internal clock. Counts at failing edge of P4/18.         1       Uses internal clock. Counts at failing edge of P4/18.         1       Uses internal clock. Counts at failing edge of P4/18.         1       Uses internal clock. Counts at failing edge of P4/18.         1 </td <td></td> <td></td> <td></td> <td></td> <td>0</td> <td>1</td> <td>Uses internal clock. Counts at rising edge of P</td>					0	1	Uses internal clock. Counts at rising edge of P
1         1         Uses internal clock. Counts at falling edge of Pe/32.           0         1         1         Uses internal clock. Counts at falling edge of Pe/8192.           0         1         Uses internal clock. Counts at rising edge of Pe/1024.           1         0         0         Uses internal clock. Counts at rising edge of Pe/1024.           1         0         0         Uses internal clock. Counts at falling edge of Pe/1024.           1         0         0         -         -           1         0         0         -         -           1         0         0         -         -           1         0         0         -         -           1         0         0         -         -           1         0         0         -         -           1         0         0         Uses internal clock. Counts at falling edge of Pe/8.           1         1         Uses internal clock. Counts at falling edge of Pe/8.           1         1         Uses internal clock. Counts at falling edge of Pe/82.           1         1         Uses internal clock. Counts at falling edge of Pe/82.           1         1         Uses internal clock. Counts at falling edge of Pe/1024. <td>Image: constraint of the constraint of the</td> <td></td> <td></td> <td></td> <td></td> <td>1</td> <td>0</td> <td>Uses internal clock. Counts at falling edge of P</td>	Image: constraint of the					1	0	Uses internal clock. Counts at falling edge of P
0         1         1         0         0         Uses internal clock. Counts at rising edge of P\u024.           1         0         Uses internal clock. Counts at rising edge of P\u024.         1         0         Uses internal clock. Counts at rising edge of P\u024.           1         0         0	0         1         1         0         0         Uses internal clock. Counts at rising edge of PVe1024.           1         0         0					1	1	Uses internal clock. Counts at falling edge of P
Image: state of the s	0         1         Uses internal clock. Counts at rising edge of Pq/1024.           1         0         Uses internal clock. Counts at rising edge of Pq/1024.           1         0         0         -         -         Counts at TCNT_1 overflow signal*.           TIMR_5         0         0         0         -         -         Clock input profibited           0         0         0         -         -         Clock input profibited           0         0         0         0         Uses internal clock. Counts at rising edge of Pq/02.           1         0         Uses internal clock. Counts at rising edge of Pq/02.         1         0         Uses internal clock. Counts at rising edge of Pq/02.           0         1         Uses internal clock. Counts at rising edge of Pq/02.         1         0         Uses internal clock. Counts at rising edge of Pq/02.           0         1         Uses internal clock. Counts at rising edge of Pq/02.         1         1         Uses internal clock. Counts at rising edge of Pq/02.           1         0         0         Uses internal clock. Counts at rising edge of Pq/02.         1         1         Uses internal clock. Counts at rising edge of Pq/02.           1         1         Uses internal clock. Counts at rising edge of Pq/024.         1         1		0	1	1	0	0	Uses internal clock. Counts at rising edge of Pø/8192.
Image: state of the s	1         0         Uses internal clock. Counts at rising edge of P.           1         0         0					0	1	Uses internal clock. Counts at rising edge of P
Image: 1         1         Uses internal clock. Counts at falling edge of P\u00f5/1024.           1         0         0	Image: 1         Uses internal clock. Counts at falling edge of Pq/1024.           TMR_5         0         0         -         -         Counts at TCNT_1 overflow signal*.           TMR_5         0         0         0         -         -         Clock input prohibited           0         0         0         Uses internal clock. Counts at failing edge of Pq/2.         -         -           1         0         Uses internal clock. Counts at failing edge of Pq/2.         -         -         -           0         1         Uses internal clock. Counts at failing edge of Pq/2.         -         -         -           0         1         Uses internal clock. Counts at failing edge of Pq/64.         -         -         -           0         1         Uses internal clock. Counts at failing edge of Pq/61.         -         -         -           1         0         Uses internal clock. Counts at failing edge of Pq/102.         -         -         -           0         1         Uses internal clock. Counts at failing edge of Pq/102.         -         -         -           0         1         Uses internal clock. Counts at failing edge of Pq/1024.         -         -         -           1         0         -         -         C					1	0	Uses internal clock. Counts at rising edge of Pø.
1         0         0          Counts at TCNT_1 overflow signal*.           TMR_5         0         0         0          Clock input prohibited           0         0         1         0         0         Uses internal clock. Counts at rising edge of P\u03e9/8.           0         0         1         Uses internal clock. Counts at falling edge of P\u03e9/8.           0         1         Uses internal clock. Counts at falling edge of P\u03e9/8.           0         1         Uses internal clock. Counts at falling edge of P\u03e9/64.           0         1         Uses internal clock. Counts at rising edge of P\u032.           0         1         Uses internal clock. Counts at rising edge of P\u03e9/32.           1         0         Uses internal clock. Counts at rising edge of P\u03e9/32.           0         1         Uses internal clock. Counts at rising edge of P\u03e9/32.           0         1         Uses internal clock. Counts at rising edge of P\u03e9/32.           0         1         Uses internal clock. Counts at rising edge of P\u03e9/32.           0         1         Uses internal clock. Counts at rising edge of P\u03e9/4192.           0         0          Counts at TCNT_0 compare match A*.           All         0          S	1         0					1	1	Uses internal clock. Counts at falling edge of Po/1024.
TMR_5       0       0       -       -       Clock input prohibited         0       0       1       0       0       Uses internal clock. Counts at rising edge of P\u03e9/8.         1       0       Uses internal clock. Counts at rising edge of P\u03e9/8.       -       -         0       1       Uses internal clock. Counts at falling edge of P\u03e9/8.       -         0       1       Uses internal clock. Counts at rising edge of P\u03e9/64.       -         0       1       Uses internal clock. Counts at rising edge of P\u03e9/64.       -         1       0       Uses internal clock. Counts at rising edge of P\u03e9/64.       -         1       1       Uses internal clock. Counts at rising edge of P\u03e9/64.       -         1       1       Uses internal clock. Counts at rising edge of P\u03e9/1024.       -         1       0       Uses internal clock. Counts at rising edge of P\u03e9/1024.       -         1       0       Uses internal clock. Counts at rising edge of P\u03e9/1024.       -         1       0       -       -       Counts at rising edge of P\u03e9/1024.         1       0       -       -       Counts at rising edge of P\u03e9/1024.         1       0       -       -       Counts at rising edge of P\u0404.<	TMR_5       0       0       -       -       Clock input prohibited         0       0       1       Uses internal clock. Counts at rising edge of P\v/8.         1       1       Uses internal clock. Counts at falling edge of P\v/8.         1       1       Uses internal clock. Counts at falling edge of P\v/8.         0       1       0       Uses internal clock. Counts at falling edge of P\v/8.         0       1       Uses internal clock. Counts at falling edge of P\v/84.         1       1       Uses internal clock. Counts at falling edge of P\v/84.         1       1       Uses internal clock. Counts at rising edge of P\v/102.         0       1       Uses internal clock. Counts at rising edge of P\v/102.         0       1       Uses internal clock. Counts at rising edge of P\v/102.         0       1       Uses internal clock. Counts at rising edge of P\v/102.         0       1       Uses internal clock. Counts at rising edge of P\v/102.         1       0       0       -         1       0       0       -         1       0       -       -         1       0       -       -         1       0       -       -         1       1       -       -		1	0	0	_	_	Counts at TCNT 1 overflow signal*.
0       0       1       0       0       Uses internal clock. Counts at rising edge of P\u03e9/8.         1       0       Uses internal clock. Counts at falling edge of P\u03e9/8.         1       1       Uses internal clock. Counts at falling edge of P\u03e9/8.         0       1       0       Uses internal clock. Counts at falling edge of P\u03e9/8.         0       1       0       Uses internal clock. Counts at rising edge of P\u03e9/64.         0       1       Uses internal clock. Counts at falling edge of P\u03e9/64.         1       1       Uses internal clock. Counts at falling edge of P\u03e9/64.         1       1       Uses internal clock. Counts at falling edge of P\u03e9/64.         1       1       Uses internal clock. Counts at falling edge of P\u03e9/82.         0       1       Uses internal clock. Counts at rising edge of P\u03e9/1024.         1       0       0       Uses internal clock. Counts at rising edge of P\u03e9/1024.         1       0       0       —       Counts at TCNT_0 compare match A*.         All       1       0       —       —       Setting prohibited         1       1       0       —       —       Setting prohibited         1       1       0       —       —       Setting prohibited </td <td>0       0       1       0       0       Uses internal clock. Counts at rising edge of Pa/8.         0       1       0       Uses internal clock. Counts at falling edge of Pa/8.         1       0       Uses internal clock. Counts at falling edge of Pa/8.         0       1       Uses internal clock. Counts at falling edge of Pa/8.         0       1       Uses internal clock. Counts at falling edge of Pa/8.         0       1       Uses internal clock. Counts at falling edge of Pa/8.         0       1       Uses internal clock. Counts at falling edge of Pa/8.         1       0       Uses internal clock. Counts at falling edge of Pa/82.         0       1       Uses internal clock. Counts at falling edge of Pa/82.         0       1       Uses internal clock. Counts at falling edge of Pa/8192.         0       1       Uses internal clock. Counts at falling edge of Pa/8192.         1       0       Uses internal clock. Counts at falling edge of Pa/8192.         1       0       Uses internal clock. Counts at falling edge of Pa/8192.         1       0       Uses internal clock. Counts at falling edge of Pa/8192.         1       0       Uses internal clock. Counts at falling edge of Pa/8192.         1       0       Uses internal clock. Counts at falling edge of Pa/8102.</td> <td>TMR 5</td> <td>0</td> <td>0</td> <td>0</td> <td>_</td> <td>_</td> <td>Clock input prohibited</td>	0       0       1       0       0       Uses internal clock. Counts at rising edge of Pa/8.         0       1       0       Uses internal clock. Counts at falling edge of Pa/8.         1       0       Uses internal clock. Counts at falling edge of Pa/8.         0       1       Uses internal clock. Counts at falling edge of Pa/8.         0       1       Uses internal clock. Counts at falling edge of Pa/8.         0       1       Uses internal clock. Counts at falling edge of Pa/8.         0       1       Uses internal clock. Counts at falling edge of Pa/8.         1       0       Uses internal clock. Counts at falling edge of Pa/82.         0       1       Uses internal clock. Counts at falling edge of Pa/82.         0       1       Uses internal clock. Counts at falling edge of Pa/8192.         0       1       Uses internal clock. Counts at falling edge of Pa/8192.         1       0       Uses internal clock. Counts at falling edge of Pa/8192.         1       0       Uses internal clock. Counts at falling edge of Pa/8192.         1       0       Uses internal clock. Counts at falling edge of Pa/8192.         1       0       Uses internal clock. Counts at falling edge of Pa/8192.         1       0       Uses internal clock. Counts at falling edge of Pa/8102.	TMR 5	0	0	0	_	_	Clock input prohibited
0       1       Uses internal clock. Counts at rising edge of P\u00f6/2.         1       0       Uses internal clock. Counts at falling edge of P\u00f6/8.         1       1       Uses internal clock. Counts at falling edge of P\u00f6/8.         0       1       0       Uses internal clock. Counts at falling edge of P\u00f6/8.         0       1       0       Uses internal clock. Counts at rising edge of P\u00f6/64.         0       1       Uses internal clock. Counts at falling edge of P\u00f6/64.         1       1       Uses internal clock. Counts at falling edge of P\u00f6/82.         0       1       1       Uses internal clock. Counts at falling edge of P\u00f6/82.         0       1       Uses internal clock. Counts at rising edge of P\u00f6/82.         0       1       Uses internal clock. Counts at rising edge of P\u00f6/8192.         0       1       Uses internal clock. Counts at rising edge of P\u00f6/1024.         1       0       0       —         1       0       0       —         1       0       0       —         1       0       0       —         1       0       0       —         1       1       0       —       —         2       1 <td< td=""><td>0       1       Uses internal clock. Counts at rising edge of P\u02.         1       0       Uses internal clock. Counts at failing edge of P\u02.         0       1       0       Uses internal clock. Counts at failing edge of P\u02.         0       1       0       Uses internal clock. Counts at failing edge of P\u02.         0       1       0       Uses internal clock. Counts at failing edge of P\u02.         1       0       Uses internal clock. Counts at failing edge of P\u03.         1       1       Uses internal clock. Counts at failing edge of P\u03.         0       1       1       Uses internal clock. Counts at failing edge of P\u03.         0       1       Uses internal clock. Counts at rising edge of P\u03.         0       1       Uses internal clock. Counts at rising edge of P\u03.         1       0       Uses internal clock. Counts at rising edge of P\u03.         1       0       0       -         1       0       0       -         1       0       0       -         1       0       0       -         1       1       0       -         1       1       0       -         1       1       -       Setting prohibited</td><td>-</td><td>0</td><td>0</td><td>1</td><td>0</td><td>0</td><td>Uses internal clock. Counts at rising edge of Pø/8.</td></td<>	0       1       Uses internal clock. Counts at rising edge of P\u02.         1       0       Uses internal clock. Counts at failing edge of P\u02.         0       1       0       Uses internal clock. Counts at failing edge of P\u02.         0       1       0       Uses internal clock. Counts at failing edge of P\u02.         0       1       0       Uses internal clock. Counts at failing edge of P\u02.         1       0       Uses internal clock. Counts at failing edge of P\u03.         1       1       Uses internal clock. Counts at failing edge of P\u03.         0       1       1       Uses internal clock. Counts at failing edge of P\u03.         0       1       Uses internal clock. Counts at rising edge of P\u03.         0       1       Uses internal clock. Counts at rising edge of P\u03.         1       0       Uses internal clock. Counts at rising edge of P\u03.         1       0       0       -         1       0       0       -         1       0       0       -         1       0       0       -         1       1       0       -         1       1       0       -         1       1       -       Setting prohibited	-	0	0	1	0	0	Uses internal clock. Counts at rising edge of Pø/8.
1       0       Uses internal clock. Counts at falling edge of P\u03ep/8.         1       1       Uses internal clock. Counts at falling edge of P\u03ep/8.         0       1       0       Uses internal clock. Counts at rising edge of P\u03ep/8.         0       1       0       Uses internal clock. Counts at rising edge of P\u03ep/64.         0       1       Uses internal clock. Counts at rising edge of P\u03ep/64.         1       0       Uses internal clock. Counts at falling edge of P\u03ep/64.         1       1       Uses internal clock. Counts at falling edge of P\u03ep/8192.         0       1       1       Uses internal clock. Counts at rising edge of P\u03ep/8192.         0       1       Uses internal clock. Counts at rising edge of P\u04ep/1024.         1       0       Uses internal clock. Counts at rising edge of P\u04ep/1024.         1       0       Uses internal clock. Counts at rising edge of P\u04ep/1024.         1       0       -       Counts at TCNT_0 compare match A*.         All       1       0       -       Setting prohibited         1       1       -       Setting prohibited       Setting prohibited         1       1       -       Setting prohibited       Setting prohibited         1       1       -       <	1       0       Uses internal clock. Counts at falling edge of P4/8.         1       1       Uses internal clock. Counts at rising edge of P4/8.         0       1       Uses internal clock. Counts at rising edge of P4/8.         1       0       0       Uses internal clock. Counts at rising edge of P4/8.         1       0       Uses internal clock. Counts at rising edge of P4/8.         1       0       Uses internal clock. Counts at rising edge of P4/812.         1       0       Uses internal clock. Counts at rising edge of P4/812.         0       1       Uses internal clock. Counts at rising edge of P4/812.         0       1       Uses internal clock. Counts at rising edge of P4/812.         1       0       Uses internal clock. Counts at rising edge of P4/812.         1       0       Uses internal clock. Counts at rising edge of P4/812.         1       0       -       Counts at rising edge of P4/812.         1       0       -       -       Counts at rising edge of P4/812.         1       0       -       -       Counts at rising edge of P4/812.         1       1       0       -       -       Setting prohibited         1       1       0       -       -       Setting prohibited         1					0	1	Uses internal clock. Counts at rising edge of Po/2.
1       1       Uses internal clock. Counts at falling edge of P\u02ep/2.         0       1       0       0       Uses internal clock. Counts at rising edge of P\u02ep/46.         0       1       Uses internal clock. Counts at falling edge of P\u02ep/46.         1       0       Uses internal clock. Counts at falling edge of P\u02ep/46.         1       1       Uses internal clock. Counts at falling edge of P\u02ep/46.         0       1       Uses internal clock. Counts at falling edge of P\u02ep/48192.         0       1       Uses internal clock. Counts at rising edge of P\u02ep/48192.         0       1       Uses internal clock. Counts at rising edge of P\u02ep/48192.         0       1       Uses internal clock. Counts at rising edge of P\u02ep/48192.         0       1       Uses internal clock. Counts at rising edge of P\u02ep/41024.         1       0       0       —         1       0       0       —         1       0       0       —         1       1       Uses internal clock. Counts at rising edge of P\u02ep/1024.         1       0       0       —         1       1       0       —         1       1       0       —         1       1       0       —	1       1       Uses internal clock. Counts at failing edge of P4/2.         0       1       0       0       Uses internal clock. Counts at rising edge of P4/24.         0       1       Uses internal clock. Counts at rising edge of P4/24.       1         1       0       Uses internal clock. Counts at rising edge of P4/24.       1         1       0       Uses internal clock. Counts at failing edge of P4/24.         1       1       Uses internal clock. Counts at rising edge of P4/024.         0       1       Uses internal clock. Counts at rising edge of P4/1024.         1       0       Uses internal clock. Counts at rising edge of P4/1024.         1       0       Uses internal clock. Counts at rising edge of P4/1024.         1       0       Uses internal clock. Counts at rising edge of P4/1024.         1       0       -       -         1       0       -       -         1       0       -       -         2       1       1       -         3       1       0       -         4       0       -       -         5       5       5       5         4       1       1       -       -         4       <					1	0	Uses internal clock. Counts at falling edge of P
0       1       0       0       Uses internal clock. Counts at rising edge of P\p/64.         0       1       Uses internal clock. Counts at rising edge of P\p/64.         1       0       Uses internal clock. Counts at falling edge of P\p/64.         1       1       Uses internal clock. Counts at falling edge of P\p/64.         1       1       Uses internal clock. Counts at falling edge of P\p/82.         0       1       1       Uses internal clock. Counts at falling edge of P\p/82.         0       1       1       Uses internal clock. Counts at falling edge of P\p/82.         0       1       1       Uses internal clock. Counts at rising edge of P\p/82.         0       1       Uses internal clock. Counts at rising edge of P\p/1024.         1       0       Uses internal clock. Counts at rising edge of P\p/1024.         1       0       0       —         1       0       0       —         1       0       0       —         1       0       1       —         1       0       1       —         1       1       0       —         1       1       0       —         1       1       1       —         1 <td>0       1       0       0       Uses internal clock. Counts at rising edge of Po/64.         0       1       Uses internal clock. Counts at rising edge of Po/64.         1       0       Uses internal clock. Counts at failing edge of Po/64.         1       1       Uses internal clock. Counts at failing edge of Po/64.         0       1       1       Uses internal clock. Counts at failing edge of Po/64.         0       1       1       Uses internal clock. Counts at rising edge of Po/64.         0       1       1       Uses internal clock. Counts at rising edge of Po/64.         1       0       0       Uses internal clock. Counts at rising edge of Po/64.         1       0       0       Uses internal clock. Counts at rising edge of Po/64.         1       0       0       Uses internal clock. Counts at rising edge of Po/1024.         1       0       0      </td> <td></td> <td></td> <td></td> <td></td> <td>1</td> <td>1</td> <td>Uses internal clock. Counts at falling edge of P//2</td>	0       1       0       0       Uses internal clock. Counts at rising edge of Po/64.         0       1       Uses internal clock. Counts at rising edge of Po/64.         1       0       Uses internal clock. Counts at failing edge of Po/64.         1       1       Uses internal clock. Counts at failing edge of Po/64.         0       1       1       Uses internal clock. Counts at failing edge of Po/64.         0       1       1       Uses internal clock. Counts at rising edge of Po/64.         0       1       1       Uses internal clock. Counts at rising edge of Po/64.         1       0       0       Uses internal clock. Counts at rising edge of Po/64.         1       0       0       Uses internal clock. Counts at rising edge of Po/64.         1       0       0       Uses internal clock. Counts at rising edge of Po/1024.         1       0       0					1	1	Uses internal clock. Counts at falling edge of P//2
0       1       Uses internal clock. Counts at rising edge of P\$\u03c9/32.         1       0       Uses internal clock. Counts at falling edge of P\$\u03c9/32.         0       1       1       Uses internal clock. Counts at falling edge of P\$\u03c9/32.         0       1       1       Uses internal clock. Counts at falling edge of P\$\u03c9/32.         0       1       1       Uses internal clock. Counts at falling edge of P\$\u03c9/32.         0       1       1       Uses internal clock. Counts at rising edge of P\$\u03c9/1024.         1       0       Uses internal clock. Counts at rising edge of P\$\u03c9/1024.         1       0       Uses internal clock. Counts at rising edge of P\$\u03c9/1024.         1       0       Uses internal clock. Counts at falling edge of P\$\u03c9/1024.         1       0       Uses internal clock. Counts at falling edge of P\$\u03c9/1024.         1       0       Uses internal clock. Counts at falling edge of P\$\u03c9/1024.         1       0       -       Counts at TCNT_0 compare match A*.         All       1       0       -       Setting prohibited         1       1       -       Setting prohibited       Setting prohibited         1       1       -       Setting prohibited       Setting prohibited         Note:	0       1       Uses internal clock. Counts at failing edge of P\v032.         1       0       Uses internal clock. Counts at failing edge of P\v032.         0       1       1       Uses internal clock. Counts at failing edge of P\v032.         0       1       1       Uses internal clock. Counts at failing edge of P\v032.         0       1       1       Uses internal clock. Counts at failing edge of P\v032.         0       1       1       Uses internal clock. Counts at failing edge of P\v032.         0       1       Uses internal clock. Counts at failing edge of P\v01024.         1       0       Uses internal clock. Counts at failing edge of P\v1024.         1       0       0		0	1	0	0	0	Uses internal clock. Counts at rising edge of Ph/64
1       0       Uses internal clock. Counts at falling edge of P\u00f64.         1       1       Uses internal clock. Counts at falling edge of P\u00f64.         0       1       1       Uses internal clock. Counts at falling edge of P\u00f64.         0       1       1       Uses internal clock. Counts at falling edge of P\u00f64.         0       1       Uses internal clock. Counts at rising edge of P\u00f64.         1       0       0       Uses internal clock. Counts at rising edge of P\u00f64.         1       0       0       Uses internal clock. Counts at rising edge of P\u00f64.         1       0       0       —       Counts at TCNT_0 compare match A*.         All       1       0       —       —       Setting prohibited         1       1       0       —       —       Setting prohibited         1       1       0       —       —       Setting prohibited         1       1       1       —       —       Setting prohibited         1       1       1       —       —       Setting prohibited         1       1       1       —       —       Setting prohibited         Note:       *       If the clock input of channel 4 is the TCNT_1 overflow signal and that	0       1       0       Uses internal clock. Counts at failing edge of Po/64.         1       1       Uses internal clock. Counts at failing edge of Po/64.         0       1       1       Uses internal clock. Counts at failing edge of Po/64.         1       0       0       Uses internal clock. Counts at failing edge of Po/64.         1       0       0       Uses internal clock. Counts at failing edge of Po/1024.         1       0       0       Uses internal clock. Counts at failing edge of Po/1024.         1       0       0       —       Counts at TCNT_0 compare match A*.         All       1       0       —       —       Setting prohibited         1       1       0       —       —       Setting prohibited         1       1       0       —       —       Setting prohibited         1       1       1       —       —       Setting prohibited         Note: *       If the clock input of channel 4 is the TCNT_1 overflow signal and that of channel 5 is the TCNT_0 compare match signal, no incrementing clock is generated. Do not use this setting.		0		U	0	1	Uses internal clock. Counts at rising edge of P#/32
1       1       Uses internal clock. Counts at falling edge of Pø/32.         0       1       1       Uses internal clock. Counts at falling edge of Pø/31.         0       1       1       Uses internal clock. Counts at rising edge of Pø/8192.         0       1       Uses internal clock. Counts at rising edge of Pø/8192.         0       1       Uses internal clock. Counts at rising edge of Pø/1024.         1       0       0       Uses internal clock. Counts at rising edge of Pø/1024.         1       0       0       —       Counts at TCNT_0 compare match A*.         All       1       0       1       —       Setting prohibited         1       1       0       —       —       Setting prohibited         1       1       0       —       —       Setting prohibited         1       1       1       —       —       Setting prohibited         Note:       *       If the clock input of channel 4 is the TCNT_1 overflow signal and that of channel 5 i	1       0       Odds include cock. Counts at failing edge of Pe/32.         0       1       1       Uses internal clock. Counts at failing edge of Pe/32.         0       1       1       Uses internal clock. Counts at rising edge of Pe/32.         0       1       Uses internal clock. Counts at rising edge of Pe/1024.         1       0       Uses internal clock. Counts at rising edge of Pe/1024.         1       0       0       —         1       0       0       —         1       0       0       —         1       0       0       —         1       0       0       —         1       0       0       —         2       0       1       Uses internal clock. Counts at rising edge of Pe/1024.         1       0       0       —       Counts at TCNT_0 compare match A*.         All       1       0       —       —         1       1       0       —       Setting prohibited         1       1       1       —       —         Setting prohibited					1	0	Uses internal clock. Counts at falling edge of $P\phi/64$
0       1       1       0       0       Uses internal clock. Counts at rising edge of P\u024.         0       1       Uses internal clock. Counts at rising edge of P\u024.       1       0       Uses internal clock. Counts at rising edge of P\u024.         1       0       0       Uses internal clock. Counts at rising edge of P\u024.       1       1       Uses internal clock. Counts at rising edge of P\u024.         1       0       0       -       -       Counts at TCNT_0 compare match A*.         All       1       0       1       -       Setting prohibited         1       1       0       -       -       Setting prohibited         1       1       0       -       -       Setting prohibited         1       1       1       -       -       Setting prohibited         1       1       1       -       -       Setting prohibited         Note:       *       If the clock input of channel 4 is the TCNT_1 overflow signal and that of channel 5 is the TCNT_0 compare m signal, no incrementing clock is generated. Do not use this setting.	Image: Note:     Image: Note: The Note: State in the Note: Counts at training edge of PW/8192.       0     1     1     Uses internal clock. Counts at trising edge of PW/1024.       1     0     0     —       1     0     0     —       1     0     0     —       1     0     0     —       1     0     0     —       1     0     1     —       1     0     1     —       1     0     1     —       1     0     1     —       1     1     0     —       1     1     0     —       1     1     0     —       1     1     1     —       2     Setting prohibited       1     1     1       1     1     1       1     1     1       1     1     0       1     1     1       2     Setting prohibited       1     1     1       1     1     1       1     1     1       1     1     1       1     1     1       1     1     1       1					1	1	Uses internal clock. Counts at falling edge of P
0       1       0	0       1       1       0		0	1	1	0	0	Uses internal clock. Counts at raining cage of \$\000000000000000000000000000000000000
0       1       0       0 Uses internal clock. Counts at rising edge of Pol.         1       0       0        Counts at TCNT_0 compare match A*.         All       1       0       1        Setting prohibited         1       1       0        Setting prohibited         1       1       1        Setting prohibited         Note: *       If the clock input of channel 4 is the TCNT_1 overflow signal and that of channel 5 is the TCNT_0 compare m signal, no incrementing clock is generated. Do not use this setting.	Image: Construction of the intervence of the inte		0		I	0	1	Uses internal clock. Counts at rising edge of $P = \frac{1}{\sqrt{1024}}$
Image:	1       0					1	0	Uses internal clock. Counts at rising edge of $P\phi$ 1024.
Image: Internal clock. Counts at failing edge of P@/1024.         Image: Internal clock. Counts at TCNT_0 compare match A*.         All       Image: Internal clock. Counts at TCNT_0 compare match A*.         All       Image: Internal clock. Counts at TCNT_0 compare match A*.         All       Image: Internal clock. Counts at TCNT_0 compare match A*.         All       Image: Internal clock. Counts at TCNT_0 compare match A*.         All       Image: Internal clock. Counts at TCNT_0 compare match A*.         All       Image: Internal clock. Counts at TCNT_0 compare match A*.         All       Image: Internal clock. Counts at TCNT_0 compare match A*.         All       Image: Internal clock. Counts at TCNT_0 compare match A*.         All       Image: Internal clock. Counts at TCNT_0 compare match A*.         Mote:       *       If the clock input of channel 4 is the TCNT_1 overflow signal and that of channel 5 is the TCNT_0 compare m signal, no incrementing clock is generated. Do not use this setting.	Image:					1	1	Uses internal clock. Counts at falling edge of $P\phi$ .
I       0       0        Counts at TCNT_0 compare match A*.         All       1       0       1        Setting prohibited         1       1       0        Setting prohibited         1       1       1        Setting prohibited         Note:       *       If the clock input of channel 4 is the TCNT_1 overflow signal and that of channel 5 is the TCNT_0 compare m signal, no incrementing clock is generated. Do not use this setting.	1       0       0		4	0	0	I	I	Oses Internal Clock. Counts at failing edge of Por 1024.
All       1       0       1	All <u>1 0 1 — Executing prohibited</u> <u>1 1 0 — Setting prohibited</u> Note: * If the clock input of channel 4 is the TCNT_1 overflow signal and that of channel 5 is the TCNT_0 compare mate signal, no incrementing clock is generated. Do not use this setting.	A 11	1	0	0	_	_	
1       1       0       —       Setting prohibited         1       1       1       —       —       Setting prohibited         Note:       *       If the clock input of channel 4 is the TCNT_1 overflow signal and that of channel 5 is the TCNT_0 compare m signal, no incrementing clock is generated. Do not use this setting.	1       1       0        Setting prohibited         Note:       *       If the clock input of channel 4 is the TCNT_1 overflow signal and that of channel 5 is the TCNT_0 compare mate signal, no incrementing clock is generated. Do not use this setting.	All	1	0	1	_	_	
<ul> <li>1 1 1 — Setting prohibited</li> <li>Note: * If the clock input of channel 4 is the TCNT_1 overflow signal and that of channel 5 is the TCNT_0 compare n signal, no incrementing clock is generated. Do not use this setting.</li> </ul>	1       1       1       -		1	1	0	_	_	Setting prohibited
Note: * If the clock input of channel 4 is the TCNT_1 overflow signal and that of channel 5 is the TCNT_0 compare n signal, no incrementing clock is generated. Do not use this setting.	Note: * If the clock input of channel 4 is the TCNT_1 overflow signal and that of channel 5 is the TCNT_0 compare mate signal, no incrementing clock is generated. Do not use this setting.		1	1	1	_	—	Setting prohibited
		All Note: *	1 1 1 If the cl signal,	0 1 1 lock inp no incre	0 1 0 1 ut of ch ementir	mannel 4	is gene	Counts at TCNT_0 compare match A*. Setting prohibited Setting prohibited TCNT_1 overflow signal and that of channel 5 is the TCNT_0 compare n erated. Do not use this setting.



### [After Change]

<b>Fable 12.2(1)</b>	Clock Input to TCNT and Count Condition (Unit 0)
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		TCR		тс	CR	
Channel	Bit 2 CKS2	Bit 1 CKS1	Bit 0 CKS0	Bit 1 ICKS1	Bit 0 ICKS0	- Description
TMR_0	0	0	0		_	Clock input prohibited
	0	0	1	0	0	Uses internal clock. Counts at rising edge of Pø/8.
				0	1	Uses internal clock. Counts at rising edge of Pø/2.
				1	0	Uses internal clock. Counts at falling edge of Pø/8.
				1	1	Uses internal clock. Counts at falling edge of Pø/2.
	0	1	0	0	0	Uses internal clock. Counts at rising edge of Pø/64.
				0	1	Uses internal clock. Counts at rising edge of Pø/32.
				1	0	Uses internal clock. Counts at falling edge of Pø/64.
				1	1	Uses internal clock. Counts at falling edge of Pø/32.
	0	1	1	0	0	Uses internal clock. Counts at rising edge of Pø/8192.
				0	1	Uses internal clock. Counts at rising edge of Pø/1024.
				1	0	Uses internal clock. Counts at falling edge of Pø/8192.
				1	1	Uses internal clock. Counts at falling edge of Pø/1024.
	1	0	0	_	_	Counts at TCNT_1 overflow signal* <sup>1</sup> .
TMR_1	0	0	0	—	_	Clock input prohibited
	0	0	1	0	0	Uses internal clock. Counts at rising edge of $P\phi/8$ .
				0	1	Uses internal clock. Counts at rising edge of Pø/2.
				1	0	Uses internal clock. Counts at falling edge of Pø/8.
				1	1	Uses internal clock. Counts at falling edge of Pø/2.
	0	1	0	0	0	Uses internal clock. Counts at rising edge of Po/64.
				0	1	Uses internal clock. Counts at rising edge of $P\phi/32$ .
				1	0	Uses internal clock. Counts at falling edge of Pø/64.
				1	1	Uses internal clock. Counts at falling edge of $P\phi/32$ .
	0	1	1	0	0	Uses internal clock. Counts at rising edge of Pø/8192.
				0	1	Uses internal clock. Counts at rising edge of P
				1	0	Uses internal clock. Counts at falling edge of Pø/8192.
				1	1	Uses internal clock. Counts at falling edge of $P\phi/1024$ .
	1	0	0	_	_	Counts at TCNT_0 compare match A* <sup>1</sup> .
All	1	0	1	_	_	Uses external clock. Counts at rising edge* <sup>2</sup> .
	1	1	0	_	_	Uses external clock. Counts at falling edge* <sup>2</sup> .
	1	1	1	_	_	Uses external clock. Counts at both rising and falling edges <sup>*2</sup> .

Notes: 1. If the clock input of channel 0 is the TCNT\_1 overflow signal and that of channel 1 is the TCNT\_0 compare match signal, no incrementing clock is generated. Do not use this setting.

2. Descriptions omitted (No change).



Sit 2 E SKS2 C	<b>Bit 1</b> CKS1	Bit 0 CKS0	Bit 1		
C C	)		ICKS1	Bit 0	Description
C	)	0	_	_	Clock input prohibited
		1	0	0	Uses internal clock. Counts at rising edge of P
			0	1	Uses internal clock. Counts at rising edge of P
			1	0	Uses internal clock. Counts at falling edge of Pø/8.
			1	1	Uses internal clock. Counts at falling edge of P
1	1	0	0	0	Uses internal clock. Counts at rising edge of P
			0	1	Uses internal clock. Counts at rising edge of P
			1	0	Uses internal clock. Counts at falling edge of P
			1	1	Uses internal clock. Counts at falling edge of P
1	1	1	0	0	Uses internal clock. Counts at rising edge of P
			0	1	Uses internal clock. Counts at rising edge of Pø/1024.
			1	0	Uses internal clock. Counts at falling edge of Pø/8192.
			1	1	Uses internal clock. Counts at falling edge of Po/1024.
C	)	0	_	_	Counts at TCNT 3 overflow signal* <sup>1</sup> .
C	)	0	_	_	Clock input prohibited
C	)	1	0	0	Uses internal clock. Counts at rising edge of P
			0	1	Uses internal clock. Counts at rising edge of Po/2.
			1	0	Uses internal clock. Counts at falling edge of Po/8.
			1	1	Uses internal clock. Counts at falling edge of Po/2.
1	1	0	0	0	Uses internal clock. Counts at rising edge of Ph/64
	•	U U	0	1	Uses internal clock. Counts at rising edge of Pø/32
			1	0	Uses internal clock. Counts at falling edge of Ph/64
			1	1	Uses internal clock. Counts at falling edge of Pø/32
1	1	1	0	0	Uses internal clock. Counts at rising edge of Ph/8192
	•		0	1	Uses internal clock. Counts at rising edge of Po/1024
			1	0	Uses internal clock. Counts at falling edge of P    //1024.
			1	1	Uses internal clock. Counts at falling edge of P\$/1024
0	۱	0	-	_	Counts at TCNT 2 compare match $A^{*1}$
	, ו	1	_	_	Uses external clock Counts at rising edge* <sup>2</sup>
1	1	0			Less external clock. Counts at falling edge*2
1	1	1	_	_	Uses external clock. Counts at both rising and falling
1	1	1	_		edges* <sup>2</sup> .
	(( () () () () () () () () () () () () (	1 0 0 0 1 1 1 1 the clock inp gnal, no incre	1         1           0         0           0         0           0         1           1         0           1         1           0         0           1         1           1         0           1         1           1         0           1         1           1         0           1         1           1         0           1         1           1         1           1         1           1         1           1         1	$\begin{array}{c c} 0 & 0 \\ \hline 1 \\ \hline 1 & 1 \\ 0 \\ 0 \\ \hline 1 \\ \hline 1 \\ \hline 1 \\ \hline 0 \\ 0 \\ \hline 1 \\ \hline 1 \\ \hline 1 \\ \hline 0 \\ 0 \\ \hline 0 \\ \hline 1 \\ \hline 1 \\ \hline 1 \\ 0 \\ \hline 1 \\ \hline 1 \\ \hline 1 \\ 0 \\ \hline 0 \\ \hline 1 \\ \hline 1 \\ \hline 1 \\ \hline 1 \\ 0 \\ \hline 1 \hline$	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$

TRR_1         TCR Brd 2         TCR Brd 3         TCR Brd 3         Brd 3         FCR 3         FCR 3         Brd 3	Table 12.3	<b>B(1)</b>	Cloc	k Inpu	it to TC	CNT an	d Count Condition (Unit 2)
Channel         Citiz         Bit I         Bit O         Description           TMR_4         0         0         0			TCR		тс	CR	
TMR_4         0         0	Channel	Bit 2 CKS2	Bit 1 CKS1	Bit 0 CKS0	Bit 1	Bit 0	- Description
0         0         1         0         0         Uses internal clock. Counts at rising edge of P4/8.           1         0         Uses internal clock. Counts at rising edge of P4/8.         1         1         Uses internal clock. Counts at rising edge of P4/8.           0         1         0         0         Uses internal clock. Counts at rising edge of P4/8.           0         1         0         0         Uses internal clock. Counts at rising edge of P4/8.           0         1         0         Uses internal clock. Counts at rising edge of P4/8.           0         1         Uses internal clock. Counts at rising edge of P4/8.           0         1         Uses internal clock. Counts at rising edge of P4/1024.           1         1         Uses internal clock. Counts at rising edge of P4/1024.           1         0         0	TMR 4	0	0	0	_	_	Clock input prohibited
0         1         Uses internal clock. Counts at rising edge of Pe/8.           0         1         0         Uses internal clock. Counts at rising edge of Pe/82.           0         1         0         0         Uses internal clock. Counts at rising edge of Pe/82.           1         0         Uses internal clock. Counts at rising edge of Pe/82.         1         0           1         0         Uses internal clock. Counts at rising edge of Pe/82.         1         0           0         1         1         Uses internal clock. Counts at rising edge of Pe/82.         1           0         1         0         Uses internal clock. Counts at rising edge of Pe/82.         1           0         1         Uses internal clock. Counts at rising edge of Pe/82.         1         1           1         0         0         -         Counts at rising edge of Pe/82.         1           1         0         0         -         Counts at rising edge of Pe/82.         1           1         0         0         -         Counts at rising edge of Pe/82.         1           1         Uses internal clock. Counts at rising edge of Pe/82.         1         0         Uses internal clock. Counts at rising edge of Pe/82.           1         0         0 <td< td=""><td>_</td><td>0</td><td>0</td><td>1</td><td>0</td><td>0</td><td>Uses internal clock. Counts at rising edge of <math>P\phi/8</math>.</td></td<>	_	0	0	1	0	0	Uses internal clock. Counts at rising edge of $P\phi/8$ .
Image: space of the system         Image: space of the system           0         1         0         Uses internal clock. Counts at failing edge of P4/2.           0         1         Uses internal clock. Counts at failing edge of P4/32.           1         0         Uses internal clock. Counts at failing edge of P4/32.           1         1         Uses internal clock. Counts at failing edge of P4/32.           0         1         Uses internal clock. Counts at failing edge of P4/32.           0         1         Uses internal clock. Counts at failing edge of P4/32.           0         1         Uses internal clock. Counts at failing edge of P4/32.           1         0         Uses internal clock. Counts at failing edge of P4/32.           1         0         Uses internal clock. Counts at failing edge of P4/32.           1         0         0         Uses internal clock. Counts at failing edge of P4/32.           1         0         0         Uses internal clock. Counts at failing edge of P4/32.           1         0         0         Uses internal clock. Counts at failing edge of P4/32.           1         0         Uses internal clock. Counts at failing edge of P4/32.           1         1         Uses internal clock. Counts at failing edge of P4/32.           1         1         Uses intern					0	1	Uses internal clock. Counts at rising edge of Po/2.
Image: second state of the second state of the second state state of the second state					1	0	Uses internal clock. Counts at falling edge of P
0       1       0       0       Uses internal clock. Counts at rising edge of P4/64.         0       1       Uses internal clock. Counts at rising edge of P4/32.         1       0       Uses internal clock. Counts at rising edge of P4/32.         0       1       1       Uses internal clock. Counts at rising edge of P4/32.         0       1       1       Uses internal clock. Counts at rising edge of P4/1024.         1       0       0       —       —         1       0       0       —       —         1       0       0       —       —         1       0       0       —       —         1       0       0       —       —         1       0       0       —       —         1       0       0       —       —         1       0       0       —       —         1       0       0       Uses internal clock. Counts at rising edge of P4/1024.         1       0       0       Uses internal clock. Counts at rising edge of P4/1024.         1       0       0       Uses internal clock. Counts at rising edge of P4/1024.         1       1       Uses internal clock. Counts at rising edge of P4/1024.					1	1	Uses internal clock. Counts at falling edge of P
0         1         Uses internal clock. Counts at rising edge of P4/32.           1         0         Uses internal clock. Counts at rising edge of P4/32.           0         1         1         Uses internal clock. Counts at rising edge of P4/32.           0         1         1         Uses internal clock. Counts at rising edge of P4/32.           0         1         0         0         Uses internal clock. Counts at rising edge of P4/1024.           1         0         0         —         Counts at rising edge of P4/1024.           1         0         0         —         Counts at rising edge of P4/1024.           1         0         0         —         Counts at rising edge of P4/1024.           1         0         0         —         Counts at rising edge of P4/1024.           1         0         0         Uses internal clock. Counts at rising edge of P4/1024.           1         1         Uses internal clock. Counts at rising edge of P4/1024.         1           0         1         0         0         Uses internal clock. Counts at rising edge of P4/1024.           1         1         Uses internal clock. Counts at rising edge of P4/1024.         1         0           1         0         0         Uses internal clock. Counts at rising e		0	1	0	0	0	Uses internal clock. Counts at rising edge of P
Image: space of the system           0         1         1         0         0         Uses internal clock. Counts at failing edge of PA/1024.           1         0         0         0         Uses internal clock. Counts at failing edge of PA/1024.           1         0         0					0	1	Uses internal clock. Counts at rising edge of P
Image: constraint of the set of					1	0	Uses internal clock. Counts at falling edge of P
0         1         1         0         0         Uses internal clock. Counts at rising edge of Pq/8192.         0         1         Uses internal clock. Counts at rising edge of Pq/1024.           1         0         0					1	1	Uses internal clock. Counts at falling edge of P
Image: state of the set of the s		0	1	1	0	0	Uses internal clock. Counts at rising edge of P
I         0         Uses internal clock. Counts at rising edge of PA:           1         0         0          -         Counts at rising edge of PA:1024.           TMR_5         0         0         0          -         Counts at rising edge of PA:024.           0         0         0          -         Clock input prohibited           0         0         1         Uses internal clock. Counts at rising edge of PA:0.           1         0         Uses internal clock. Counts at rising edge of PA:0.           1         1         Uses internal clock. Counts at rising edge of PA:0.           1         0         Uses internal clock. Counts at rising edge of PA:0.           1         0         Uses internal clock. Counts at rising edge of PA:0.           1         0         Uses internal clock. Counts at rising edge of PA:0.           1         1         Uses internal clock. Counts at rising edge of PA:02.           0         1         Uses internal clock. Counts at rising edge of PA:02.           1         0         0         Uses internal clock. Counts at rising edge of PA:024.           1         0         0          Counts at rising edge of PA:024.           1         0         0         -					0	1	Uses internal clock. Counts at rising edge of P
Image: 1         1         Uses internal clock. Counts at falling edge of Po/1024.           TMR_5         0         0         -         -         Clock in at TCNT_5 overflow signal*.           TMR_5         0         0         0         -         -         Clock input prohibited           0         0         0         1         0         0         Uses internal clock. Counts at rising edge of Po/42.           1         1         Uses internal clock. Counts at rising edge of Po/42.         1         1         Uses internal clock. Counts at falling edge of Po/42.           0         1         0         0         Uses internal clock. Counts at failing edge of Po/42.           0         1         Uses internal clock. Counts at failing edge of Po/42.         1         1         Uses internal clock. Counts at failing edge of Po/42.           1         0         0         Uses internal clock. Counts at failing edge of Po/42.         1         1         Uses internal clock. Counts at failing edge of Po/42.           0         1         Uses internal clock. Counts at failing edge of Po/42.         1         1         Uses internal clock. Counts at failing edge of Po/42.           1         0         -         Counts at TCNT_4 counts at failing edge of Po/4024.         1         1 <th1< th="">         Uses internal clock.</th1<>					1	0	Uses internal clock. Counts at rising edge of Pø.
Image: 1         0         0					1	1	Uses internal clock. Counts at falling edge of P
TMR_5         0         0		1	0	0	_	_	Counts at TCNT_5 overflow signal*.
0       0       1       0       Uses internal clock. Counts at rising edge of Pig/8.         1       0       Uses internal clock. Counts at falling edge of Pig/8.         1       0       Uses internal clock. Counts at falling edge of Pig/8.         0       1       0       Uses internal clock. Counts at falling edge of Pig/8.         0       1       0       Uses internal clock. Counts at rising edge of Pig/8.         0       1       0       Uses internal clock. Counts at rising edge of Pig/8.         1       0       Uses internal clock. Counts at rising edge of Pig/8.         1       0       Uses internal clock. Counts at rising edge of Pig/8.         1       0       Uses internal clock. Counts at rising edge of Pig/82.         0       1       Uses internal clock. Counts at rising edge of Pig/82.         0       1       Uses internal clock. Counts at rising edge of Pig/1024.         1       0       Uses internal clock. Counts at rising edge of Pig/1024.         1       0       Uses internal clock. Counts at rising edge of Pig/1024.         1       0       Uses internal clock. Counts at rising edge of Pig/1024.         1       0       -       Counts at rising edge of Pig/1024.         1       1       -       Setting prohibited	TMR_5	0	0	0	_	_	Clock input prohibited
0       1       Uses internal clock. Counts at fising edge of Pe/2.         1       0       Uses internal clock. Counts at failing edge of Pe/2.         0       1       0       0         0       1       Uses internal clock. Counts at failing edge of Pe/2.         0       1       Uses internal clock. Counts at failing edge of Pe/2.         1       0       Uses internal clock. Counts at failing edge of Pe/2.         1       0       Uses internal clock. Counts at failing edge of Pe/2.         1       0       Uses internal clock. Counts at failing edge of Pe/2.         1       1       Uses internal clock. Counts at failing edge of Pe/2.         0       1       1       Uses internal clock. Counts at failing edge of Pe/32.         0       1       1       Uses internal clock. Counts at failing edge of Pe/32.         0       1       Uses internal clock. Counts at failing edge of Pe/32.         1       0       Uses internal clock. Counts at failing edge of Pe/1024.         1       1       Uses internal clock. Counts at failing edge of Pe/1024.         1       1       0       -         1       0       -       Setting prohibited         1       1       -       Setting prohibited         1       1		0	0	1	0	0	Uses internal clock. Counts at rising edge of P
1       0       Uses internal clock. Counts at falling edge of Pty/8.         0       1       0       Uses internal clock. Counts at falling edge of Pty/64.         0       1       Uses internal clock. Counts at falling edge of Pty/64.         1       0       Uses internal clock. Counts at falling edge of Pty/64.         1       0       Uses internal clock. Counts at falling edge of Pty/64.         1       1       Uses internal clock. Counts at falling edge of Pty/64.         1       0       Uses internal clock. Counts at falling edge of Pty/64.         1       0       Uses internal clock. Counts at falling edge of Pty/64.         1       0       Uses internal clock. Counts at falling edge of Pty/64.         1       0       Uses internal clock. Counts at falling edge of Pty/64.         1       0       Uses internal clock. Counts at falling edge of Pty/1024.         1       0       Uses internal clock. Counts at falling edge of Pty/1024.         1       0       -       Counts at falling edge of Pty/1024.         1       0       -       Setting prohibited         1       1       -       Setting prohibited         1       1       -       Setting prohibited         1       1       -       Setting prohibited					0	1	Uses internal clock. Counts at rising edge of P
1       1       Uses internal clock. Counts at falling edge of P\p/2.         0       1       0       0       Uses internal clock. Counts at rising edge of P\p/64.         1       0       Uses internal clock. Counts at falling edge of P\p/64.         1       1       Uses internal clock. Counts at falling edge of P\p/64.         1       1       Uses internal clock. Counts at falling edge of P\p/64.         0       1       1       Uses internal clock. Counts at falling edge of P\p/64.         1       0       0       Uses internal clock. Counts at rising edge of P\p/64.         0       1       Uses internal clock. Counts at rising edge of P\p/64.         1       0       Uses internal clock. Counts at rising edge of P\p/1024.         1       0       Uses internal clock. Counts at falling edge of P\p/1024.         1       0       -       Counts at falling edge of P\p/1024.         1       0       -       Setting prohibited         1       1       D       D       Setting a					1	0	Uses internal clock. Counts at falling edge of Pø/8.
0       1       0       0       Uses internal clock. Counts at rising edge of Pq/64.         1       0       Uses internal clock. Counts at falling edge of Pq/62.         1       1       Uses internal clock. Counts at falling edge of Pq/64.         1       1       Uses internal clock. Counts at falling edge of Pq/64.         0       1       1       Uses internal clock. Counts at falling edge of Pq/61.         0       1       0       Uses internal clock. Counts at falling edge of Pq/61.         0       1       Uses internal clock. Counts at falling edge of Pq/61.         1       0       0       Uses internal clock. Counts at falling edge of Pq/6124.         1       0       0       —       Counts at TCNT_4 compare match A*.         All       1       0       —       —       Setting prohibited         1       1       0       —       —       Setting prohibited         1       1       1       —       —       Setting prohibited         1       1       1       —       —       Setting prohibited         Notes: *       If the clock input of channel 4 is the TCNT_5 overflow signal and that of channel 5 is the signal, no incrementing clock is generated. Do not use this setting.					1	1	Uses internal clock. Counts at falling edge of Pø/2.
0       1       Uses internal clock. Counts at rising edge of Pe/32.         1       0       Uses internal clock. Counts at falling edge of Pe/64.         1       1       Uses internal clock. Counts at falling edge of Pe/192.         0       1       1       Uses internal clock. Counts at rising edge of Pe/192.         0       1       Uses internal clock. Counts at rising edge of Pe/1024.         1       0       Uses internal clock. Counts at rising edge of Pe/1024.         1       0       Uses internal clock. Counts at rising edge of Pe/1024.         1       0       0       —         1       0       Uses internal clock. Counts at rising edge of Pe/1024.         1       0       0       —         1       0       0       —         1       0       0       —         1       0       0       —         1       1       0       —         1       1       —       —         Setting prohibited       1       1       1         1       1       1       —       —         Notes:       *       If the clock input of channel 4 is the TCNT_5 overflow signal and that of channel 5 is the TCNT_4 compare matorsignal, no incrementing clock is generated. Do not		0	1	0	0	0	Uses internal clock. Counts at rising edge of P
1       0       Uses internal clock. Counts at falling edge of P\p/64.         1       1       Uses internal clock. Counts at fisling edge of P\p/8192.         0       1       1       Uses internal clock. Counts at rising edge of P\p/1024.         1       0       Uses internal clock. Counts at rising edge of P\p/1024.         1       0       Uses internal clock. Counts at rising edge of P\p/1024.         1       0       Uses internal clock. Counts at rising edge of P\p/1024.         1       0       0       —         1       0       0       —         1       0       0       —         1       0       0       —         1       0       1       —         1       0       1       —         2       1       0       —         3       1       0       —         4       1       1       —       —         5       Setting prohibited					0	1	Uses internal clock. Counts at rising edge of P
1       1       Uses internal clock. Counts at falling edge of Pe/8192.         0       1       Uses internal clock. Counts at rising edge of Pe/8192.         0       1       Uses internal clock. Counts at rising edge of Pe/1024.         1       0       0       Uses internal clock. Counts at falling edge of Pe/1024.         1       0       0					1	0	Uses internal clock. Counts at falling edge of Pø/64.
0       1       1       0       0       Uses internal clock. Counts at rising edge of Pq/8192.         0       1       Uses internal clock. Counts at rising edge of Pq/1024.       1       0       Uses internal clock. Counts at rising edge of Pq/1024.         1       0       0       -       -       Counts at Toking edge of Pq/1024.         1       0       0       -       -       Counts at Toking edge of Pq/1024.         1       0       0       -       -       Counts at Toking edge of Pq/1024.         1       0       0       -       -       Counts at Toking edge of Pq/1024.         1       0       0       -       -       Counts at Toking edge of Pq/1024.         1       1       0       -       -       Setting prohibited         1       1       0       -       -       Setting prohibited         1       1       1       -       -       Setting prohibited         Notes: *       If the clock input of channel 4 is the TCNT_5 overflow signal and that of channel 5 is the TCNT_4 compare match as signal, no incrementing clock is generated. Do not use this setting.					1	1	Uses internal clock. Counts at falling edge of Pø/32.
0       1       Uses internal clock. Counts at rising edge of P		0	1	1	0	0	Uses internal clock. Counts at rising edge of P
1       0       Uses internal clock. Counts at rising edge of Po.         1       1       Uses internal clock. Counts at falling edge of Po/1024.         All       1       0       -       -         All       1       0       1       -       Setting prohibited         1       1       0       -       -       Setting prohibited         1       1       1       -       -       Setting prohibited         Notes:       *       If the clock input of channel 4 is the TCNT_5 overflow signal and that of channel 5 is the TCNT_4 compare match signal, no incrementing clock is generated. Do not use this setting.					0	1	Uses internal clock. Counts at rising edge of P
1       1       Uses internal clock. Counts at falling edge of P\\$/1024.         All       1       0       -       -       Counts at TCNT_4 compare match A*.         All       1       0       1       -       -       Setting prohibited         1       1       0       -       -       Setting prohibited         1       1       0       -       -       Setting prohibited         Notes: *       If the clock input of channel 4 is the TCNT_5 overflow signal and that of channel 5 is the TCNT_4 compare match signal, no incrementing clock is generated. Do not use this setting.					1	0	Uses internal clock. Counts at rising edge of Pø.
1       0       0					1	1	Uses internal clock. Counts at falling edge of Pø/1024.
All       1       0       1		1	0	0	_	_	Counts at TCNT_4 compare match A*.
1       1       0	All	1	0	1	_	_	Setting prohibited
1       1       -       Setting prohibited         Notes: *       If the clock input of channel 4 is the TCNT_5 overflow signal and that of channel 5 is the TCNT_4 compare mate signal, no incrementing clock is generated. Do not use this setting.		1	1	0	_	_	Setting prohibited
Notes: * If the clock input of channel 4 is the TCNT_5 overflow signal and that of channel 5 is the TCNT_4 compare mate signal, no incrementing clock is generated. Do not use this setting.		1	1	1		_	Setting prohibited
		signal,	no incre	ementir	ng clock	is gene	erated. Do not use this setting.



The set of	Table 12.3	3(2)	Cloc	k Inpu	it to TC	CNT an	d Count Condition (Unit 3)
Channel         EK2         EK14         EK16         EKK80         Description           TMR_6         0         0         0			TCR		тс	CR	
TMR_6         0         0         0         0         0         Uses internal clock. Counts at rising edge of Pw2.           1         0         Uses internal clock. Counts at rising edge of Pw2.         1         0         Uses internal clock. Counts at rising edge of Pw2.           0         1         Uses internal clock. Counts at rising edge of Pw2.         1         0         Uses internal clock. Counts at rising edge of Pw2.           0         1         Uses internal clock. Counts at rising edge of Pw3.         1         0         Uses internal clock. Counts at rising edge of Pw3.           0         1         Uses internal clock. Counts at rising edge of Pw3.         1         0         Uses internal clock. Counts at rising edge of Pw164.           1         0         Uses internal clock. Counts at rising edge of Pw1024.         1         0         Uses internal clock. Counts at rising edge of Pw1024.           1         0         0         -         Clock input prohibited         1         Uses internal clock. Counts at rising edge of Pw1024.           1         0         0         -         Clock input prohibited         1         Uses internal clock. Counts at rising edge of Pw1024.           1         0         0         Uses internal clock. Counts at rising edge of Pw1024.         1         0         Uses internal clock. Counts at rising e	Channel	Bit 2 CKS2	Bit 1 CKS1	Bit 0 CKS0	Bit 1	Bit 0	- Description
0         0         1         0         0         Uses internal clock. Counts at rising edge of Pk/8.           1         0         Uses internal clock. Counts at rising edge of Pk/8.         1         1         Uses internal clock. Counts at rising edge of Pk/8.           0         1         0         0         Uses internal clock. Counts at rising edge of Pk/8.           0         1         0         0         Uses internal clock. Counts at rising edge of Pk/8.           0         1         Uses internal clock. Counts at rising edge of Pk/8.         1           0         1         Uses internal clock. Counts at rising edge of Pk/8.           0         1         1         Uses internal clock. Counts at rising edge of Pk/8.           0         1         1         Uses internal clock. Counts at rising edge of Pk/02.           1         0         0         —         Counts at rising edge of Pk/02.           1         0         0         —         Counts at rising edge of Pk/8.           1         0         0         —         Counts at rising edge of Pk/8.           0         1         Uses internal clock. Counts at rising edge of Pk/8.         Counts at rising edge of Pk/8.           1         1         Uses internal clock. Counts at rising edge of Pk/8.         Counts	TMR 6	0	0	0	_	_	Clock input prohibited
Image: space of the system         Image: space of the system           0         1         Uses internal clock. Counts at failing edge of Poi/2.           0         1         0         Uses internal clock. Counts at failing edge of Poi/2.           0         1         Uses internal clock. Counts at failing edge of Poi/2.           1         0         Uses internal clock. Counts at failing edge of Poi/2.           1         0         Uses internal clock. Counts at failing edge of Poi/2.           0         1         Uses internal clock. Counts at failing edge of Poi/2.           0         1         Uses internal clock. Counts at failing edge of Poi/2.           0         1         Uses internal clock. Counts at failing edge of Poi/2.           1         0         Uses internal clock. Counts at failing edge of Poi/2.           1         1         Uses internal clock. Counts at failing edge of Poi/2.           1         1         Uses internal clock. Counts at failing edge of Poi/2.           1         1         Uses internal clock. Counts at failing edge of Poi/2.           1         1         Uses internal clock. Counts at failing edge of Poi/2.           1         1         Uses internal clock. Counts at failing edge of Poi/2.           1         1         Uses internal clock. Counts at failing edge of Poi/2. <td></td> <td>0</td> <td>0</td> <td>1</td> <td>0</td> <td>0</td> <td>Uses internal clock. Counts at rising edge of <math>P\phi/8</math>.</td>		0	0	1	0	0	Uses internal clock. Counts at rising edge of $P\phi/8$ .
Image: space of the system         Image: space of the system           0         1         0         0         Uses internal clock. Counts at failing edge of P42.           0         1         0         0         Uses internal clock. Counts at failing edge of P432.           1         0         Uses internal clock. Counts at failing edge of P432.         1         0           0         1         Uses internal clock. Counts at failing edge of P432.         1         0           0         1         Uses internal clock. Counts at failing edge of P432.         1         0           0         1         Uses internal clock. Counts at failing edge of P432.         1         0           1         0         0         Uses internal clock. Counts at failing edge of P432.         1           1         0         0         -         Counts at failing edge of P432.         1           1         Uses internal clock. Counts at failing edge of P432.         1         0         Uses internal clock. Counts at failing edge of P432.           1         0         0         -         Counts at failing edge of P432.         1           1         0         0         Uses internal clock. Counts at failing edge of P432.         1           0         1         Uses internal clo					0	1	Uses internal clock. Counts at rising edge of Po/2.
Image: Non-state in the state of the state in t					1	0	Uses internal clock. Counts at falling edge of Po/8.
0         1         0         0         Uses internal clock. Counts at rising edge of P4/64.           1         0         Uses internal clock. Counts at rising edge of P4/64.           1         1         Uses internal clock. Counts at rising edge of P4/64.           1         1         Uses internal clock. Counts at rising edge of P4/64.           1         1         Uses internal clock. Counts at rising edge of P4/64.           1         0         0         Uses internal clock. Counts at rising edge of P4/64.           1         0         0					1	1	Uses internal clock. Counts at falling edge of P
Image: sinternal clock.         Counts at rising edge of P4/32.           1         0         Uses internal clock. Counts at rising edge of P4/32.           0         1         1         Uses internal clock. Counts at rising edge of P4/32.           0         1         1         Uses internal clock. Counts at rising edge of P4/32.           1         0         0         Uses internal clock. Counts at rising edge of P4/32.           1         0         0         Uses internal clock. Counts at rising edge of P4/32.           1         0         0		0	1	0	0	0	Uses internal clock. Counts at rising edge of P
Image: space of the s					0	1	Uses internal clock. Counts at rising edge of P
Image: constraint of the					1	0	Uses internal clock. Counts at falling edge of P
0         1         1         0         0         Uses internal clock. Counts at rising edge of P4/8192.           1         0         Uses internal clock. Counts at rising edge of P4/01024.           1         0         0					1	1	Uses internal clock. Counts at falling edge of P
0         1         Uses internal clock. Counts at rising edge of Po/1024.           1         0         Uses internal clock. Counts at rising edge of Po/1024.           1         0         0         -           1         0         0         -         -           1         0         0         -         -         Clock input prohibited           1         0         0         0         -         -         Clock input prohibited           0         0         1         Uses internal clock. Counts at rising edge of Po/2.         -           1         0         Uses internal clock. Counts at rising edge of Po/2.         -         -           0         1         Uses internal clock. Counts at rising edge of Po/2.         -         -           0         1         Uses internal clock. Counts at rising edge of Po/64.         -         -           0         1         Uses internal clock. Counts at rising edge of Po/1024.         -         -           1         0         0         Uses internal clock. Counts at rising edge of Po/1024.         -           0         1         Uses internal clock. Counts at rising edge of Po/1024.         -         -           1         0         0         -		0	1	1	0	0	Uses internal clock. Counts at rising edge of Pø/8192.
Image: 1         0         Uses internal clock. Counts at rising edge of P(x)           1         0         0         -         -         Counts at rising edge of P(x)           TIMP_7         0         0         0         -         -         Clock input prohibited           0         0         1         0         0         Uses internal clock. Counts at rising edge of P(x)           1         1         Uses internal clock. Counts at rising edge of P(x)         1         0         Uses internal clock. Counts at rising edge of P(x)           1         1         Uses internal clock. Counts at rising edge of P(x)         1         1         Uses internal clock. Counts at rising edge of P(x)           0         1         0         Uses internal clock. Counts at rising edge of P(x)         1         1         Uses internal clock. Counts at rising edge of P(x)           0         1         Uses internal clock. Counts at rising edge of P(x)         1         1         Uses internal clock. Counts at rising edge of P(x)           1         0         Uses internal clock. Counts at rising edge of P(x)         1         0         Uses internal clock. Counts at rising edge of P(x)           1         0         Uses internal clock. Counts at rising edge of P(x)         1         1         Uses internal clock. Counts at rising edge of P(x) </td <td></td> <td></td> <td></td> <td></td> <td>0</td> <td>1</td> <td>Uses internal clock. Counts at rising edge of P</td>					0	1	Uses internal clock. Counts at rising edge of P
1         1         Uses internal clock. Counts at falling edge of Pq/1024.           TMR_7         0         0         -         -         Counts at TCNT_7 overflow signal*.           TMR_7         0         0         1         0         0         Uses internal clock. Counts at rising edge of Pq/8.           0         0         1         Uses internal clock. Counts at rising edge of Pq/8.           1         0         Uses internal clock. Counts at failing edge of Pq/8.           1         Uses internal clock. Counts at failing edge of Pq/8.           0         1         Uses internal clock. Counts at failing edge of Pq/8.           1         0         Uses internal clock. Counts at failing edge of Pq/82.           0         1         Uses internal clock. Counts at failing edge of Pq/82.           0         1         Uses internal clock. Counts at failing edge of Pq/81.           1         Uses internal clock. Counts at rising edge of Pq/81.           0         Uses internal clock. Counts at rising edge of Pq/81.           1         Uses internal clock. Counts at rising edge of Pq/1024.           1         Uses internal clock. Counts at rising edge of Pq/1024.           1         Uses internal clock. Counts at rising edge of Pq/1024.           1         Uses internal clock. Counts at failing edge of Pq/1024.					1	0	Uses internal clock. Counts at rising edge of Pø.
1         0					1	1	Uses internal clock. Counts at falling edge of Po/1024.
TMR_7         0         0		1	0	0	_	_	Counts at TCNT 7 overflow signal*.
0       0       1       0       Uses internal clock. Counts at rising edge of P\#8.         1       0       Uses internal clock. Counts at falling edge of P\#2.         1       0       Uses internal clock. Counts at falling edge of P\#2.         0       1       0       Uses internal clock. Counts at falling edge of P\#2.         0       1       0       Uses internal clock. Counts at rising edge of P\#2.         0       1       0       Uses internal clock. Counts at rising edge of P\#2.         1       0       Uses internal clock. Counts at rising edge of P\#22.         0       1       1       Uses internal clock. Counts at falling edge of P\#22.         0       1       1       Uses internal clock. Counts at rising edge of P\#22.         0       1       1       Uses internal clock. Counts at rising edge of P\#22.         0       1       1       Uses internal clock. Counts at rising edge of P\#1024.         1       0       0       Uses internal clock. Counts at rising edge of P\#1024.         1       0       0       —       Counts at TCNT_6 compare match A*.         All       1       0       —       —       Setting prohibited         1       1       0       —       —       Setting prohibited	TMR 7	0	0	0	_	_	Clock input prohibited
0       1       Uses internal clock. Counts at rising edge of Pe/2.         1       0       Uses internal clock. Counts at falling edge of Pe/2.         0       1       0       0         0       1       Uses internal clock. Counts at falling edge of Pe/2.         0       1       Uses internal clock. Counts at falling edge of Pe/2.         1       0       Uses internal clock. Counts at falling edge of Pe/2.         1       0       Uses internal clock. Counts at falling edge of Pe/2.         1       0       Uses internal clock. Counts at falling edge of Pe/2.         0       1       Uses internal clock. Counts at falling edge of Pe/2.         0       1       Uses internal clock. Counts at falling edge of Pe/2.         0       1       Uses internal clock. Counts at falling edge of Pe/2.         1       0       Uses internal clock. Counts at falling edge of Pe/2.         1       1       Uses internal clock. Counts at falling edge of Pe/2.         1       1       Uses internal clock. Counts at falling edge of Pe/2.         1       0       -       Counts at TCNT_6 compare math A*.         All       1       0       -       Setting prohibited         1       1       -       -       Setting prohibited	-	0	0	1	0	0	Uses internal clock. Counts at rising edge of Pø/8.
1       0       Uses internal clock. Counts at falling edge of Pe/8.         1       1       Uses internal clock. Counts at falling edge of Pe/8.         0       1       0       0         0       1       0       0         0       1       Uses internal clock. Counts at falling edge of Pe/84.         1       0       Uses internal clock. Counts at falling edge of Pe/82.         1       0       Uses internal clock. Counts at falling edge of Pe/8192.         0       1       1       Uses internal clock. Counts at falling edge of Pe/8192.         0       1       1       Uses internal clock. Counts at rising edge of Pe/8192.         0       1       1       Uses internal clock. Counts at rising edge of Pe/8192.         0       1       0       Uses internal clock. Counts at rising edge of Pe/8192.         1       0       Uses internal clock. Counts at rising edge of Pe/8192.         1       1       Uses internal clock. Counts at rising edge of Pe/8102.         1       1       Uses internal clock. Counts at rising edge of Pe/8102.         1       1       -       Setting prohibited         1       1       -       -         1       1       -       -         1       1					0	1	Uses internal clock. Counts at rising edge of Po/2.
1       1       Uses internal clock. Counts at falling edge of P4/2.         0       1       0       0       Uses internal clock. Counts at rising edge of P4/84.         0       1       Uses internal clock. Counts at rising edge of P4/84.       1         1       1       Uses internal clock. Counts at falling edge of P4/84.       1         0       1       1       Uses internal clock. Counts at falling edge of P4/81.         0       1       1       Uses internal clock. Counts at falling edge of P4/81.         0       1       1       Uses internal clock. Counts at falling edge of P4/81.         0       1       Uses internal clock. Counts at rising edge of P4/81.         1       0       Uses internal clock. Counts at rising edge of P4/81.         1       0       Uses internal clock. Counts at rising edge of P4/81.         1       0       0       Uses internal clock. Counts at falling edge of P4/1024.         1       1       Uses internal clock. Counts at falling edge of P4/1024.         1       1       0       -         1       1       0       -         2       5       5       6         1       1       -       -         2       6       1       1					1	0	Uses internal clock. Counts at falling edge of Po/8.
0       1       0       0       Uses internal clock. Counts at rising edge of P4/64.         0       1       Uses internal clock. Counts at rising edge of P4/64.         1       0       Uses internal clock. Counts at falling edge of P4/64.         0       1       1       Uses internal clock. Counts at falling edge of P4/64.         0       1       1       Uses internal clock. Counts at falling edge of P4/61.         0       1       1       Uses internal clock. Counts at rising edge of P4/6192.         0       1       1       Uses internal clock. Counts at rising edge of P4/6192.         0       1       Uses internal clock. Counts at rising edge of P4/6192.         1       0       Uses internal clock. Counts at rising edge of P4/1024.         1       0       Uses internal clock. Counts at rising edge of P4/1024.         1       0       0					1	1	Uses internal clock. Counts at falling edge of Po/2.
0       1       Uses internal clock. Counts at rising edge of Pq/32.         1       0       Uses internal clock. Counts at falling edge of Pq/32.         0       1       1       Uses internal clock. Counts at falling edge of Pq/32.         0       1       1       Uses internal clock. Counts at falling edge of Pq/32.         0       1       1       Uses internal clock. Counts at rising edge of Pq/32.         0       1       1       Uses internal clock. Counts at rising edge of Pq/32.         1       0       0       Uses internal clock. Counts at rising edge of Pq/32.         1       0       0       Uses internal clock. Counts at rising edge of Pq/32.         1       0       0       —       Counts at rising edge of Pq/32.         1       0       0       —       Counts at rising edge of Pq/32.         1       1       0       —       —         2       1       1       Uses internal clock. Counts at rising edge of Pq/1024.         1       1       0       —       —         2       2       5       2         3       1       1       —       —         4       1       1       —       —         4       1       <		0	1	0	0	0	Uses internal clock. Counts at rising edge of Po/64.
1       0       Uses internal clock. Counts at failing edge of Po/64.         1       1       Uses internal clock. Counts at failing edge of Po/64.         0       1       1       Uses internal clock. Counts at failing edge of Po/64.         0       1       1       Uses internal clock. Counts at failing edge of Po/64.         0       1       Uses internal clock. Counts at rising edge of Po/1024.         1       0       Uses internal clock. Counts at rising edge of Po/1024.         1       0       0					0	1	Uses internal clock. Counts at rising edge of Pø/32.
1       1       Uses internal clock. Counts at falling edge of P\u032.         0       1       1       Uses internal clock. Counts at rising edge of P\u032.         0       1       Uses internal clock. Counts at rising edge of P\u032.         1       0       Uses internal clock. Counts at rising edge of P\u032.         1       0       Uses internal clock. Counts at rising edge of P\u032.         1       0       Uses internal clock. Counts at rising edge of P\u032.         1       0       0       —         Counts at TCNT_6 compare match A*.       All         1       0       1       —         All       1       0       1       —         1       1       0       —       —         Setting prohibited       —       —       Setting prohibited         1       1       1       —       —         Notes: *       If the clock input of channel 6 is the TCNT_7 overflow signal and that of channel 7 is the TCNT_6 compare match as signal, no incrementing clock is generated. Do not use this setting.					1	0	Uses internal clock. Counts at falling edge of Po/64.
0       1       1       0       0       Uses internal clock. Counts at rising edge of P\s/8192.         0       1       Uses internal clock. Counts at rising edge of P\s/1024.         1       0       Uses internal clock. Counts at rising edge of P\s/1024.         1       0       0        Counts at Trising edge of P\s/1024.         1       0       0        Counts at Trising edge of P\s/1024.         1       0       0        Counts at TCNT_6 compare match A*.         All       1       0        Setting prohibited         1       1       0        Setting prohibited         1       1        Setting prohibited         1       1        Setting prohibited         Notes: *       If the clock input of channel 6 is the TCNT_7 overflow signal and that of channel 7 is the TCNT_6 compare match signal, no incrementing clock is generated. Do not use this setting.					1	1	Uses internal clock. Counts at falling edge of Pø/32.
0     1     Uses internal clock. Counts at rising edge of P¢/1024.       1     0     Uses internal clock. Counts at rising edge of P¢.       1     1     Uses internal clock. Counts at falling edge of P¢/1024.       1     0     0     -       1     1     Uses internal clock. Counts at falling edge of P¢/1024.       1     0     0     -       2     1     0     -       3     1     0     -       4     1     0     -       1     1     -     -       2     1     1     -       3     1     1     -       4     1     1     -       5     8     8     1       4     1     1     -       5     9     9       4     1     1       4     -     -       5     9     9       4     1     1       4     -     -       5     9     9       6     1     1       6     1     1       7     9     9       8     1     1       9     1     1       9     1     1 <td></td> <td>0</td> <td>1</td> <td>1</td> <td>0</td> <td>0</td> <td>Uses internal clock. Counts at rising edge of Po/8192.</td>		0	1	1	0	0	Uses internal clock. Counts at rising edge of Po/8192.
1       0       Uses internal clock. Counts at rising edge of Po.         1       1       Uses internal clock. Counts at falling edge of Po/1024.         1       0       0       -         All       1       0       1         1       0       1       -         All       1       0       1         1       1       0       -         1       1       0       -         1       1       0       -         2       Setting prohibited       -         1       1       -       -         Setting prohibited       -       -         Notes: *       If the clock input of channel 6 is the TCNT_7 overflow signal and that of channel 7 is the signal, no incrementing clock is generated. Do not use this setting.					0	1	Uses internal clock. Counts at rising edge of Po/1024.
1       1       Uses internal clock. Counts at falling edge of Pi/1024.         1       0       0       -       -       Counts at TCNT_6 compare match A*.         All       1       0       1       -       -       Setting prohibited         1       1       0       -       -       Setting prohibited         1       1       1       -       -       Setting prohibited         Notes: *       If the clock input of channel 6 is the TCNT_7 overflow signal and that of channel 7 is the TCNT_6 compare mat signal, no incrementing clock is generated. Do not use this setting.       TCNT_6 compare mat					1	0	Uses internal clock. Counts at rising edge of Po.
1       0       0       -       Counts at TCNT_6 compare match A*.         All       1       0       1       -       Setting prohibited         1       1       0       -       Setting prohibited         1       1       1       -       Setting prohibited         1       1       1       -       Setting prohibited         Notes: *       If the clock input of channel 6 is the TCNT_7 overflow signal and that of channel 7 is the TCNT_6 compare match signal, no incrementing clock is generated. Do not use this setting.					1	1	Uses internal clock. Counts at falling edge of P   //1024.
All       1       0       1       -       Setting prohibited         1       1       0       -       -       Setting prohibited         1       1       1       -       -       Setting prohibited         Notes:       *       If the clock input of channel 6 is the TCNT_7 overflow signal and that of channel 7 is the TCNT_6 compare mathematication incrementing clock is generated. Do not use this setting.		1	0	0			Counts at TCNT 6 compare match $A^*$ .
1       1       0        Setting prohibited         1       1       1        Setting prohibited         Notes: *       If the clock input of channel 6 is the TCNT_7 overflow signal and that of channel 7 is the TCNT_6 compare mat signal, no incrementing clock is generated. Do not use this setting.	All	1	0	1		_	Setting prohibited
1       1       1       -       Setting prohibited         Notes: *       If the clock input of channel 6 is the TCNT_7 overflow signal and that of channel 7 is the TCNT_6 compare mat signal, no incrementing clock is generated. Do not use this setting.		1	1	0	_	_	Setting prohibited
Notes: * If the clock input of channel 6 is the TCNT_7 overflow signal and that of channel 7 is the TCNT_6 compare mat signal, no incrementing clock is generated. Do not use this setting.		1	1	1	_	_	Setting prohibited
		signal,	no incre	ementir	ng clock	is gene	erated. Do not use this setting.

