To our customers,

Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: http://www.renesas.com

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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H8/3067Series ,H8/3067F-ZTAT Hardware Manual Errata

Corrections have been made to the H8/3067Series,H8/3067F-ZTAT™ Hardware Manual (ADE-602-135B) as shown below. Please amend your manual accordingly.

Page 205 7.2.2 I/O Address Registers (IOAR)

Incorrect

An IOAR functions as a source or destination address register depending on how the DMAC is activated: as a destination address register if activation is by a receive-data-full interrupt from serial communication interface (SCI) channel 0 or by an A/D converter conversion-end interrupt, and as a source address register otherwise.

Correct

An IOAR functions as a source or destination address register depending on how the DMAC is activated: as a source address register if activation is by a receive-data-full interrupt from serial communication interface (SCI) channel 0 or by an A/D converter conversion-end interrupt, and as a destination address register otherwise

Page 221 Table 7.6 Register Functions in I/O Mode
Page 224 Table 7.7 Register Functions in Idle Mode
Page 226 Table 7.8 Register Functions in Repeat Mode

Incorrect

	Function			
Register	Activated by SCI 0 Receive-Data-Full Interrupt	Other Activation	Initial Setting	Operation
≈				≈

Correct

	Function			
Register	Activated by SCI 0 Receive-Data-Full Interrupt or by A/D Converter Conversion-End Interrupt	Other Activation	Initial Setting	Operation
≈				≈

Page 598 Table 18.6 Setting On-Board Programming Modes

Incorrect

Mode		FWE	MD_2	MD_1	MD_0	Notes
Boot mode	mode 5	1*[1]	0*[2]	0	0	0 : V_{IL}
	mode 7		0*[2]	1	0	1 : V_{IH}
User program mode	mode 5		1	0	1	
	mode 7		1	1	1	

Correct

Mode		FWE	MD_2	MD_1	MD_0	Notes
Boot mode	mode 5	1*[1]	0*[2]	0	1	0 : V_{IL}
	mode 7		0*[2]	1	1	1 : V_{IH}
User program mode	mode 5		1	0	1	
	mode 7		1	1	1	