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## **RENESAS TECHNICAL UPDATE**

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Product classification		MPU & MCU	Document No.	TN-V85-A021A/E		Rev.	1.00	
Title		urther notes about the function of timer/event ounter registor		Notification classification	Technical Information			
Affecte	ed -		Affected production lot	Related	User's Manual : Hardware of		dware of	
Products		In the main part	All lots	documents	affected produc			

For the V850 series microcontrollers on <u>2.Affected Products</u>, The following further notes about the function of timer/event counter registor is released.

- (1) TMPn counter read buffer register (TPnCNT)
- (2) TMQn counter read buffer register (TQnCNT)

## 1. Notification

(1) TMPn counter read buffer register (TPnCNT)

If TPnCNT register is read when the TPnCE bit = 0, 0000H is read.

If TPnCNT register is read when the TPnCE bit = 1, the count value of the 16-bit timer can be read.

The count value of the 16-bit counter is different by operation mode as follows.

· External event count mode

The 16-bit counter is set to 0000H at the timing when the TPnCE bit changes from 0 to 1.

After that, the counter counts up from 0001H to 0002H, 0003H, and so on, each time a valid edge of an external event count input is detected.

·Other modes

The 16-bit counter starts counting from the default value FFFFH.

It counts up from FFFFH to 0000H, 0001H, 0002H, 0003H, and so on.



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(2) TMQn counter read buffer register (TQnCNT)

If TQnCNT register is read when the TQnCE bit = 0, 0000H is read.

If TQnCNT register is read when the TQnCE bit = 1, the count value of the 16-bit timer can be read.

The count value of the 16-bit counter is different by operation mode as follows.

• External event count mode

The 16-bit counter is set to 0000H at the timing when the TQnCE bit changes from 0 to 1.

After that, the counter counts up from 0001H to 0002H, 0003H, and so on, each time a valid edge of an external event count input is detected.

·Other modes

The 16-bit counter starts counting from the default value FFFFH.

It counts up from FFFFH to 0000H, 0001H, 0002H, 0003H, and so on.

## 2.Affected Products

Products series	Timer			
	TMP	TMQ		
V850ES/SG1 series	0	-		
V850ES/SG2 series	0	0		
V850ES/SJ2 series	0	0		

 $<sup>\</sup>circ : provided, \ \, \cdot : non\text{-}provided$