To our customers,

Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

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April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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RENESAS TECHNICAL UPDATE

Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan
Renesas Technology Corp.

Product Category	MPU/MCU		Document No.	TN-SH7-A720A/E	Rev.	1.00
Title	Error correction of registers of the hardware manual		Information Category	Technical Notification		
Applicable Product	SH7720 Group SH7721 Group	Lot No.	Reference Document	SH7720 Group, SH7721 Group hardware manual Rev.3.00 (REJ09B0033-0300)		
		All				

There are error correction of registers of the hardware manual.

1. Page 1290 of 1458   37.3 Register States in Each Operating Mode

Original:

Register Abbreviation	Manual Reset	Module
CMNCR	Initialized	BSC
CS0BCR	Initialized	
CS2BCR	Initialized	
CS3BCR	Initialized	
CS4BCR	Initialized	
CS5ABCR	Initialized	
CS5BBCR	Initialized	
CS6ABCR	Initialized	
CS6BBCR	Initialized	
CS0WCR	Initialized	
CS2WCR	Initialized	
CS3WCR	Initialized	
CS4WCR	Initialized	
CS5AWCR	Initialized	
CS5BWCR	Initialized	
CS6AWCR	Initialized	
CS6BWCR	Initialized	
SDCR	Initialized	
RTCSR	Initialized	
RTCNT	Initialized	
RTCOR	Initialized	

Amended:

Register Abbreviation	Manual Reset	Module
CMNCR	Retained	BSC
CS0BCR	Retained	
CS2BCR	Retained	
CS3BCR	Retained	
CS4BCR	Retained	
CS5ABCR	Retained	
CS5BBCR	Retained	
CS6ABCR	Retained	
CS6BBCR	Retained	
CS0WCR	Retained	
CS2WCR	Retained	
CS3WCR	Retained	
CS4WCR	Retained	
CS5AWCR	Retained	
CS5BWCR	Retained	
CS6AWCR	Retained	
CS6BWCR	Retained	
SDCR	Retained	
RTCSR	Retained	
RTCNT	Retained	
RTCOR	Retained	

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2.   Page 1302 of 1458   37.3 Register States in Each Operating Mode

Original: Amended:

Register Abbreviation	Manual Reset	Module	Register Abbreviation	Manual Reset	Module
BDRB	Initialized	UBC	BDRB	Retained	UBC
BDMRB	Initialized		BDMRB	Retained	
BRCR	Initialized		BRCR	Retained	
BETR	Initialized		BETR	Retained	
BARB	Initialized		BARB	Retained	
BAMRB	Initialized		BAMRB	Retained	
BBRB	Initialized		BBRB	Retained	
BRSR	Initialized		BRSR	Retained	
BARA	Initialized		BARA	Retained	
BAMRA	Initialized		BAMRA	Retained	
BBRA	Initialized		BBRA	Retained	
BRDR	Initialized		BRDR	Retained	
BASRA	Initialized		BASRA	Retained	
BASRB	Initialized		BASRB	Retained	

3.   Page 328 of 1458   9.4.5 Refresh Timer Control/Status Register (RTCSR)

Original: Bit 31 to 8   Reserved

These bits are always read as 0. The write value should always be 0.

Amended: Bit 31 to 8   Reserved

These bits are always read as 0. The write value should always be H'A55A 00.

4.   Page 329 of 1458   9.4.6 Refresh Timer Counter (RTCNT)

Original: Bit 31 to 8   Reserved

These bits are always read as 0. The write value should always be 0.

Amended: Bit 31 to 8   Reserved

These bits are always read as 0. The write value should always be H'A55A 00.

5.   Page 330 of 1458   9.4.7 Refresh Time Constant Register (RTCOR)

Original: Bit 31 to 8   Reserved

These bits are always read as 0. The write value should always be 0.

Amended: Bit 31 to 8   Reserved

These bits are always read as 0. The write value should always be H'A55A 00.

6.   Page 658 of 1458   20.3.10 I2C Bus Master Transfer Clock Select Register (ICCKS)

Original: Bit 7 to 5   Reserved

These bits are always read as 0. The write value should always be 0.

Amended: Bit 7 to 5   Reserved

These bits are always read as 1. The write value should always be 0.