

To our customers,

Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: <http://www.renesas.com>

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

Send any inquiries to <http://www.renesas.com/inquiry>.

RENESAS TECHNICAL UPDATE

Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan
Renesas Technology Corp.

Product Category	MPU/MCU		Document No.	TN-SH7-A748A/E	Rev.	1.00
Title	The error correction about I ² C Bus Interrupt Enable Register (ICIER) of I ² C bus interface 3 (IIC3)		Information Category	Technical Notification		
Applicable Product	SH7200 Series SH7210 Series SH7260 Series	Lot No.	Reference Document	See below		
		ALL				

We would like to inform you of the following error correction about I²C Bus Interrupt Enable Register (ICIER) of the I2C bus interface 3 (IIC3) included in the applicable products listed in the following table.

1. Error correction about RIE of ICIER

[Error] Enables or disables the receive data full interrupt request (RXI) and the overrun error interrupt request (ERI) in the clocked synchronous format when receive data is transferred from ICDSR to ICDSR and the RDRF bit in ICSR is set to 1. RXI can be canceled by clearing the RDRF or RIE bit to 0.

[Correction] Enables or disables the receive data full interrupt request (RXI) when receive data is transferred from ICDSR to ICDSR and the RDRF bit in ICSR is set to 1. RXI can be canceled by clearing the RDRF or RIE bit to 0.

2. Error correction about NAKIE of ICIER

[Error] Enables or disables the NACK detection interrupt request (NAKI) and the overrun error (OVE set in ICSR) interrupt request (ERI) in the clocked synchronous format when the NACKF or AL/OVE bit in ICSR is set. NAKI can be canceled by clearing the NACKF, AL/OVE, or NAKIE bit to 0.

[Correction] Enables or disables the NACK detection and Arbitration Lost / Overrun Error interrupt request (NAKI) when the NACKF or AL/OVE bit in ICSR is set. NAKI can be canceled by clearing the NACKF, AL/OVE, or NAKIE bit to 0.

Applicable Products and Reference Documents

Applicable Products		Reference Document Title	Rev	Document No.
Series	Group			
SH7200 Series	SH7201	SH7201 Group Hardware Manual	Rev.3.0	REJ09B0321-0300
	SH7203	SH7203 Group Hardware Manual	Rev.2.0	REJ09B0313-0200
	SH7205	SH7205 Group Hardware Manual	Rev.1.0	REJ09B0372-0100
	SH7206	SH7206 Group Hardware Manual	Rev.3.0	REJ09B0191-0300
SH7210 Series	SH7211	SH7211 Group Hardware Manual	Rev.3.0	REJ09B0344-0300
SH7260 Series	SH7261	SH7261 Group Hardware Manual	Rev.2.0	REJ09B0320-0200
	SH7262 SH7264	SH7262 Group SH7264 Group Hardware Manual	Rev.2.0	REJ09B0445-0200
	SH7263	SH7263 Group Hardware Manual	Rev.3.0	REJ09B0290-0300
	SH7265	SH7265 Group Hardware Manual	Rev.1.0	REJ09B0351-0100