

To our customers,

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## Old Company Name in Catalogs and Other Documents

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On April 1<sup>st</sup>, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: <http://www.renesas.com>

April 1<sup>st</sup>, 2010  
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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# RENESAS TECHNICAL UPDATE

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Renesas Technology Corp.

Product Category	MPU&MCU		Document No.	TN-SH7-A652A/E	Rev.	1.00
Title	The error correction about A/D converter start request control register (TADCR) of Multi-Function Timer Pulse Unit 2		Information Category	Technical Notification		
Applicable Product	R5S72630P200FP R5S72631P200FP R5S72632P200FP R5S72633P200FP R5S72030W200FP	Lot No.	Reference Document	<ul style="list-style-type: none"> <li>• SH7263 Group Hardware Manual (REJ09B0290-0200)</li> <li>• SH7203 Group Hardware Manual (REJ09B0313-0100)</li> </ul>		
		ALL				

We would like to inform you of the following error corrections of Multi-Function Timer Pulse Unit 2 included in the above-mentioned applicable products.

## 1. Error correction about A/D Converter Start Request Delaying Function

[Error]

### 11.4.9 A/D Converter Start Request Delaying Function

- A/D Converter Start Request Delaying Function Linked with Interrupt Skipping

A/D converter start requests (TRG4AN and TRG4BN) can be issued in coordination with interrupt skipping by making settings in the UT4AE, DT4AE, UT4BE, and DT4BE bits in the timer A/D converter start request control register (TADCR).

[Correction]

### 11.4.9 A/D Converter Start Request Delaying Function

- A/D Converter Start Request Delaying Function Linked with Interrupt Skipping

A/D converter start requests (TRG4AN and TRG4BN) can be issued in coordination with interrupt skipping by making settings in the [ITA3AE](#), [ITA4VE](#), [ITB3AE](#), and [ITB4VE](#) bits in the timer A/D converter start request control register (TADCR).

## 2. Error correction about A/D Converter Activation

[Error]

### 11.5.3 A/D Converter Activation

- (3) A/D Converter Activation by A/D Converter Start Request Delaying Function

The A/D converter can be activated by generating A/D converter start request signal TRG4AN or TRG4BN when the TCNT\_4 count matches the TADCORA or TADCORB value if the TAD4AE or TAD4BE bit in the A/D converter start request control register (TADCR) is set to 1.

[Correction]

### 11.5.3 A/D Converter Activation

- (3) A/D Converter Activation by A/D Converter Start Request Delaying Function

The A/D converter can be activated by generating A/D converter start request signal TRG4AN or TRG4BN when the TCNT\_4 count matches the TADCORA or TADCORB value if the [UT4AE](#), [DT4AE](#), [UT4BE](#), or [DT4BE](#) bit in the A/D converter start request control register (TADCR) is set to 1.