

RENESAS TECHNICAL UPDATE

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Product Category	MPU/MCU		Document No.	TN-RX*-A0207A/E	Rev.	1.00
Title	Errata to User's Manuals Regarding the Bit Settings for Multi-Function Timer Pulse Unit 3 (MTU3)		Information Category	Technical Notification		
Applicable Product	RX64M Group, RX65N Group, RX651 Group, RX71M Group		Lot No.	Reference Document	User's Manual: Hardware for applicable products (Refer to the table on the last page for details)	
			All			

This document describes corrections to the settings of the TIOR register in the “Multi-Function Timer Pulse Unit (MTU3)” section of the User’s Manual: Hardware for the applicable products stated above. Page and table numbers are based on the manual for the RX64M Group. Refer to the table on the last page of this update for the corresponding page and table numbers in the other groups.

•Page 872 of 2936

The settings of the IOB[2] bit in combination with the other IOB bits when the TGRB register is in use as an input capture register in Table 24.25, TIORH (MTU8), are to be corrected as follows.

Before correction

Table 24.25 TIORH (MTU8)

Bit 7	Bit 6	Bit 5	Bit 4	Description
IOB[3]	IOB[2]	IOB[1]	IOB[0]	TGRB Register Function MTIOC8B Pin Function
(Omitted)				
1	x	0	0	Input capture register Input capture at rising edge
1	x	0	1	Input capture at falling edge
1	x	1	x	Input capture at both edge
1	1	x	x	Capture input source is the clock source for counting in MTU1. Input capture on counting up or down by MTU1.TCNT (LWA = 0) or MTU1.TCNTLW (LWA = 1).

After correction

Table 24.25 TIORH (MTU8)

Bit 7	Bit 6	Bit 5	Bit 4	Description
IOB[3]	IOB[2]	IOB[1]	IOB[0]	TGRB Register Function MTIOC8B Pin Function
(Omitted)				
1	0	0	0	Input capture register Input capture at rising edge
1	0	0	1	Input capture at falling edge
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1	1	x	x	Capture input source is the clock source for counting in MTU1. Input capture on counting up or down by MTU1.TCNT (LWA = 0) or MTU1.TCNTLW (LWA = 1).

Reference Documents

Group	Manual Title (Document Number)	Page Number	Section Number	Table Number
RX64M Group	RX64M Group User's Manual: Hardware Rev.1.10 (R01UH0377EJ0110)	Page 872 of 2936	24.2.6	Table 24.25
RX65N Group, RX651 Group	RX65N Group, RX651 Group User's Manual: Hardware Rev.2.10 (R01UH0590EJ0210)	Page 903 of 2695	24.2.6	Table 24.25
RX71M Group	RX71M Group User's Manual: Hardware Rev.1.10 (R01UH0493EJ0110)	Page 878 of 2952	24.2.6	Table 24.25