The following descriptions should be changed.

1. **OSCSF**
   
   Page 132
   
   - Incorrect
     
     HOCOSF flag (HOCO Clock Oscillation Stabilization Flag)
     
     The HOCOSF flag indicates the operating status of the counter that measures the wait time for the HOCO. Confirm that the OSCSF.HOCOSF is set to 1 before using the HOCO clock.
   
   - Correct
     
     HOCOSF flag (HOCO Clock Oscillation Stabilization Flag)
     
     The HOCOSF flag indicates the operating status of the counter that measures the wait time for the high-speed clock oscillator (HOCO). When **OFS1.HOCOEN is set to 0**, confirm that OSCSF.HOCOSF is set to 1 before using the HOCO clock.

2. **HOCOCR**
   
   Page 128
   
   - Incorrect
     
     HCSTP bit (HOCO Stop)
     
     The HCSTP bit starts or stops the HOCO clock.
     
     After setting the HCSTP bit to 0 to start the HOCO clock, confirm that the OSCSF.HOSCSF bit is set to 1 before using the clock. A fixed stabilization wait is required after setting the HOCO clock to start operation. A fixed wait time for oscillation to stop is also required. For the HOCO to operate, the HOCO Wait Control Register (HOCOWTCR) must also be set.
     
     The following restrictions apply when starting and stopping operation:
     
     - After stopping the HOCO, confirm that the OSCSF.HOCOSF bit is 0 before restarting the HOCO clock
     - Confirm that the HOCO clock operates and that the OSCSF.HOCOSF bit is 1 before stopping the HOCO clock
     - Regardless of whether the HOCO clock is selected as the system clock, confirm that the
OSCSF.HOSCSF bit is set to 1 before executing a WFI instruction to place the MCU in Software Standby or Deep Software Standby mode.

- Correct HCSTP bit (HOCO Stop)

The HCSTP bit starts or stops the HOCO clock. After setting the HCSTP bit to 0 to start the HOCO clock, confirm that the OSCSF.HOSCSF bit is set to 1 before using the clock. When OFS1.HOCOEN is set to 0, confirm that the OSCSF.HOCOSF is set to 1 before using the HOCO clock. A fixed stabilization wait is required after setting the HOCO clock to start operation. A fixed wait for oscillation to stop is also required. For the HOCO to operate, the HOCO Wait Control Register (HOCOWTCR) must also be set.

The following constraints apply when starting and stopping operation:

- After stopping the HOCO, confirm that the OSCSF.HOCOSF bit is 0 before restarting the HOCO clock
- Confirm that the HOCO clock is operating and that the OSCSF.HOCOSF bit is 1 before stopping the HOCO clock
- Regardless of whether the HOCO clock is selected as the system clock, confirm that the OSCSF.HOSCSF bit is set to 1 before executing a WFI instruction to place the MCU in Software Standby or Deep Software Standby mode while HOCOCR.HCSTP bit is 0.