To our customers,

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**Old Company Name in Catalogs and Other Documents**

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: [http://www.renesas.com](http://www.renesas.com)

April 1st, 2010
Renesas Electronics Corporation

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Issued by: Renesas Electronics Corporation ([http://www.renesas.com](http://www.renesas.com))

There is an errata in the SH7705 Hardware Manual (Rev.1.0) on SDRAM mode register access.

The section 7.8.10 Power-On Sequence in the manual shows that the SDRAM mode register access is byte-size access. However, it must be word-size access.

7.8.10 Power-On Sequence (page 229 of 690)

Line 8–11

Errata:

In this operation the data is ignored, the mode write is performed as a byte-size access. To set burst read/single write, CAS latency 2 to 3, wrap type = sequential, and burst length 1 supported by the LSI, arbitrary data is written in a byte-size access to the addresses shown in table 7.17.

Correction:

In this operation the data is ignored, the mode write is performed as a word-size access. To set burst read/single write, CAS latency 2 to 3, wrap type = sequential, and burst length 1 supported by the LSI, arbitrary data is written in a word-size access to the addresses shown in table 7.17.