## Old Company Name in Catalogs and Other Documents

On April 1<sup>st</sup>, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: http://www.renesas.com

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## **RENESAS TECHNICAL UPDATE**

Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan Renesas Technology Corp.

| Category  | MPU/MCU  |   |  | Document<br>No.         | TN-SH7-A736A/E                                      | Rev.        | 1.00    |
|---|--|---|--|-------------------------|---|-------------|---------|
| Title   | An Errata of SH7670 Group Pin Assignment   |   |  | Information<br>Category | Technical Notification                              |             |         |
| Applicable<br>Product   | SH7670 Group:<br>R5S76700B200BG, R5S76710B200BG,<br>R5S76720B200BG, R5S76730B200BG,<br>R5S76700D133BG, R5S76710D133BG,<br>R5S76720D133BG, R5S76730D133BG |   | Lot No.<br>All                                     | Reference<br>Document   | SH7670 Group Hardw<br>Rev.2.00<br>(REJ09B0437-0200) | are Manu    | ual     |
| Dear value c  | ustomer,   |   |  |                         |   |             |         |
| Please be inf   | formed that the  | re is an errata of SH7670 grou                            | p pin assignr                                      | nent.                   |   |             |         |
|   |  | - F   | escriptions -                                      |                         |   |             |         |
| Tabla 1 4. L  | ict of 1/0 Attribu   | utes of Each Pin" is corrected                            |  |                         | ailor figuros and tables (                          | ("Eiguro 1  | 1 2. Di |
|   |  |   |  |                         | inal lightes and tables (                           | ( i iguic i | 1.0.1 1 |
| Assignmer   | nts" and "Table  | 23.8: List of Pins for Each Ope                           | rating Mode"                                       | ') has no errata.       |   |             |         |
|   |  |   |  | /                       |   |             |         |
|   |  |   |  | ,                       |   |             |         |
| Before cor  | rection:   |   |  | ,                       |   |             |         |
| Before cor  |  | Function Name   | I/O Attr   |                         |   |             |         |
|   |  | Function Name<br>VCCQ_09                                  | I/O Attr   | ibute                   |   |             |         |
| Pin Nu  |  |   |  | ibute                   |   |             |         |
| Pin Nu<br>G16   |  | VCCQ_09   | Power  | ibute                   |   |             |         |
| Pin Nu<br>G16<br>G17  |  | VCCQ_09<br>D31  | Power  | ibute                   |   |             |         |
| Pin Nu<br>G16<br>G17<br>G18   | umber  | VCCQ_09<br>D31<br>D23                                     | Power<br>IO<br>IO                                  | ibute                   |   |             |         |
| Pin Nu<br>G16<br>G17<br>G18<br>G19  | umber  | VCCQ_09<br>D31<br>D23                                     | Power<br>IO<br>IO                                  | ribute                  |   |             |         |
| Pin Nu<br>G16<br>G17<br>G18<br>G19<br>After corre                         | umber  | VCCQ_09<br>D31<br>D23<br>D22                              | Power<br>IO<br>IO<br>IO                            | ribute                  |   |             |         |
| Pin Nu<br>G16<br>G17<br>G18<br>G19<br>After corre<br>Pin Nu               | umber  | VCCQ_09<br>D31<br>D23<br>D22<br>Function Name             | Power<br>IO<br>IO<br>IO                            | ribute                  |   |             |         |
| Pin Nu<br>G16<br>G17<br>G18<br>G19<br>After corre<br>Pin Nu<br>G17        | umber  | VCCQ_09<br>D31<br>D23<br>D22<br>Function Name<br>VCCQ_09  | Power<br>IO<br>IO<br>IO<br>I/O Attr<br>Power       | ribute                  |   |             |         |
| Pin Nu<br>G16<br>G17<br>G18<br>G19<br>After corre<br>Pin Nu<br>G17<br>G18 | umber  | VCCQ_09   D31   D23   D22   Function Name   VCCQ_09   D31 | Power<br>IO<br>IO<br>IO<br>I/O Attr<br>Power<br>IO | ribute                  |   |             |         |

