RENESAS TECHNICAL UPDATE

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Product Category	MPU/MCU		Document No.	TN-RX*-A0220A/E	Rev.	1.00
Title	Errata to RX72M Group User's Manual: Hardware Rev.1.00		Information Category	Technical Notification		
Applicable Product	RX72M Group	Lot No.	Reference Document	RX72M Group User's Manual: Hardware Rev.1.00 (R01UH0804EJ0100)		
		All				

This document describes correction to the RX72M Group User's Manual: Hardware Rev.1.00.

• Page 3227 of 3322

The equations in note 3 of Table 65.6, DC Characteristics (3) in section 65.3, DC Characteristics are corrected as follows.

<u>Before correction</u>

Note 3. I_{CC} depends on the f (ICLK) as follows.
(when ICLK : PCLKA : PCLKB/PCLKC/PCLKD : BCLK : BCLK pin = 4 : 2 : 1 : 2 : 1 and EXTAL = 12 MHz)
- D version
I_{CC} max. = 0.77 × f + 74 (full operation in high-speed operating mode)
I_{CC} typ. = 0.22 × f + 7 (normal operation in high-speed operating mode)
I_{CC} typ. = 0.50 × f + 3.7 (ICLK 1 MHz max) (low-speed operating mode 1)
I_{CC} max. = 0.29 × f + 74 (sleep mode)
- G version
I_{CC} max. = 0.89 × f + 105 (full operation in high-speed operating mode)
I_{CC} typ. = 0.22 × f + 7 (normal operation in high-speed operating mode)
I_{CC} typ. = 0.50 × f + 3.7 (ICLK 1 MHz max) (low-speed operating mode 1)
I_{CC} max. = 0.37 × f + 105 (sleep mode)

<u>After correction</u>

Note 3. I_{CC} depends on the f (ICLK) as follows.
(when ICLK : PCLKA : PCLKB/PCLKC/PCLKD : BCLK : BCLK pin = 4 : 2 : 1 : 2 : 1 and EXTAL = 12 MHz)
- D version
I_{CC} max. = 0.62 × f + 113 (full operation in high-speed operating mode)
I_{CC} typ. = 0.22 × f + 7 (normal operation in high-speed operating mode)
I_{CC} typ. = 0.50 × f + 3.7 (ICLK 1 MHz max) (low-speed operating mode 1)
I_{CC} max. = 0.13 × f + 113 (sleep mode)
- G version
I_{CC} max. = 0.65 × f + 164 (full operation in high-speed operating mode)
I_{CC} typ. = 0.22 × f + 7 (normal operation in high-speed operating mode)
I_{CC} typ. = 0.50 × f + 3.7 (ICLK 1 MHz max) (low-speed operating mode 1)
I_{CC} max. = 0.13 × f + 164 (sleep mode)