

# RENESAS TECHNICAL UPDATE

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Product Category	MPU/MCU		Document No.	TN-RX*-A0208A/E	Rev.	1.00
Title	Errata to RX71M Group User's Manual Regarding the USB 2.0 FS Host/Function Module (USBb)		Information Category	Technical Notification		
Applicable Product	RX71M Group	Lot No.	Reference Document	RX71M Group User's Manual: hardware Rev.1.10 (R01UH0493EJ0110)		
		All				

This document describes corrections to the descriptions of the PHYSLEW register in the “USB 2.0 FS Host/Function Module (USBb)” section of RX71M Group User's Manual: Hardware, Rev.1.10.

The following corrections have already been reflected in the RX Family USB Basic Host and Peripheral Driver Firmware Integration Technology Rev1.23

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The descriptions for section 38.2.36, PHY Cross Point Adjustment Register (PHYSLEW) are corrected as follows.

### Before correction

Bit	Symbol	Bit Name	Description	R/W
b0	SLEWR00	Driver Cross Point Adjustment 00	0: When the host controller is selected. 1: When the function controller is selected.	R/W
b1	SLEWR01	Driver Cross Point Adjustment 01	0: When the host controller is selected. 1: When the host controller is selected.	R/W
b2	SLEWF00	Driver Cross Point Adjustment 00	Set this bit to 1.	R/W
b3	SLEWF01	Driver Cross Point Adjustment 01	0: When the host controller is selected. 1: When the host controller is selected.	R/W
b31 to b4	—	Reserved	These bits are read as 0. The write value should be 0.	R/W

The PHYSLEW register adjusts the cross point of the driver.

Set the value (0000000Eh when the host controller is selected or 00000005h when the function controller is selected) to the register before starting the USB operation.

### After correction

Bit	Symbol	Bit Name	Description	R/W
b0	SLEWR00	Driver Cross Point Adjustment 00	<b>Set this bit to 1.</b>	R/W
b1	SLEWR01	Driver Cross Point Adjustment 01	<b>Set this bit to 0.</b>	R/W
b2	SLEWF00	Driver Cross Point Adjustment 00	Set this bit to 1.	R/W
b3	SLEWF01	Driver Cross Point Adjustment 01	<b>Set this bit to 0.</b>	R/W
b31 to b4	—	Reserved	These bits are read as 0. The write value should be 0.	R/W

The PHYSLEW register adjusts the cross point of the driver.

Set the value (**00000005h**) to the register before starting the USB operation.