

# RENESAS TECHNICAL UPDATE

TOYOSU FORESIA, 3-2-24, Toyosu, Koto-ku, Tokyo 135-0061, Japan  
Renesas Electronics Corporation

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Title	Errata to RX66N Group User's Manual: Hardware Rev.1.00		Information Category	Technical Notification	
Applicable Product	RX66N Group	Lot No.	Reference Document	RX66N Group User's Manual: Hardware Rev.1.00 (R01UH0825EJ0100)	
		All			

This document describes correction to the RX66N Group User's Manual: Hardware Rev.1.00.

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The equations in note 3 of Table 61.6, DC Characteristics (3) in section 61.3, DC Characteristics are corrected as follows.

### Before correction

- Note 3.  $I_{CC}$  depends on the  $f$  (ICLK) as follows.  
(when ICLK : PCLKA : PCLKB/PCLKC/PCLKD : BCLK : BCLK pin = 2 : 2 : 1 : 2 : 1 and EXTAL = 12 MHz)
- D version
    - $I_{CC}$  max. =  $1.14 \times f + 74$  (full operation in high-speed operating mode)
    - $I_{CC}$  typ. =  $0.35 \times f + 7$  (normal operation in high-speed operating mode)
    - $I_{CC}$  typ. =  $0.50 \times f + 3.7$  (ICLK 1 MHz max) (low-speed operating mode 1)
    - $I_{CC}$  max. =  $0.58 \times f + 74$  (sleep mode)
  - G version
    - $I_{CC}$  max. =  $1.37 \times f + 105$  (full operation in high-speed operating mode)
    - $I_{CC}$  typ. =  $0.35 \times f + 7$  (normal operation in high-speed operating mode)
    - $I_{CC}$  typ. =  $0.50 \times f + 3.7$  (ICLK 1 MHz max) (low-speed operating mode 1)
    - $I_{CC}$  max. =  $0.75 \times f + 105$  (sleep mode)

### After correction

- Note 3.  $I_{CC}$  depends on the  $f$  (ICLK) as follows.  
(when ICLK : PCLKA : PCLKB/PCLKC/PCLKD : BCLK : BCLK pin = 2 : 2 : 1 : 2 : 1 and EXTAL = 12 MHz)
- D version
    - $I_{CC}$  max. =  $0.82 \times f + 113$  (full operation in high-speed operating mode)
    - $I_{CC}$  typ. =  $0.35 \times f + 7$  (normal operation in high-speed operating mode)
    - $I_{CC}$  typ. =  $0.50 \times f + 3.7$  (ICLK 1 MHz max) (low-speed operating mode 1)
    - $I_{CC}$  max. =  $0.26 \times f + 113$  (sleep mode)
  - G version
    - $I_{CC}$  max. =  $0.88 \times f + 164$  (full operation in high-speed operating mode)
    - $I_{CC}$  typ. =  $0.35 \times f + 7$  (normal operation in high-speed operating mode)
    - $I_{CC}$  typ. =  $0.50 \times f + 3.7$  (ICLK 1 MHz max) (low-speed operating mode 1)
    - $I_{CC}$  max. =  $0.27 \times f + 164$  (sleep mode)