

RENESAS TECHNICAL UPDATE

1753, Shimonumabe, Nakahara-ku, Kawasaki-shi, Kanagawa 211-8668 Japan
Renesas Electronics Corporation

Product Category	MPU/MCU	Document No.	TN-16C-A199A/E	Rev.	1.00
Title	Errata to R32C/151 Group, R32C/152 Group, R32C/153 Group User's Manuals	Information Category	Technical Notification		
Applicable Product	R32C/151 Group R32C/152 Group R32C/153 Group	Lot No.	Reference Document	R32C/151 Group User's Manual: Hardware Rev. 1.10 (REJ09B0503-0110) R32C/152 Group User's Manual: Hardware Rev. 1.10 (REJ09B0504-0110) R32C/153 Group User's Manual: Hardware Rev. 1.10 (REJ09B0505-0110)	

This document describes corrections to the I/O Pins chapter of the user's manuals listed above.

Chapter Numbers and Page Numbers for Each Group

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R32C/152	26	511 of 642
R32C/153	26	525 of 656

The corrections are indicated in red in the list below.

- Description in lines 3 and 4 of Chapter 26 was corrected as follows:

“However, a pull-up resistor is separated from other peripheral functions even if it is enabled, when a pin functions as an output pin.”

(“or an analog I/O pin” was deleted.)

- Figure 26.1 was corrected as follows:

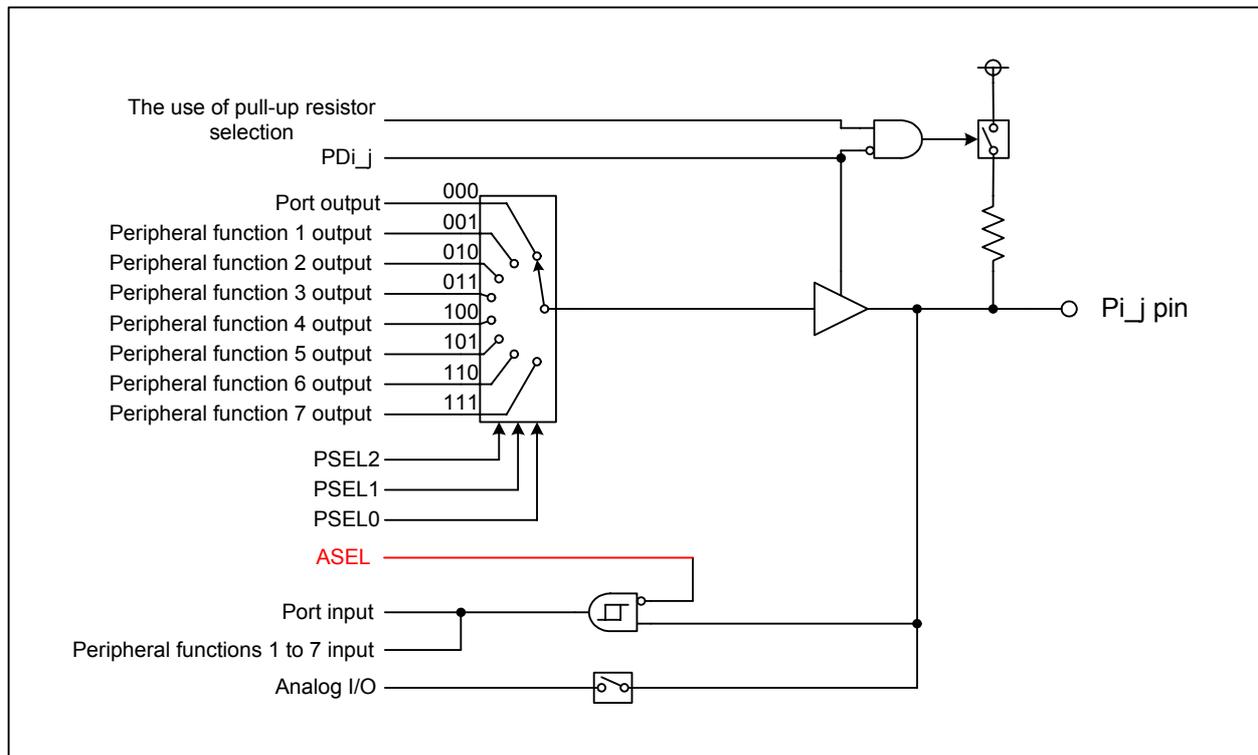


Figure 26.1 Typical I/O Pin Block Diagram (i = 0 to 18; j = 0 to 7)

(The ASEL bit was deleted from conditions for separating a pull-up resistor.)