

RENESAS TECHNICAL UPDATE

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Product Category	MPU/MCU	Document No.	TN-RX*-A0280A/E	Rev.	1.00
Title	Errata Regarding Setting of the RCR4.RCKSEL Bit for the RX65N Group and RX651 Group MCUs		Information Category	Technical Notification	
Applicable Product	RX65N Group, RX651 Group	Lot No.	Reference Document	RX65N Group, RX651 Group User's Manual: Hardware Rev.2.40 (R01UH0590EJ0240)	
		All			

This document describes corrections to the flowcharts in section 9.10.6, Notes on Sub-Clock Oscillator, section 31.3.2, Clock and Count Mode Setting Procedure, and section 31.6.7, Initialization Procedure When the Realtime Clock is Not to be Used in RX65N Group, RX651 Group User's Manual: Hardware.

• Purpose of Corrections

The procedure for setting the sub-clock is described in parts of both sections 9 and 31: section 9.10.6, Notes on Sub-clock Oscillator, section 31.3.2, Clock and Count Mode Setting Procedure, and section 31.6.7, Initialization Procedure When the Realtime Clock is Not to be Used. Although writing to the RCR4.RCKSEL bit should only proceed once, a value is set in the bit in the flowcharts in each of the sections stated above.

For this reason, setting of the RCR4.RCKSEL bit should only proceed in accord with the flowchart from the Realtime Clock section and the step should be omitted from the sub-clock setting procedures.

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The following deletions are made in Figure 9.13, Example Flowchart of Initialization when the Sub-Clock is to be Used as the Source to Drive Counting by the Realtime Clock.

- The RCR4.RCKSEL bit setting
- Note 2

The title of Figure 9.13 is also modified.

Before correction

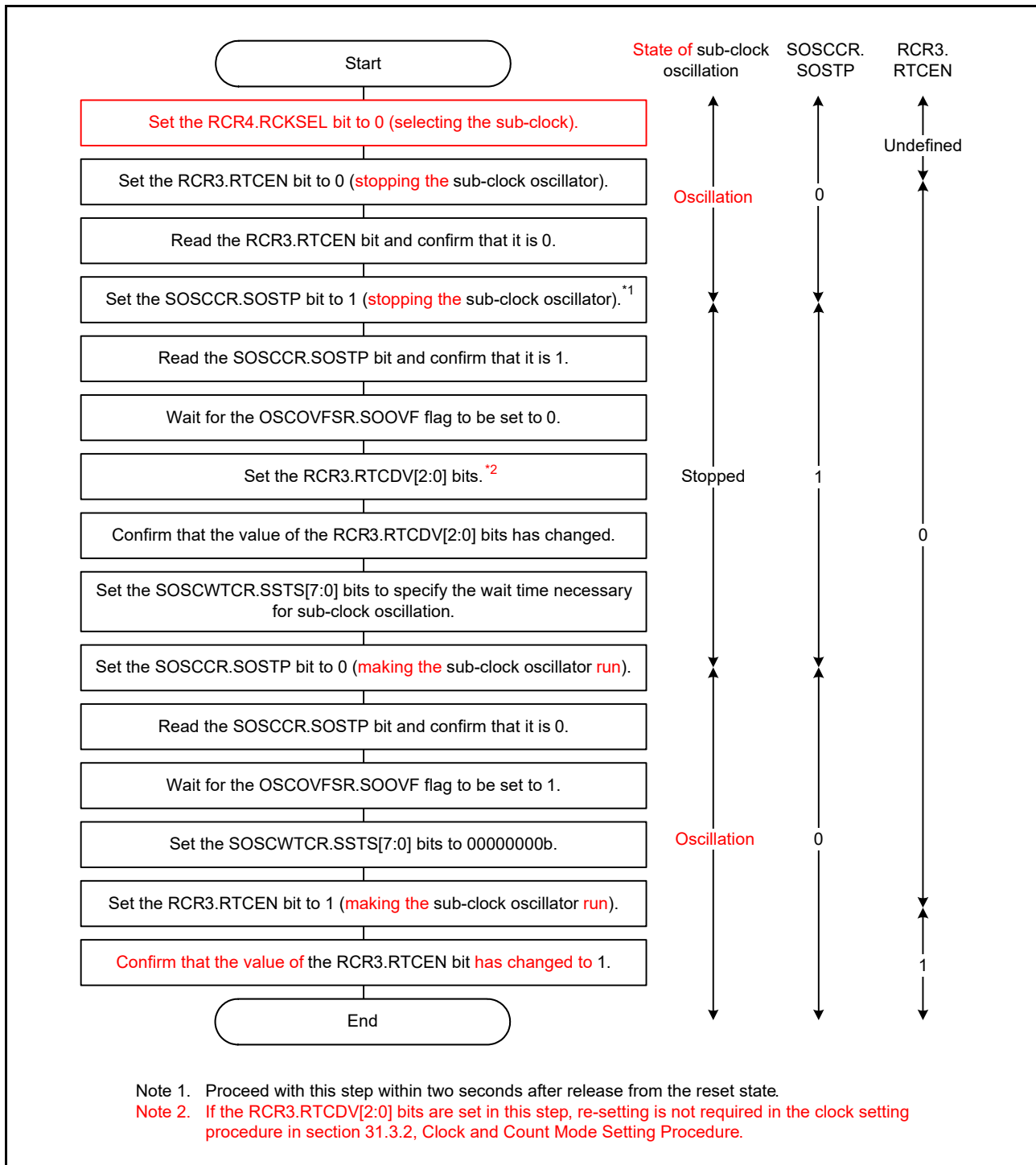


Figure 9.13 Example Flowchart of Initialization when the Sub-Clock is to be Used as the Source to Drive Counting by the Realtime Clock

After correction

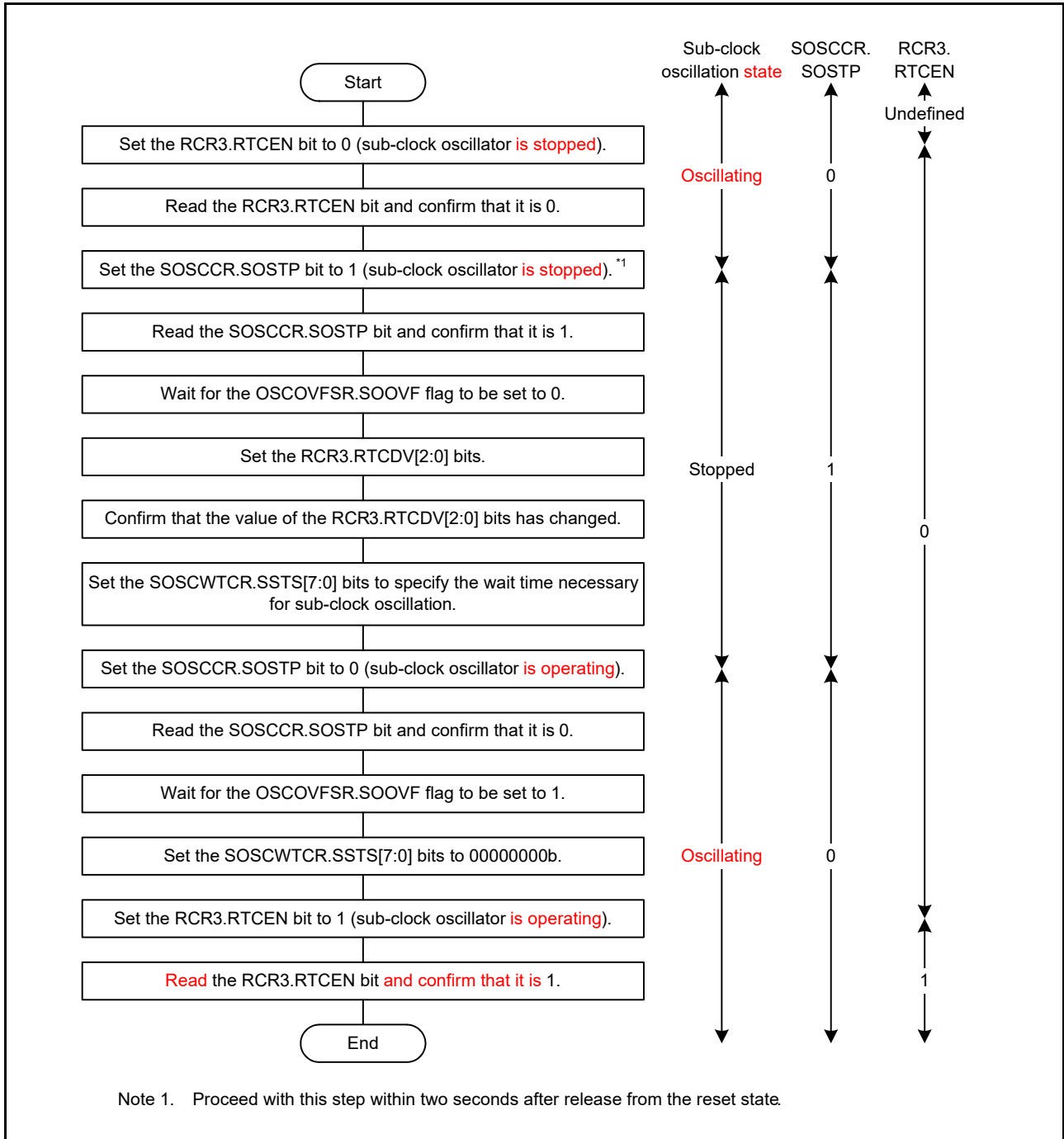


Figure 9.13 Example of Initialization Flowchart When the Sub-Clock Is to Be Used as the Clock Source for the RTC

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The following deletions are made in Figure 9.14, Example Flowchart for Initialization when the Sub-Clock is to be Used only as the System Clock.

- The RCR4.RCKSEL bit setting
- Note 2

The title of Figure 9.14 is also modified.

Before correction

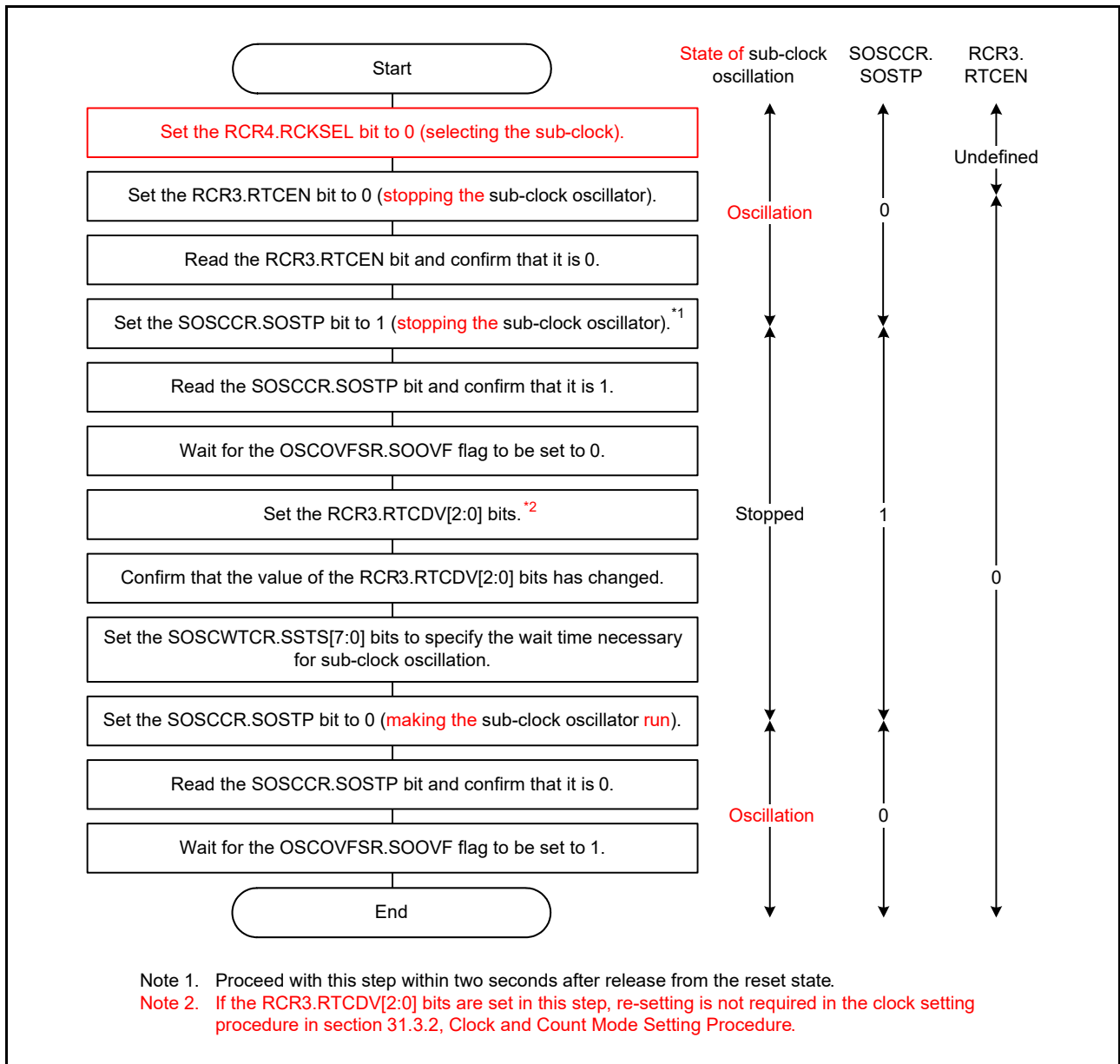


Figure 9.14 Example Flowchart for Initialization when the Sub-Clock is to be Used only as the System Clock

After correction

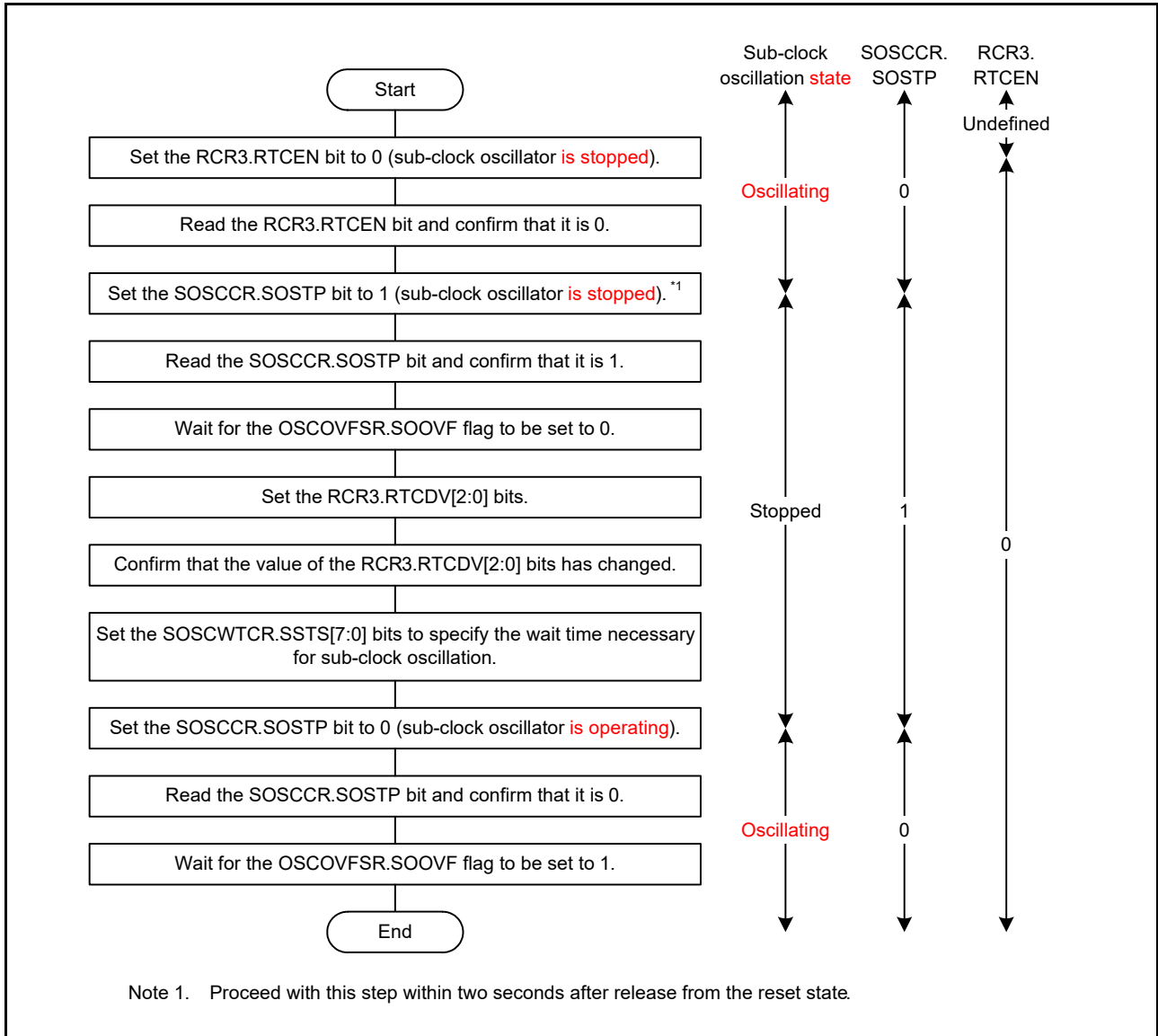


Figure 9.14 Example of Initialization Flowchart When the Sub-Clock is Not to Be Used as the Clock Source for the RTC

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The following deletion is made in Figure 9.15, Example Flowchart for when the Sub-Clock is not to be Used.

- The RCR4.RCKSEL bit setting

Before correction

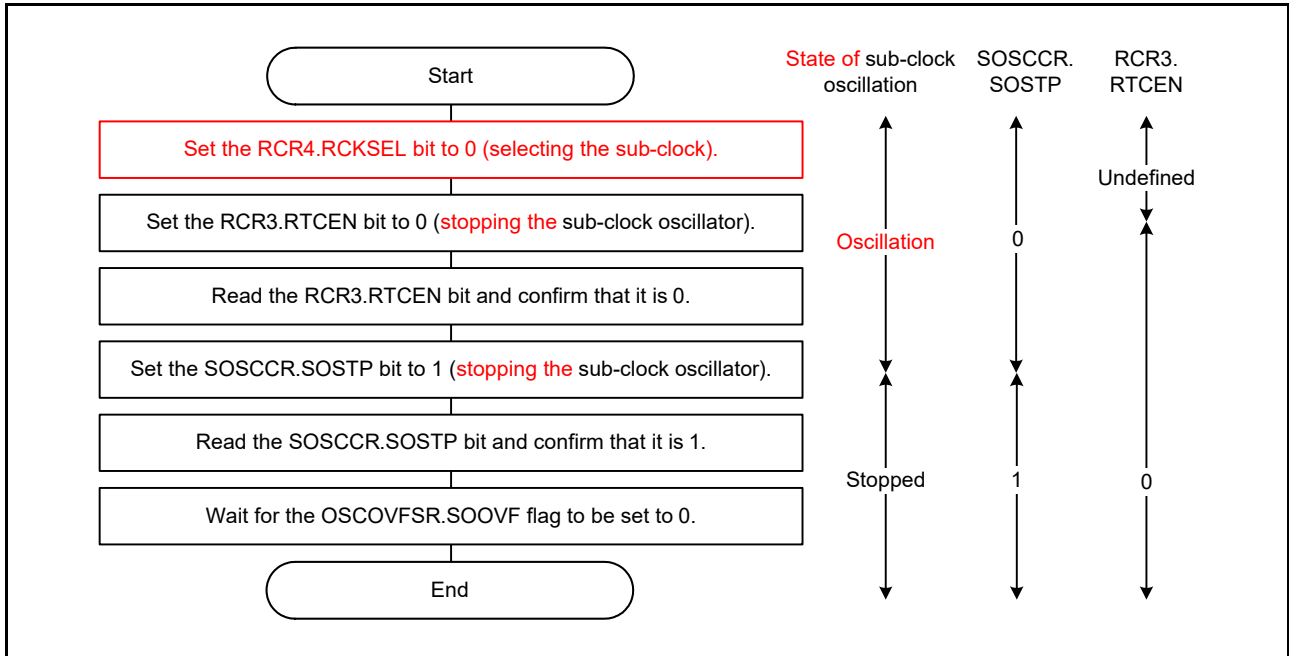


Figure 9.15 Example Flowchart for when the Sub-Clock is not to be Used

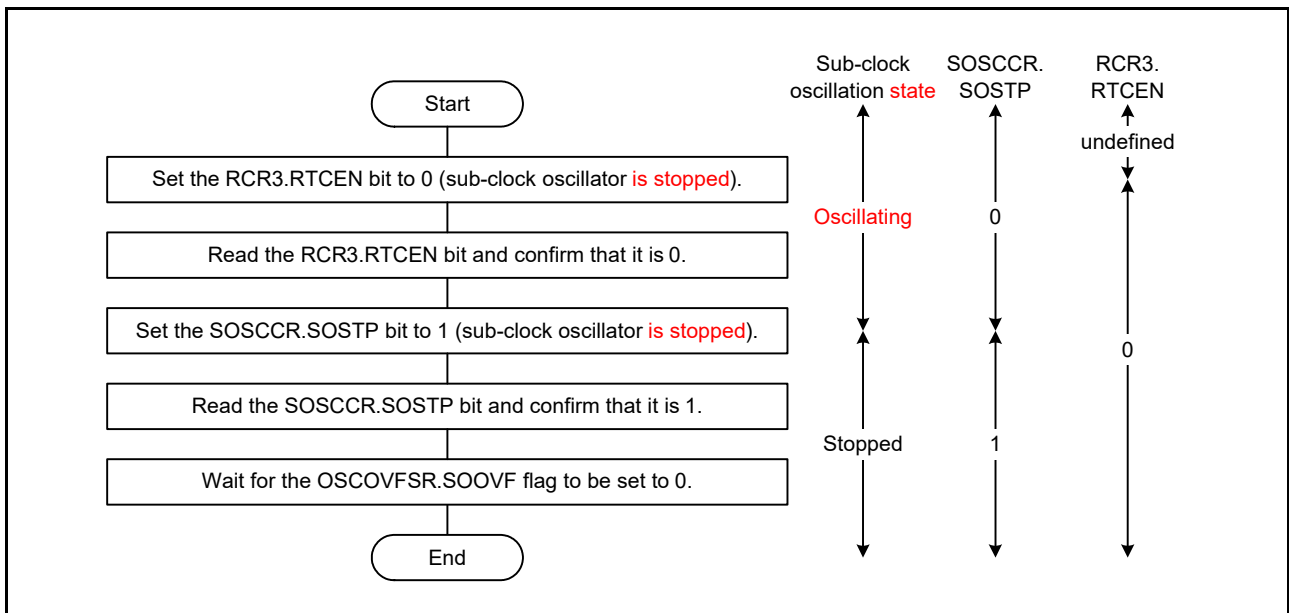


Figure 9.15 Example of Initialization Flowchart When the Sub-Clock Is Not to Be Used

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Figure 31.3, Clock and Count Mode Setting Procedure, in section 31.3.2, Clock and Count Mode Setting Procedure, is separated into two flowcharts as follows to suit the clock that is in use.

Before correction

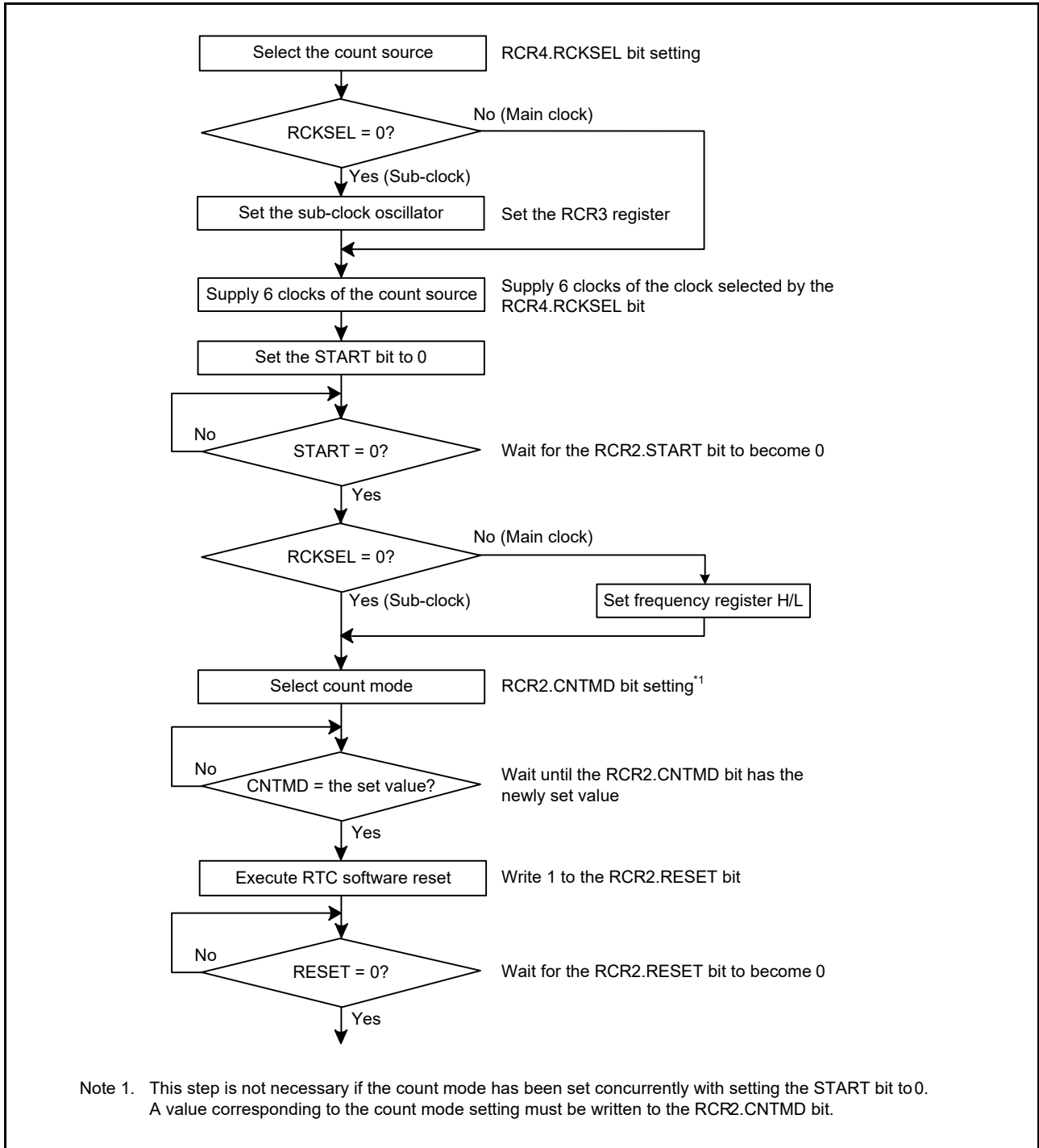


Figure 31.3 Clock and Count Mode Setting Procedure

After correction

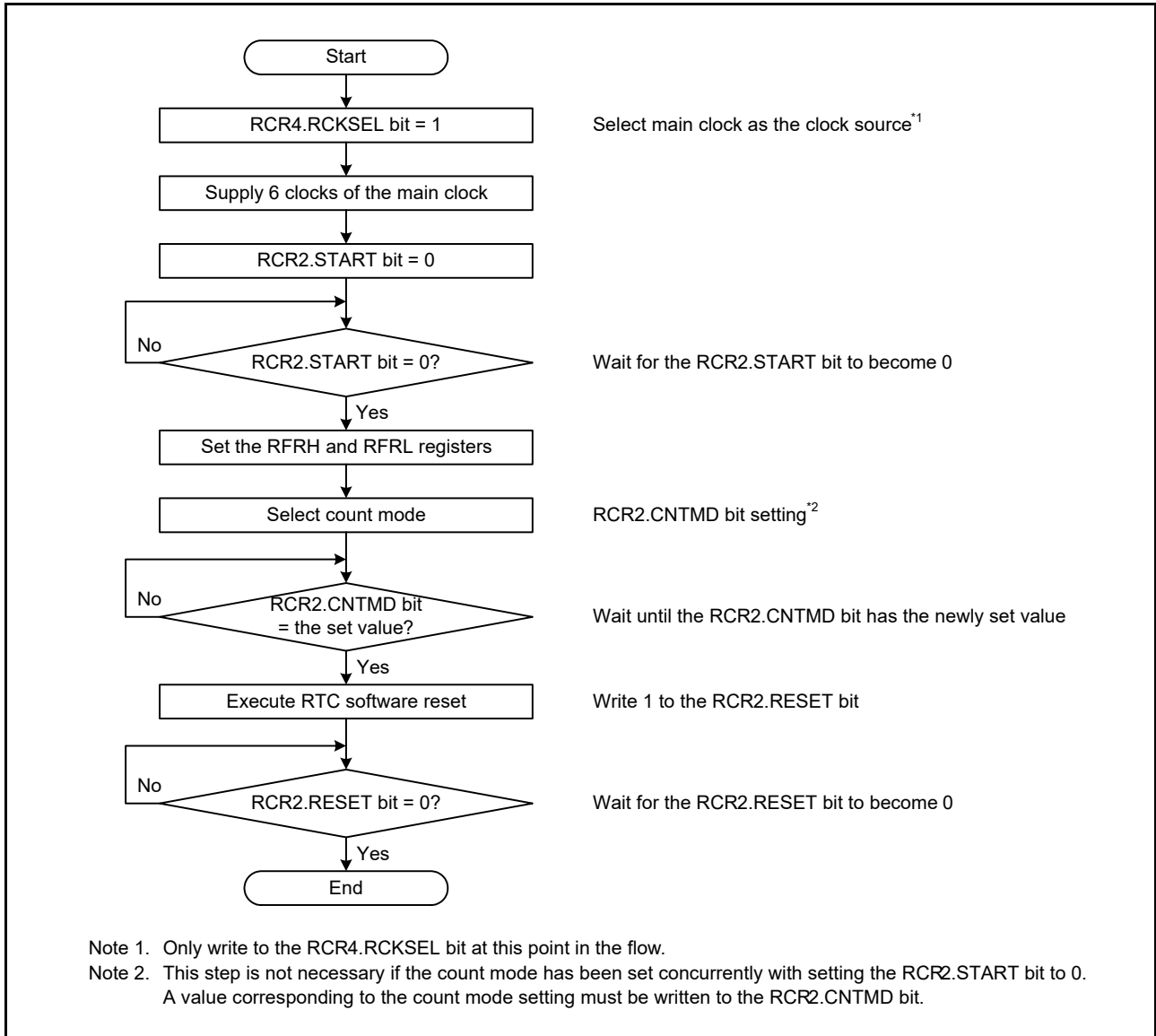
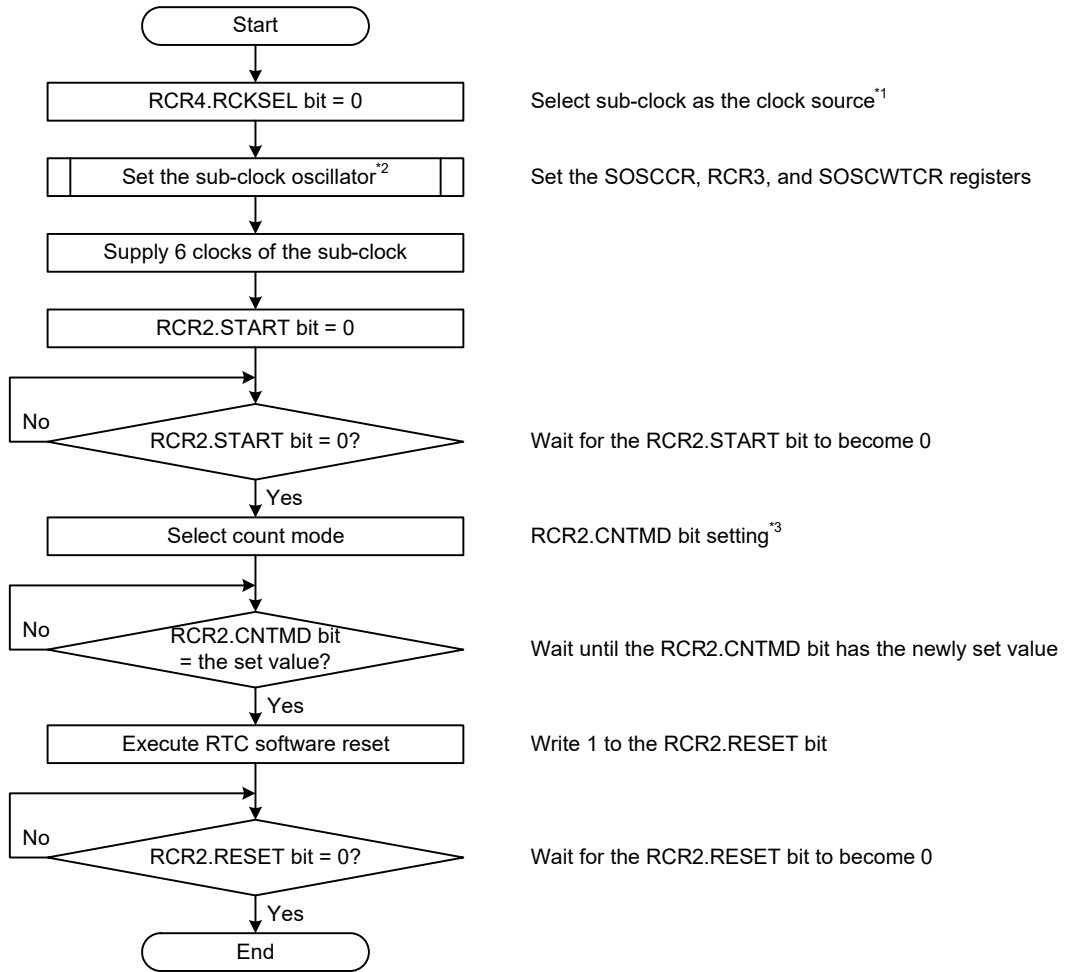


Figure 31.3 Clock and Count Mode Setting Procedure (When Using Main Clock)



Note 1. Only write to the RCR4.RCKSEL bit at this point in the flow.

Note 2. For the procedure for setting up a sub-clock oscillator, refer to section 9, Clock Generation Circuit.

Note 3. This step is not necessary if the count mode has been set concurrently with setting the RCR2.START bit to 0. A value corresponding to the count mode setting must be written to the RCR2.CNTMD bit.

Figure 31.4 Clock and Count Mode Setting Procedure (When Using Sub-Clock)

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Figure 31.14, Initialization Procedure, in section 31.6.7, Initialization Procedure When the Realtime Clock is Not to be Used, is separated into two flowcharts as follows to suit the clock that is in use. A step of clearing the interrupt status flags is also added to both flowcharts.

Before correction

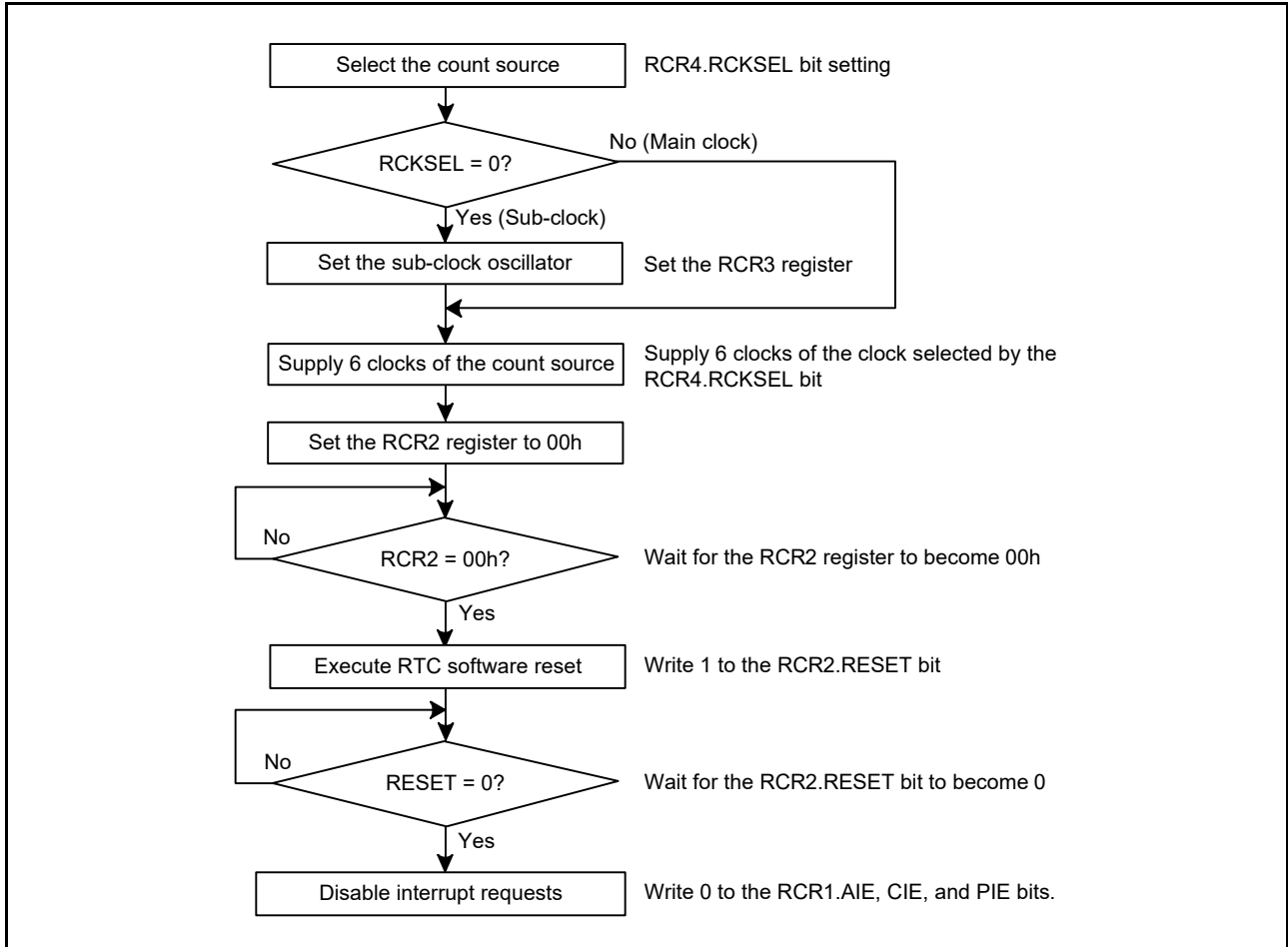


Figure 31.14 Initialization Procedure

After correction

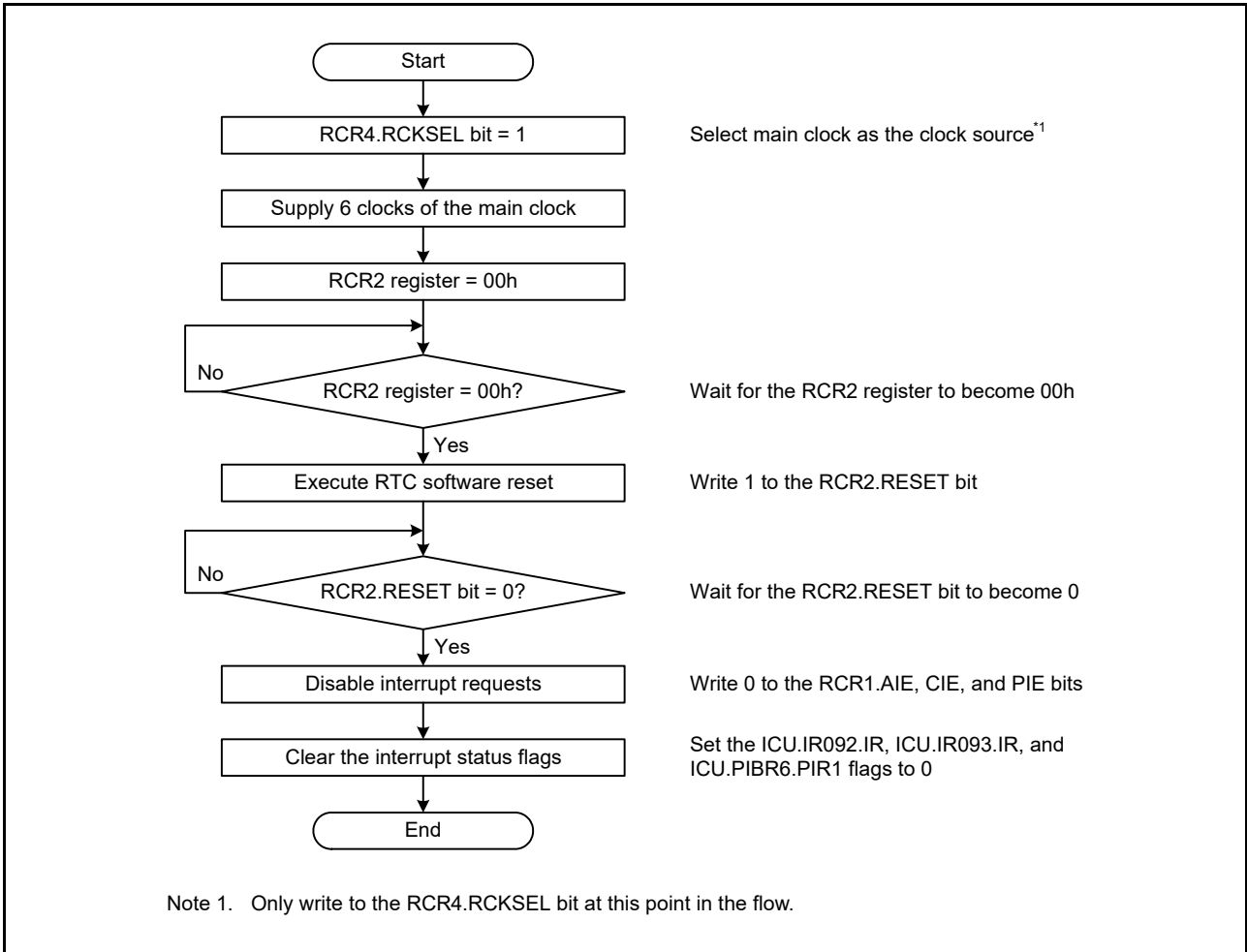


Figure 31.14 Initialization Procedure (When Using Main Clock)

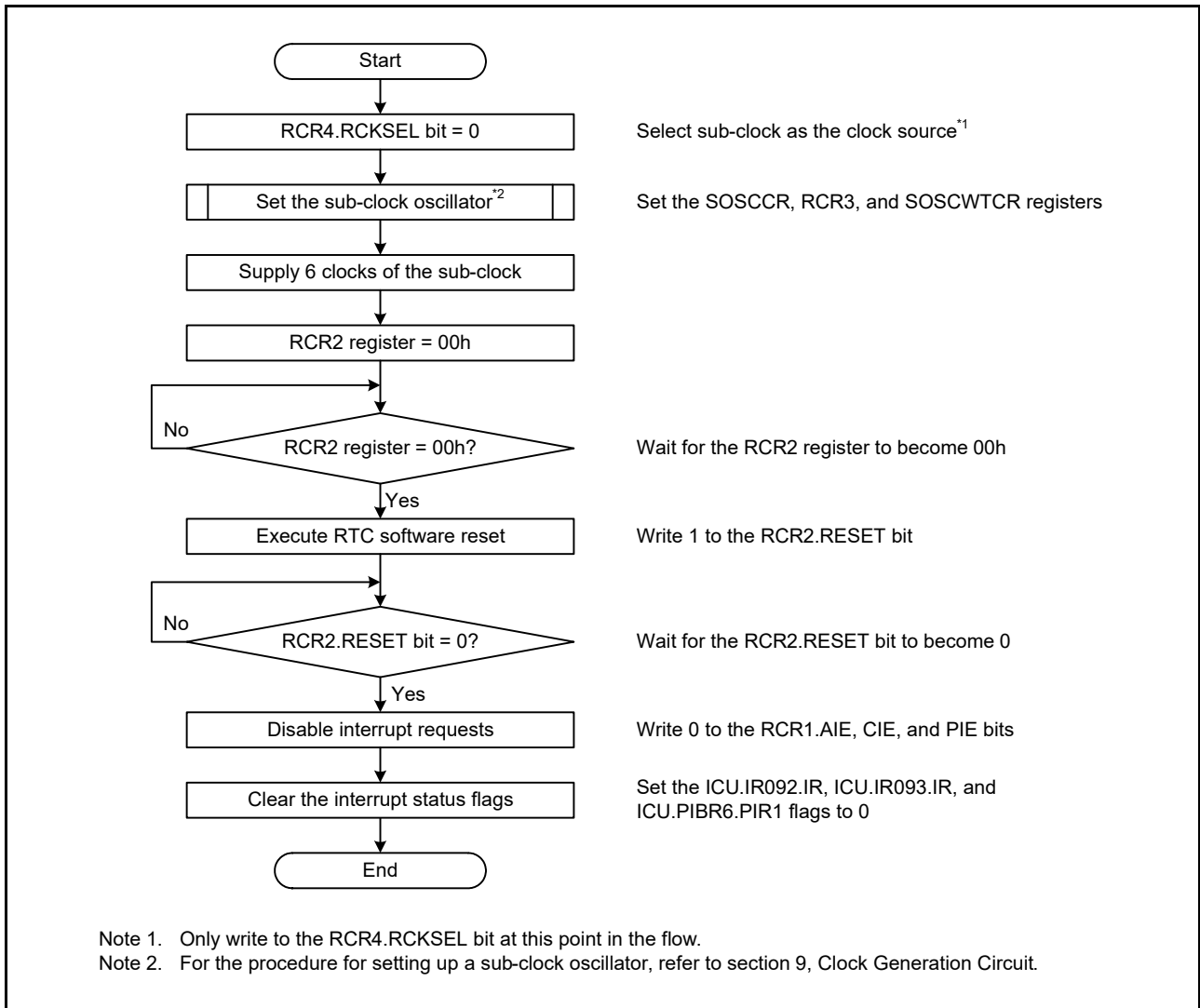


Figure 31.15 Initialization Procedure (When Using Sub-Clock)