This document describes the disclosure of specifications for the high-speed on-chip oscillator trimming registers of the above applicable products in RX200 Series.

9.2.x High-Speed On-Chip Oscillator Trimming Register n (HOCOTRRn) (n = 0 to 3)

Address(es): HOCOTRR0 0008 0400h, HOCOTRR1 0008 0401h, HOCOTRR2 0008 0402h, HOCOTRR3 0008 0403h

The HOCOTRRn register is a 8-bit readable/writable register that adjusts the high-speed on-chip oscillator (HOCO) frequency. The HOCOTRR0, HOCOTRR1, HOCOTRR2, and HOCOTRR3 registers correspond to frequencies of 32 MHz, 36.864 MHz, 40 MHz, and 50 MHz, respectively. The HOCO frequency changes by rewriting the register value. Set the register value in the range from 00h to FFh (0 to 255). The greater the register value is, the higher the frequency is.

The frequency is adjusted under certain conditions before shipment, so the value after reset varies with the chip. After a reset, the oscillation frequency returns to the factory default.