To our customers,

Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

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April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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Date: May.22.2007

RENESAS TECHNICAL UPDATE

Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan
Renesas Technology Corp.

Product Category	MPU&MCU		Document No.	TN-H8*-A370A/E	Rev.	1.00
Title	Delta sigma A/D converter Analog power supply current		Information Category	Technical Notification		
Applicable Product	H8/38086R Group	Lot No. All	Reference Document	H8/38086R Group Hardware manual (REJ09B0182-0300)		

Delta sigma A/D converter Analog power supply current of the H8/38086R group hardware manual is corrected.

Please refer to the following for details.

[Before change]

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Table 25.7 ΔΣA/D Converter Characteristics

Item	Symbol	Applicable Pins	Test Condition	Values Min.	Typ.	Max.	Unit	Notes
Analog power supply current	DIope	DVcc	PGA not used, BGR=OFF, DVcc=3.0V, Fovs=2.0MHz	-	2.0	-	mA	Reference value
	DIstop1	DVcc		-	0.1	-	μA	*2 Reference value
	DIstop2	DVcc		-	0.1	-	μA	*3 Reference value

Notes: 2. DIstop1 is the current in active and sleep modes while the ΔΣA/D converter is idle.

3. DIstop2 is the current at a reset and in standby, watch, subactive, and subsleep modes while the ΔΣA/D converter is idle.

[After change]

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Table 25.7 ΔΣA/D Converter Characteristics

Item	Symbol	Applicable Pins	Test Condition	Values Min.	Typ.	Max.	Unit	Notes
Analog power supply current	DIope	DVcc	PGA not used, BGR=OFF, DVcc=3.0V, Fovs=2.0MHz	-	2.0	-	mA	Reference value
	DIstop1	DVcc		-	1.8	-	mA	*2*8 Reference value
	DIstop2	DVcc		-	0.1	-	μA	*3*8 Reference value
	DIRESET	DVcc	$\overline{RES}$=GND	-	2.7	-	mA	Reference value

Notes: 2. DIstop1 is the current in active and sleep modes while the ΔΣA/D converter is idle.

3. DIstop2 is the current in standby, watch, subactive, and subsleep modes while the ΔΣA/D converter is idle.

8. VREF1 and 0 bits in the ADCR is set to B'00.

[Before change]

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Table 25.18 ΔΣA/D Converter Characteristics

Item	Symbol	Applicable Pins	Test Condition	Values Min.	Typ.	Max.	Unit	Notes
Analog power supply current	DIope	DVcc	PGA not used, BGR=OFF, DVcc=3.0V, Fovs=2.0MHz	-	2.0	-	mA	Reference value
	DIstop1	DVcc		-	0.1	-	μA	*2 Reference value
	DIstop2	DVcc		-	0.1	-	μA	*3 Reference value

Notes: 2. DIstop1 is the current in active and sleep modes while the ΔΣA/D converter is idle.

3. DIstop2 is the current at a reset and in standby, watch, subactive, and subsleep modes while the ΔΣA/D converter is idle.

[After change]

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Table 25.18 ΔΣA/D Converter Characteristics

Item	Symbol	Applicable Pins	Test Condition	Values Min.	Typ.	Max.	Unit	Notes
Analog power supply current	DIope	DVcc	PGA not used, BGR=OFF, DVcc=3.0V, Fovs=2.0MHz	-	2.0	-	mA	Reference value
	DIstop1	DVcc		-	1.8	-	mA	*2*8 Reference value
	DIstop2	DVcc		-	0.1	-	μA	*3*8 Reference value
	DIRESET	DVcc	——	-	2.7	-	mA	Reference value

Notes: 2. DIstop1 is the current in active and sleep modes while the ΔΣA/D converter is idle.

3. DIstop2 is the current in standby, watch, subactive, and subsleep modes while the ΔΣA/D converter is idle.

8. VREF1 and 0 bits in the ADCR is set to B'00.