

# RENESAS TECHNICAL UPDATE

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Product Category	MPU/MCU		Document No.	TN-RX*-A031A/E	Rev.	1.00
Title	Corrections to Manual Regarding USB Clock for RX63N and RX631 Groups		Information Category	Technical Notification		
Applicable Product	RX63N Group RX631 Group	Lot No.	Reference Document	RX63N Group, RX631 Group User's Manual: Hardware Rev.1.00 (R01UH0041EJ0100)		
		All				

This document describes corrections to the UCK[3:0] bits in "9.2.2 System Clock Control Register 2 (SCKCR2)" of RX63N Group, RX631 Group User's Manual: Hardware Rev.1.00.

### Before Correction

Bit	Symbol	Bit Name	Description	R/W
b7 to b4	UCK[3:0]	USB Clock (UCLK) Select	b7 b4 0 0 0 1: x1/2 0 0 1 0: x1/3 0 0 1 1: x1/4 Settings other than above are prohibited when USB is in use. When USB is not in use, these bits are read as 0001b. The write value should be 0001b.	R/W

### Correction

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