

RENESAS TECHNICAL UPDATE

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Product Category	MPU/MCU		Document No.	TN-ÜÝ*-OEI FA/E	Rev. A	00
Title	Corrections to Manual regarding the I2C Bus Interface Module (RIIC) Module		Information Category	Technical Notification		
Applicable Product	RX71M Group, RX64M Group RX610 Group, RX62N Group, RX621 Group, RX62T Group, RX62G Group, RX630 Group, RX63N Group, RX631 Group, RX63T Group, RX210 Group, RX21A Group, RX220 Group, RX230 Group, RX231 Group, RX23T Group, RX110 Group, RX111 Group, RX113 Group	Lot No.	Reference Document	See below.		
		All lots				
<p>We would like to inform you of a correction regarding the I2C Bus Interface Module (RIIC).</p> <p>■ Details of manual revision</p> <p>The RX630 Group User's Manual: Hardware is taken as the example in describing the details of the correction.</p> <ul style="list-style-type: none"> • On page 1181, HOA Flag (Host Address Detection) was changed. <p>[Before change]</p> <p>[Setting condition]</p> <ul style="list-style-type: none"> • When the received slave address matches the host address (0001 000b) with the HOAE bit in IC SER set to 1 (host address detection enabled) <p>This flag is set to 1 at the rising edge of the ninth SCL clock cycle in the frame.</p> <p>[Clearing conditions]</p> <ul style="list-style-type: none"> • When 0 is written to the HOA bit after reading HOA = 1 • When a stop condition is detected • When 0 is written to the SMBS bit in ICMR3 or the HOAE bit in IC SER • When the received slave address does not match the host address (0001 000b) with the HOAE bit in IC SER set to 1 (host address detection enabled) <p>This flag is cleared to 0 at the rising edge of the ninth SCL clock cycle in the frame.</p> <ul style="list-style-type: none"> • When 1 is written to the IICRST bit in ICCR1 to apply an RIIC reset or an internal reset 						

[After change]

[Setting condition]

- When the received slave address matches the host address (0001 000b) with the HOAE bit in IC SER set to 1 (host address detection enabled)

This flag is set to 1 at the rising edge of the ninth SCL clock cycle in the frame.

[Clearing conditions]

- When 0 is written to the HOA bit after reading HOA = 1
- When a stop condition is detected
- ~~When 0 is written to the SMBS bit in ICMR3 or the HOAE bit in IC SER~~
- When the received slave address does not match the host address (0001 000b) with the HOAE bit in IC SER set to 1 (host address detection enabled)

This flag is cleared to 0 at the rising edge of the ninth SCL clock cycle in the frame.

- When 1 is written to the IICRST bit in ICCR1 to apply an RIIC reset or an internal reset

[Related Documents]

Series	Group	Related Documents	Rev.	Control Code	Page
RX700	RX71M	RX71M Group User's Manual: Hardware	1.00	R01UH0493EJ0100	2176
RX600	RX610	RX610 Group User's Manual: Hardware	1.20	R01UH0032EJ0120	715
	RX62N, RX621	RX62N Group, RX621 Group User's Manual: Hardware	1.40	R01UH0033EJ0140	1503
	RX62T, RX62G	RX62T Group, RX62G Group User's Manual: Hardware	2.00	R01UH0034EJ0200	1014
	RX630	RX630 Group User's Manual: Hardware	1.60	R01UH0040EJ0160	1181
	RX63N, RX631	RX63N Group, RX631 Group User's Manual: Hardware	1.80	R01UH0041EJ0180	1457
	RX63T	RX63T Group User's Manual: Hardware	2.10	R01UH0238EJ0210	1271
	RX64M	RX64M Group User's Manual: Hardware	1.00	R01UH0377EJ0100	2159
RX200	RX210	RX210 Group User's Manual: Hardware	1.50	R01UH0037EJ0150	1113
	RX21A	RX21A Group User's Manual: Hardware	1.10	R01UH0251EJ0110	933
	RX220	RX220 Group User's Manual: Hardware	1.10	R01UH0292EJ0110	870
	RX230, RX231	RX230 Group, RX231 Group User's Manual: Hardware	1.00	R01UH0496EJ0100	1272
	RX23T	RX23T Group User's Manual: Hardware	1.00	R01UH0520EJ0100	756
RX100	RX110	RX110 Group User's Manual: Hardware	1.10	R01UH0421EJ0110	651
	RX111	RX111 Group User's Manual: Hardware	1.20	R01UH0365EJ0120	899
	RX113	RX113 Group User's Manual: Hardware	1.02	R01UH0448EJ0102	966

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