

RENESAS TECHNICAL UPDATE

TOYOSU FORESIA, 3-2-24, Toyosu, Koto-ku, Tokyo 135-0061, Japan
Renesas Electronics Corporation

Product Category	MPU/MCU		Document No.	TN-RX*-A148A/E	Rev.	1.00
Title	Corrections to descriptions for the timer I/O control register of the multi-function timer pulse unit 2 (MTU2a)		Information Category	Technical Notification		
Applicable Product	RX230 Group, RX231 Group, RX130 Group, RX113 Group, RX111 Group, RX634 Group		Lot No.	Reference Document	User's Manual: Hardware for applicable products (see page 2.)	
			All			

This document describes corrections to descriptions for the timer I/O control register of the multi-function timer pulse unit 2 (MTU2a) in the above applicable products.

The details are described below using the page and table number of the RX113 Group manual as an example. For the other products, see the list on the next page.

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The pin function of MTIC5U, MTIC5V, and MTIC5W in Table 20.28 is corrected as follows.

Before correction

Table 20.28 TIORU, TIORV, and TIORW (MTU5)

Bit 4	Bit 3	Bit 2	Bit 1	Bit 0	Description
IOC[4]	IOC[3]	IOC[2]	IOC[1]	IOC[0]	MTU5.TGRU, MTU5.TGRV, MTU5.TGRW Function - Omitted -
					MTIC5U, MTIC5V, MTIC5W Pin Function
					Input capture register
1	1	0	0	1	Measurement of low pulse width of external input signal Capture at trough in complementary PWM mode
1	1	0	1	0	Measurement of low pulse width of external input signal Capture at trough in complementary PWM mode
1	1	0	1	1	Measurement of low pulse width of external input signal Capture at trough in complementary PWM mode
1	1	1	0	0	Setting prohibited
1	1	1	0	1	Measurement of high pulse width of external input signal Capture at trough in complementary PWM mode
1	1	1	1	0	Measurement of high pulse width of external input signal Capture at trough in complementary PWM mode
1	1	1	1	1	Measurement of high pulse width of external input signal Capture at trough in complementary PWM mode

After correction

Table 20.28 TIORU, TIORV, and TIORW (MTU5)

Bit 4	Bit 3	Bit 2	Bit 1	Bit 0	Description
IOC[4]	IOC[3]	IOC[2]	IOC[1]	IOC[0]	MTU5.TGRU,MTU5.TGRV, MTU5.TGRW Function - Omitted - Input capture register
1	1	0	0	1	Measurement of low pulse width of external input signal Capture at trough in complementary PWM mode
1	1	0	1	0	Measurement of low pulse width of external input signal Capture at crest in complementary PWM mode
1	1	0	1	1	Measurement of low pulse width of external input signal Capture at crest and trough in complementary PWM mode
1	1	1	0	0	Setting prohibited
1	1	1	0	1	Measurement of high pulse width of external input signal Capture at trough in complementary PWM mode
1	1	1	1	0	Measurement of high pulse width of external input signal Capture at crest in complementary PWM mode
1	1	1	1	1	Measurement of high pulse width of external input signal Capture at crest and trough in complementary PWM mode

< Documents for reference >

Group	Related Documents	Rev.	Control Code	Table Numbers (Page)
RX634 Group	RX634 Group User's Manual: Hardware	Rev.1.00	R01UH0495EJ0100	Table 22.28 (p.538)
RX230 Group RX231 Group	RX230 Group, RX231 Group User's Manual: Hardware	Rev.1.10	R01UH0496EJ0110	Table 23.28 (p.607)
RX130 Group	RX130 Group User's Manual: Hardware	Rev.1.00	R01UH0560EJ0100	Table 20.28 (p.395)
RX113 Group	RX113 Group User's Manual: Hardware	Rev.1.02	R01UH0448EJ0102	Table 20.28 (p.418)
RX111 Group	RX111 Group User's Manual: Hardware	Rev.1.20	R01UH0365EJ0120	Table 20.28 (p.390)

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