

RENESAS TECHNICAL UPDATE

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Product Category	MPU/MCU		Document No.	TN-RX*-A185A/E	Rev.	1.00
Title	Corrections to Descriptions for the P/E Suspend Command of Flash Memory in RX62N Group, RX621 Group, RX62T Group, and RX62G Group		Information Category	Technical Notification		
Applicable Product	RX62N Group, RX621 Group, RX62T Group, RX62G Group	Lot No.	Reference Document	RX62N Group, RX621 Group User's Manual: Hardware Rev.1.40 (R01UH0033EJ0140) RX62T Group, RX62G Group User's Manual: Hardware Rev.2.00 (R01UH0034EJ0200)		
		All				

This document describes corrections to the descriptions for the SUSRDY bit and the flow chart of the P/E suspend command in the "ROM (Flash Memory for Code Storage)" section of User's Manual: Hardware for the applicable products mentioned above.

Page and section numbers are based on the RX62N Group and RX621 Group. Refer to the table on the last page for the corresponding page and section numbers in the RX62T Group and RX62G Group.

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Clearing conditions of the SUSRDY bit in section 38.2.5, Flash Status Register 0 (FSTATR0) are corrected as follows.

Before Correction

[Clearing conditions]

- The FCU has accepted a P/E suspend command.
- During programming/erasure process, the FCU enters the command-locked state.

After Correction

[Clearing conditions]

- The FCU has accepted a P/E suspend command.
- During programming/erasure process, the FCU enters the command-locked state.
- **When programming/erasure process has been completed.**

• Reference Documents

Group	Manual Title	Document Number	Page	Number
RX62N RX621	RX62N Group, RX621 Group User's Manual: Hardware Rev.1.40	R01UH0033EJ0140	Page 1780 of 2026 Page 1817 of 2026	38.2.5 Figure 38.19
RX62T RX62G	RX62T Group, RX62G Group User's Manual: Hardware Rev.2.00	R01UH0034EJ0200	Page 1340 of 1489 Page 1374 of 1489	31.2.5 Figure 31.19

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