

RENESAS TECHNICAL UPDATE

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Product Category	MPU/MCU		Document No.	TN-RX*-A143A/E	Rev.	1.00
Title	Corrections to Descriptions for the Multi-Function Pin Controller (MPC) in the RX113 Group User's Manual		Information Category	Technical Notification		
Applicable Product	RX113 Group	Lot No.	Reference Document	RX113 Group User's Manual: Hardware Rev.1.02 (R01UH0448EJ0102)		
		All				

This document describes corrections to 19. Multi-Function Pin Controller (MPC) in the RX113 Group User's Manual

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Table 19.11 Register Settings for I/O Pin Functions in 100pin is corrected as follows.

Before correction

Table 19.11 Register Settings for I/O Pin Functions in 100pin.

PSEL[4:0] Settings	Pin							
	PA0	PA1	PA2	PA3	PA4	PA5	PA6	PA7
00000b (initial value)	Hi-Z							
00001b	MTIOC4A	MTIOC0B	-	MTIOC0D	MTIC5U	-	MTIC5V	-
00010b	-	MTCLKC	-	MTCLKD	MTCLKA	-	MTCLKB	-
00101b	-	-	-	MTIOC1B	MTIOC2B	-	MTIOC2A	-
00011b	-	-	-	-	TMR10	-	TMC13	-

omitted

After correction

Table 19.11 Register Settings for I/O Pin Functions in 100pin.

PSEL[4:0] Settings	Pin							
	PA0	PA1	PA2	PA3	PA4	PA5	PA6	PA7
00000b (initial value)	Hi-Z							
00001b	MTIOC4A	MTIOC0B	-	MTIOC0D	MTIC5U	-	MTIC5V	-
00010b	-	MTCLKC	-	MTCLKD	MTCLKA	-	MTCLKB	-
00011b	-	-	-	MTIOC1B	MTIOC2B	-	MTIOC2A	-
00101b	-	-	-	-	TMR10	-	TMC13	-

omitted

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Table 19.12 Register Settings for I/O Pin Functions in 64pin is corrected as follows.

Before correction

Table 19.12 Register Settings for I/O Pin Functions in 64pin.

PSEL[4:0] Settings	Pin				
	PA0	PA1	PA3	PA4	PA6
00000b (initial value)	Hi-Z				
00001b	MTIOC4A	MTIOC0B	MTIOC0D	MTIC5U	MTIC5V
00010b	-	MTCLKC	MTCLKD	MTCLKA	MTCLKB
00101b	-	-	MTIOC1B	MTIOC2B	MTIOC2A
00011b	-	-	-	TMR10	TMC13

omitted

After correction

Table 19.12 Register Settings for I/O Pin Functions in 64pin.

PSEL[4:0] Settings	Pin				
	PA0	PA1	PA3	PA4	PA6
00000b (initial value)	Hi-Z				
00001b	MTIOC4A	MTIOC0B	MTIOC0D	MTIC5U	MTIC5V
00010b	-	MTCLKC	MTCLKD	MTCLKA	MTCLKB
00011b	-	-	MTIOC1B	MTIOC2B	MTIOC2A
00101b	-	-	-	TMRI0	TMC13
<i>omitted</i>					