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RENESAS TECHNICAL UPDATE

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Product category	MPU/MCU	Document No.	TN-ÜÝ*-ŒJÎ A/E AWWWWWWWWWWÜÜev.A	
Title	Corrections to Descriptions for the Address Match Detection of the I ² C Bus Interface (RIIC) in the RX600 series and RX200 series		Information category	Technical information
		Lot No.		RX610 Group User's Manual: Hardware Rev.1.20
Applicable products	RX610 Group, RX62N Group, RX621 Group, RX62G Group, RX62T Group, RX630 Group, RX63N Group, RX631 Group, RX63T Group, RX210 Group, RX21A Group, RX220 Group	All	Reference document	(R01UH0032JJ0120) RX62N Group/RX621 Group User's Manual: Hardware Rev.1.30 (R01UH0033JJ0130) RX62T Group/RX62G Group User's Manual: Hardware Rev.2.00 (R01UH0034JJ0200) RX630 Group User's Manual: Hardware Rev.1.50 (R01UH0040JJ0150) RX63N Group/RX631 Group User's Manual: Hardware Rev.1.70 (R01UH0041JJ0170) RX63T Group User's Manual: Hardware Rev.2.10 (R01UH0238JJ0210) RX210 Group User's Manual: Hardware Rev.1.50 (R01UH0037JJ0150) RX21A Group User's Manual: Hardware Rev.1.00 (R01UH0251JJ0100) RX220 Group User's Manual: Hardware Rev.1.10 (R01UH0292JJ0110)

This document describes corrections to descriptions for the address match detection of the "I²C bus interface (RIIC)" section in User's Manual: Hardware of the applicable products mentioned above. Changes are underlined in the list below.

Detection of the general call address (Not applicable to RX62N group and RX62T group)
 Fifth to sixth lines of the body in the "Detection of the General Call Address" section are changes as indicated below.

[False] When the RIIC detects the general call address, both the GCA flag in ICSR1 and the RDRF flag in ICSR2 are set to 1 on the falling edge of the ninth cycle of SCL clock.

[Correct] When the RIIC detects the general call address, both the GCA flag in ICSR1 and the RDRF flag in ICSR2 are set to 1 on the <u>rising edge</u> of the ninth cycle of SCL clock.



Device-ID Address detection Third to fourth lines of the body in the "Device-ID Address Detection" section are changes as indicated below.				
[False] sets the DID flag in ICSR1 to 1 on the rising edge of the <u>ninth</u> SCL clock cycle when the following R/W# bit is 0, and then compares the second and subsequent bytes with its own slave address. [Correct] sets the DID flag in ICSR1 to 1 on the rising edge of the <u>eighth SCL</u> clock cycle when the following R/W# bit is 0, and then compares the second and subsequent bytes with its own slave address.				
 Host Address detection (Not applicable to RX62N group and RX62T group) Fourth to fifth lines of the body in the "Host Address Detection" section are changes as indicated below. 				
[False] When the RIIC detects the host address, the HOA flag in ICSR1 is set to 1 at the <u>falling edge</u> of the ninth SCL clock cycle, and at the same time, the TDRE flag in ICSR2.RDRF is set to 1 when the R/W# bit is 0 (Wr bit).				
[Correct] When the RIIC detects the host address, the HOA flag in ICSR1 is set to 1 at the <u>rising edge</u> of the ninth SCL clock cycle, and at the same time, the TDRE flag in ICSR2.RDRF is set to 1 when the R/W# bit is 0 (Wr bit).				