To our customers,

Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: http://www.renesas.com

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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RENESAS TECHNICAL UPDATE

Classification of Production	MPU			No	TN-SH7-478A/E	Rev	1
THEME	Correctional specs and additional specs concerning SH7750 electric characteristic tSTD		Classification of Information	1. Spec change ② Supplement of Documents 3. Limitation of Use 4. Change of Mask 5. Change of Production Line			
PRODUCT NAME	SH7750/ SH7750S / SH7750R	Lot No. All	Reference Documents	SH7750 Hardware Manual ADE-602-124E Rev. 6.0		Effective Date Eternity	

This is to notify you of the correction and the addition of the pin drive timing at following table 22.34 and 22.35 control signal timing and figure 22.14 standby mode for output delay time (tSTD) of electric characteristic STATUS pins of SH7750.

(1)The contents before manual correction

Table 22.34 and 22.35 Control Signal Timing

Item	Symbol	Min.	Max.	Unit	Figure
Bus tri-state delay time to standby mode	t_{BOFF2}	—	2	t_{cyc}	22.14
Bus buffer on time	t_{BON1}	—	12	ns	22.13
Bus buffer on time from standby	t_{BON2}	—	2	t_{cyc}	22.14
STATUS 0/1 delay time	t_{STD1}	—	6	ns	22.14
STATUS 0/1 delay time to standby	t_{STD2}	—	2	t_{cyc}	22.14

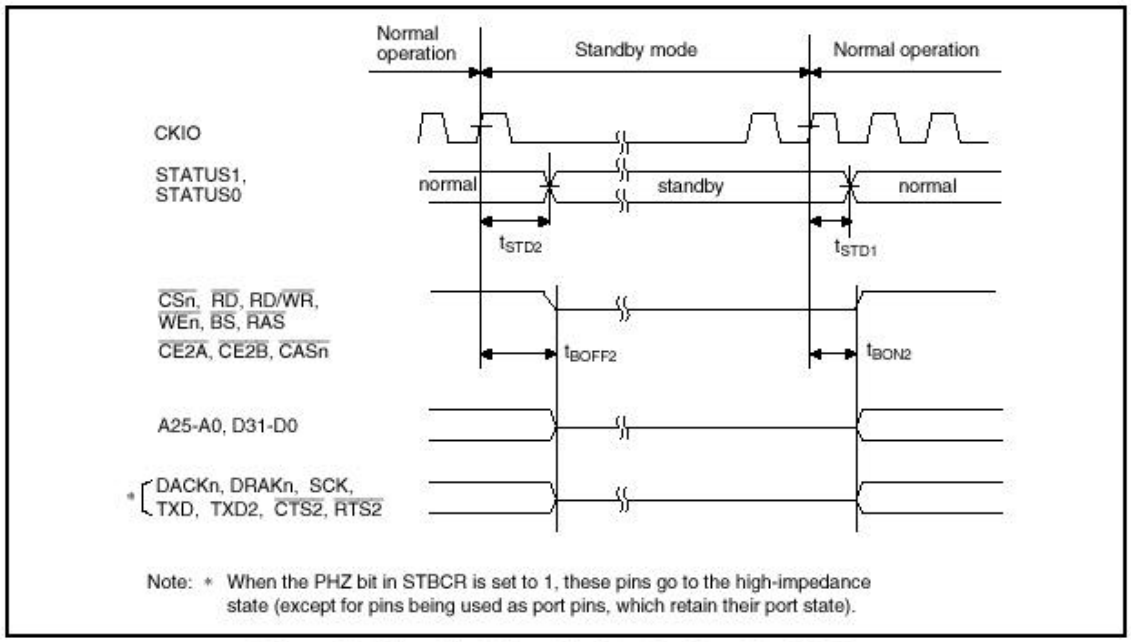

Figure 22.14 Pin Drive Timing for Standby Mode

(2)The contents after manual correction

Table 22.34 and 22.35 Control Signal Timing

Item	Symbol	Min.	Max.	Unit	Figure
Bus tri-state delay time to standby mode	t_{BOFF2}	—	2	t_{cyc}	22.14(2)
Bus buffer on time	t_{BON1}	—	12	ns	22.13
Bus buffer on time from standby	t_{BON2}	—	2	t_{cyc}	22.14(2)
STATUS 0/1 delay time	t_{STD1}	—	6	ns	22.14(1)
	t_{STD2}	—	2	t_{cyc}	22.14(1)(2)
	t_{STD3}	—	2	t_{cyc}	22.14(2)

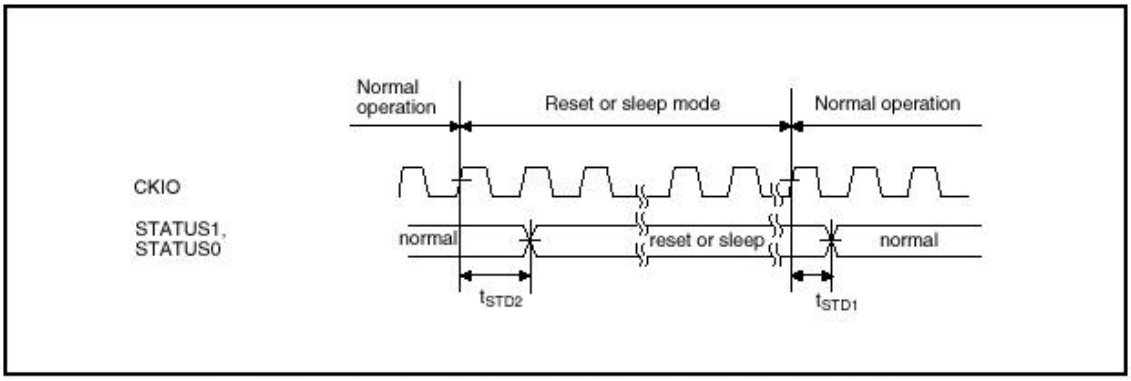

Figure 22.14(1) Pin Drive Timing for Reset or Sleep Mode

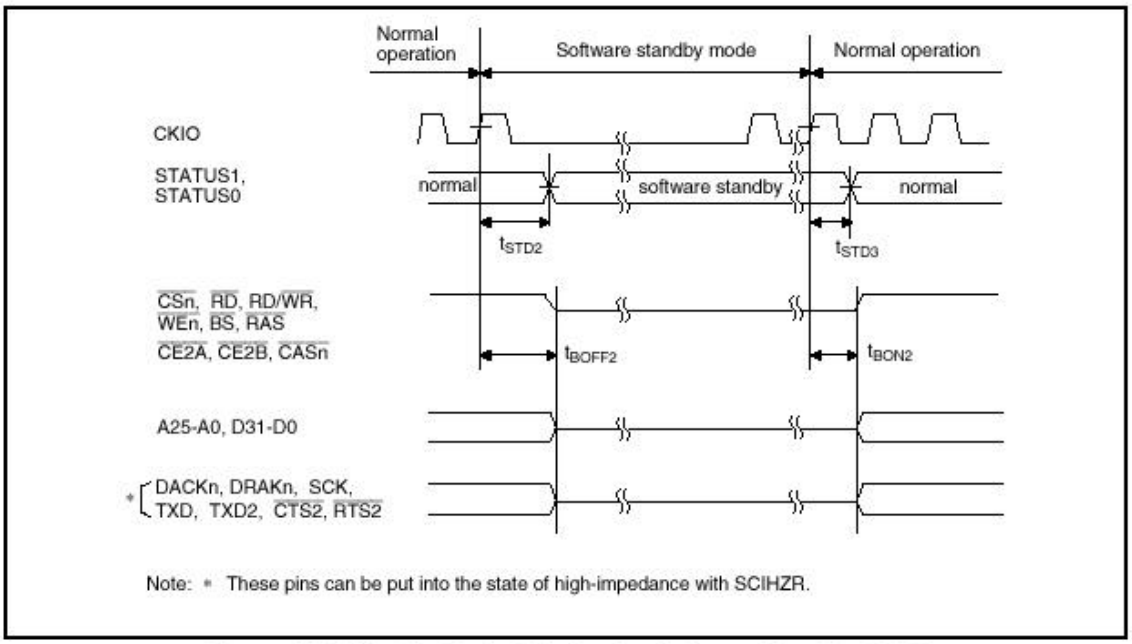

Figure 22.14(2) Pin Drive Timing for Software Standby Mode