To our customers,

Old Company Name in Catalogs and Other Documents

   On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: http://www.renesas.com

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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RENESAS TECHNICAL UPDATE

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Product Category	MPU&MCU			Document No.	TN-H8*-A307A/E	Rev.	1.00
Title	Correction of the H8/38086R Group and H8/38076R Group Hardware Manual			Information Category	Technical Notification		
Applicable Product	H8/38086R Group H8/38076R Group	Lot No.		Reference Document	H8/38086R Group Hardware Manual (REJ09B0182-0200 Rev.2.00) H8/38076R Group Hardware Manual (REJ09B0093-0300 Rev.3.00)		
		All					

We wish to notify you of the following corrections of the H8/38086R and H8/38076R Hardware Manual, as detailed below.
Shaded parts indicate the Revision parts.

Item and Page

1) H8/38086R Group

Section 10   Realtime Clock (RTC), 10.3.7   Clock Source Select Register (RTCCSR), Page 212

2) H8/38076R Group

Section 10   Realtime Clock (RTC), 10.3.7   Clock Source Select Register (RTCCSR), Page 210

Revisions

< Before change >

Bit	Bit Name	Initial Value	R/W	Description
3	RCS3	1	R/W	Clock Source Selection
2	RCS2	0	R/W	0000: $\phi/8$··············Free running counter operation
1	RCS1	0	R/W	0001: $\phi/32$··············Free running counter operation
0	RCS0	0	R/W	0010: $\phi/128$··············Free running counter operation
				0011: $\phi/256$··············Free running counter operation
				0100: $\phi/512$··············Free running counter operation
				0101: $\phi/2048$··············Free running counter operation
				0110: $\phi/4096$··············Free running counter operation
				0111: $\phi/8192$··············Free running counter operation
				1xxx: 32.768 kHz·······RTC operation

[Legend]   x: Don't care

< After change >

Bit	Bit Name	Initial Value	R/W	Description
3	RCS3	1	R/W	Clock Source Selection
2	RCS2	0	R/W	0000: $\phi/8$··············Free running counter operation
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0	RCS0	0	R/W	0010: $\phi/128$··············Free running counter operation
				0011: $\phi/256$··············Free running counter operation
				0100: $\phi/512$··············Free running counter operation
				0101: $\phi/2048$··············Free running counter operation
				0110: $\phi/4096$··············Free running counter operation
				0111: $\phi/8192$··············Free running counter operation
				1000: 32.768 kHz·······RTC operation
				1001 to 1111: Reserved (setting prohibited)

[Legend]   x: Don't care

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