

RENESAS TECHNICAL UPDATE

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Product Category	MPU/MCU		Document No.	TN-SY*-A016B/E	Rev.	2.00
Title	Correction to the description that was added for the Security MPU Program Counter Start Address Register (SECMPUPCSn)		Information Category	Technical Notification		
Applicable Product	Renesas Synergy™ S3 Series S3A7	Lot No.	Reference Document	S3A7 User's Manual: Rev.1.20		
		All lots				

The released Technical Update (document number "TN-SY*-A016A/E") contained incorrect information about the Security MPU Program Counter Start Address Register (SECMPUPCSn) in the MPU chapter of S3A7 MCU Group User's Manual.

1. 16.6.1.1 Security MPU Program Counter Start Address Register (SECMPUPCSn) (n = 0, 1)

[Before (TN-SY*-A016A/E)]

A time interval of greater than 12 bytes is required between the last instruction of the non-secure program and the first instruction of the secure program.

[After]

An address space of greater than 12 bytes is required between the last instruction of a non-secure program and the first instruction of a secure program.