Description for the Security MPU Program Counter Start Address Register (SECMPUPCSn) in the MPU chapter of S5D5, S3A3, and S3A6 MCU Group User’s Manuals is updated.

**S5D5 and S3A3**

1. Security MPU Program Counter Start Address Register (SECMPUPCSn) (n = 0, 1)
   
   [Before]
   
   A time interval of greater than 12 bytes is required between the last instruction of the non-secure program and the first instruction of the non-secure program.
   
   [After]
   
   An address space of greater than 12 bytes is required between the last instruction of a non-secure program and the first instruction of a secure program.

**S3A6**

1. Security MPU Program Counter Start Address Register (SECMPUPCSn) (n = 0, 1)
   
   [Before]
   
   The secure program must set a time interval of more than 12 bytes between the last instruction of a non-secure program and the first instruction of a secure program.
   
   [After]
   
   An address space of greater than 12 bytes is required between the last instruction of a non-secure program and the first instruction of a secure program.